SAM	ISUNG		Product li	nformation
			DATE : 18.N	lov.2009
N <u>The li</u>	AMSUNG TFT-L AODEL: L	.TI460A	LO5 ation is Preliminary and can be ch	hanged without
API	PROVED BY	DATE 18.Nov.2009	PREPARED BY Yu-Geun Lee	DATE
Nam			Tu-Geun Lee	18.Nov.2009
Nam	DID Deve	elopment 7	Feam, LCD Busines	

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Nov 18, 2009	000	all	First issued			
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General Description

Description

LTI460AL05 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a LED backlight unit. The resolution of a 46.0" is 1366 x 768 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and Digital Information Display (DID).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, High luminance
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle (±178°)
- High speed response
- Super narrow bezel, Black top chassis
- Landscape / Portrait type compatible
- WXGA (1366 x 768 pixels) resolution (16:9)
- Low power consumption
- Direct Type 2100 LEDs(light emitting diode)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (1pixel/clock)

	Items	Speci	Unit	Note		
Ma		1025.653(W _{TYP}		±1.0m	m	
IVIC	odule Size	58.53	58.53(D _{MAX})			
,	Weight	16,00	0(Max)	g		
Pi	ixel Pitch	0.7455(H)	x 0.7455(V)	mm		
Active	Display Area	1018.353(H)) x 572.544(V)	mm		
Surface Treatment		Haze 5.5% , H	Haze 5.5% , Hard-coating (3H)			
Disp	olay Colors	8 bit -	- 16.7M	colors		
Num	ber of Pixels	1366	6 x 768	pixel		
Pixel /	Arrangement	RGB ver	tical stripe			
Dis	play Mode	Norma	lly Black			
Lumina	ance of White	750 (Тур.)		cd/m ²		
				· ·		
IODEL	LTI460AL0	5 Doc. No	oc. No 05–000–G–0		Page	4 / 2

General Information

1. Absolute Maximum Ratings

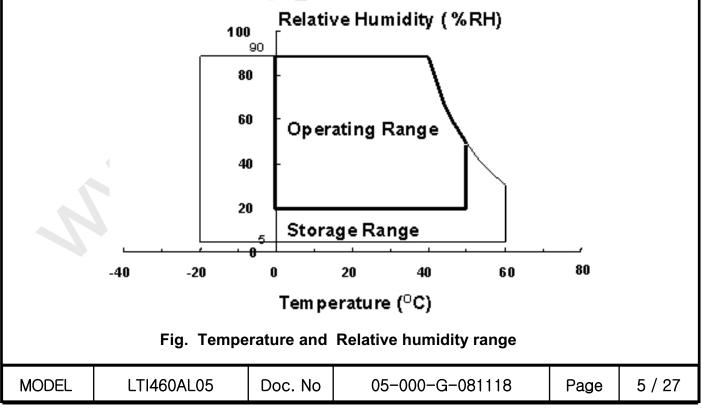
If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	ı	Sym	bol	Min.	Max.	Unit	Note
Power Suppl	Power Supply Voltage		D	GND-0.5	13.2	V	(1)
Storage terr	perature	T _s .	ΓG	-20	60	Ĉ	(2)
Glass surface	Center	T _{CENTER}		0	50	°C	
temperature (Operation)	T. Uniformity		Т	-	10	Ĵ	(2),(5)
Shook (non			x,y	-	TBD	G	(2)
SHOCK (HOH -	Shock(non - operating)		z	-	TBD	9	(3)
Vibration (non	Vibration (non - operating)		ор		TBD	G	(4)

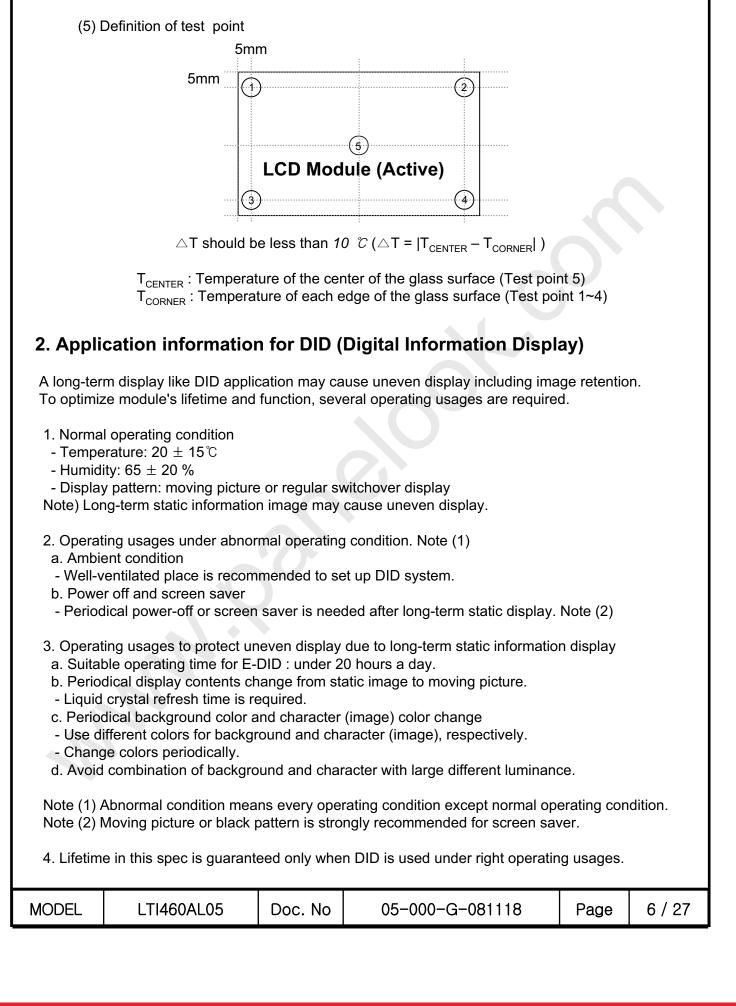
Note (1) Ta= 25 \pm 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

- a. 90 % RH Max. (Ta \leq 39 °C)
- b. Relative Humidity is 90% or less. (Ta > 39 °C)
- c. No condensation
- (3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis
- (4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis







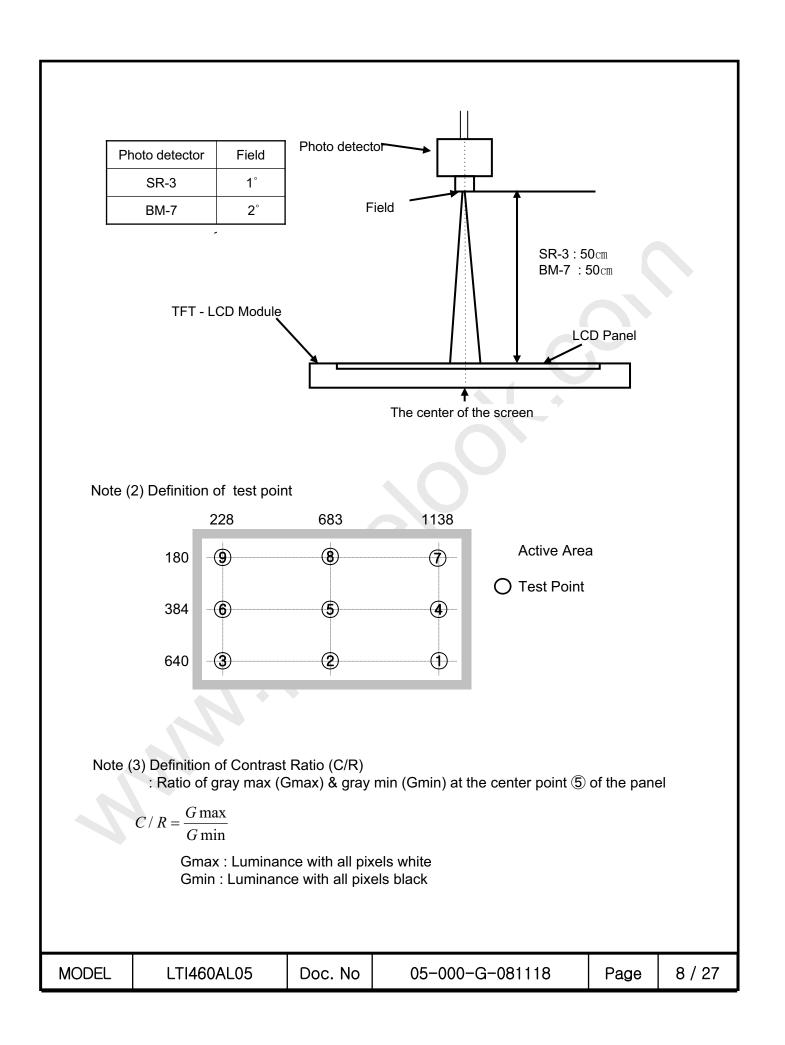
3. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent. Measuring equipment : TOPCON BM-7,SPECTRORADIOMETER SR-3

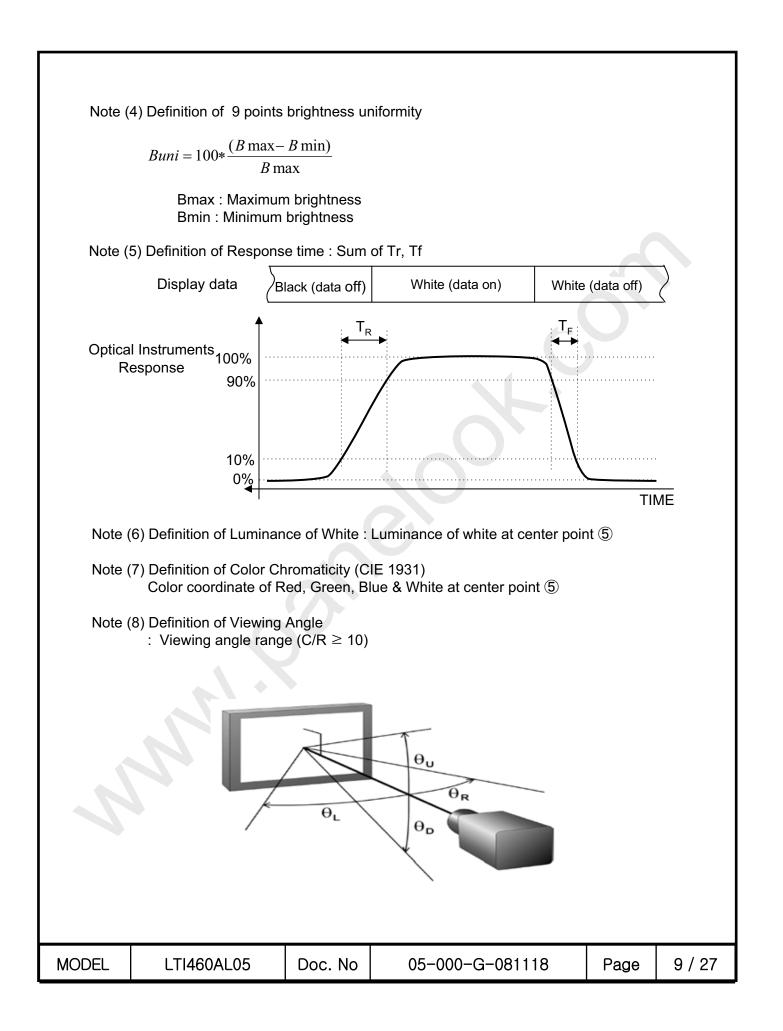
		(Ta	= $25 \pm 2^{\circ}$ C,	VDD = 12	V, fv = 60⊢	lz, f _{DCLK} = 8	30MHz, I _L	= 6.0mArms)	
It	em	Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
	ast Ratio of screen)	C/R		3500	4500	-		(3) SR-3	
	Rising	Tr		-	10	18			
Response Time	Falling	Tf		-	6	10	msec	(5) BM-7	
	G-to-G	Тg		-	8	-			
	ce of White of screen)	Y _L	Normal ⊕ L,R =0	750	850	- (cd/m ²	(6) SR-3	
	Ded	Rx	θ U,D =0		0.643				
	Red	Ry	Viewing		0.328	•			
	Orean	Gx	Angle			0.271			
Color	Green	Gy		TYP.	0.599	TYP.		(7),(8)	
Chromatici (CIE 1931	Ň l	Bx		-0.03	0.143	+0.03		SR-3	
	[/] Blue	Ву			0.060				
		Wx				0.280			
	White	Wy			0.290				
Color	Gamut	-		-	72	-	%	(7) SR-3	
Color Te	mperature	-		-	10000	-	К	(7) SR-3	
	Hor.	θ		75	89	-			
Viewing		θ_{R}	C/R≥10	75	89	-	Dograa	(8)	
Angle	Ver.	θυ	C/R=10	75	89	-	Degree	SR-3	
	ver.	θ		75	89	-			
•	s Uniformity oints)	B _{uni}		-	-	15	%	(4) SR-3	
Th 40 of Sir	Note (1) Test Equipment Setup The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen. Single lamp current : 380.0mA Environment condition : Ta = 25 ± 2 °C								
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One step solution for LCD / PDP / OLED panel application: Datasheet, inventory and accessory! www.panelook.com

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4. Electrical Characteristics

4.1 TFT LCD Module

The connector for display data & timing signal should be connected.

Ta = $25^{\circ}C \pm 2^{\circ}C$

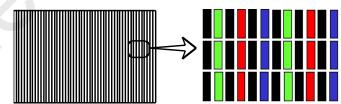
Item		Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of	Power Supply	V _{DD}	10.8	12.0	13.2	V	(1)
Current	(a) Black		-	450	-	mA	
of Power	(b) White	I _{DD}	-	480	-	mA	(2),(3)
Supply	Supply (c) N-Pattern		-	700	1000	mA	
Vsync Free	quency	f _v	-	60		Hz	
Hsync Fre	quency	f _H	43	50	53	kHz	
Main Frequency		f _{DCLK}	65	80	85	MHz	
Rush Curr	Rush Current		-	-	6	А	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

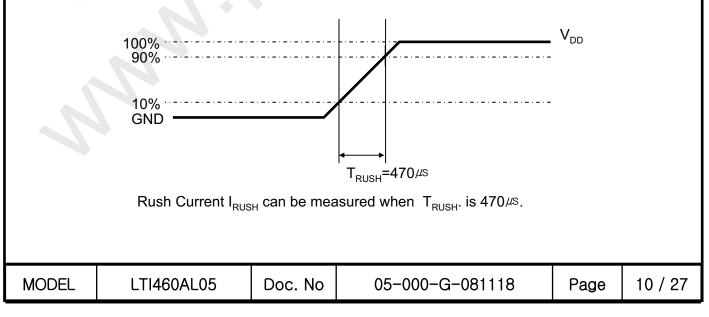
- (2) fv = 60Hz, fDCLK = 80MHz, $V_{DD} = 12.0V$, DC Current.
- (3) Power dissipation check pattern (LCD Module only) b) White Pattern
- a) Black Pattern

c) N-Pattern





(4) Measurement Conditions



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4.2 Back Light Unit

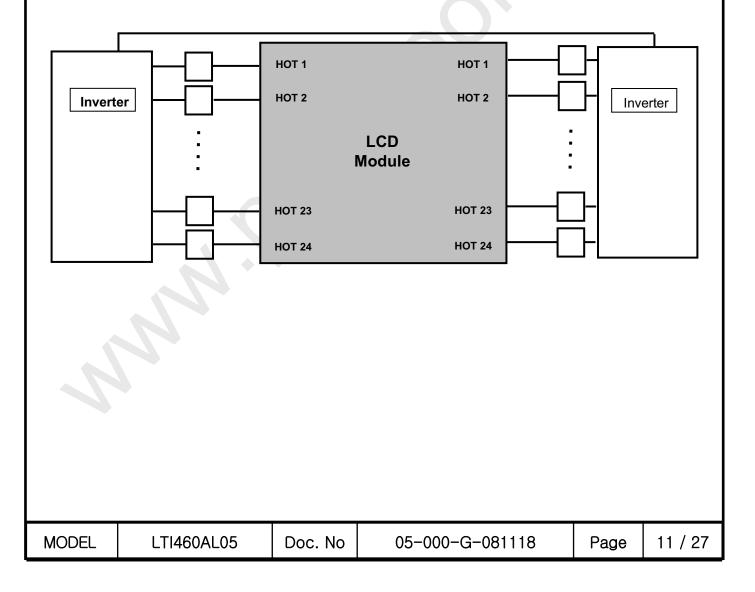
The back light unit contains 2100 direct-lighting type LEDs (light emitting diode). The characteristics of lamps are shown in the following tables.

Ta=25 \pm 2°C

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp Current	I _L	40	380	420	mArms	
Lamp Voltage	VL	-	75	-	Vrms	
Operating Life Time	Hr	50.000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = 25 ± 2 °C, I_L = 5.5 mArms, For single lamp only.]



4.3 Inverter Input Condition & Specification								
Items	Symbol	Conditions	Sp	Specifications			Note	
nems	Gymbol	Conditions	Min.	Тур.	Max.	Unit	Note	
Input Voltage	Vin	-	22	24	26	V	Ta=25 ±2 °C	
Input Current	lin	Vin = 24.0V Vdim = 3.3V	-	-	11	A	After 1 hour	
Lamp Current	I _{O,MAX}	Vdim = 3.3V	-	380	420	mArms	Warm-up	
Frequency	F _{LAMP}	Vin = 24.0V	-	-	-	kHz		
Backlight	ON	Vin = 24.0V	2.4	-	5.25	V		
On/Off	OFF	Vin = 24.0V	0	-	0.8	V		
Dimming	V	Max Lum	3.3	-	-	v		
Control	V _{DIM}	Min. Lum	-	-	0	v		

Note (1) Power Consumption is measured at 700[cd/m2] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.

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5. Input Terminal Pin Assignment

5.1 LVDS Connector

Connector : FI-E30S (JAE)

Pin No.	Description	Pin No.	Description	
1	No Connection (Note 1)	16	GND	
2	No Connection (Note 1)	17	RxIN3-	
3	No Connection (Note 1)	18	RxIN3+	
4	GND	19	GND	
5	RxIN0-	20	No Connection (Note 1)	
6	RxIN0+	21	LVDS Option (Note 2)	
7	GND	22	No Connection (Note 1)	
8	RxIN1-	23	GND	
9	RxIN1+	24	GND	
10	GND	25	GND	
11	RxIN2-	26	Vdd (12V)	
12	RxIN2+	27	Vdd (12V)	
13	GND	28	Vdd (12V)	
14	RxCLK-	29	Vdd (12V)	
15	RxCLK+	30	Vdd (12V)	

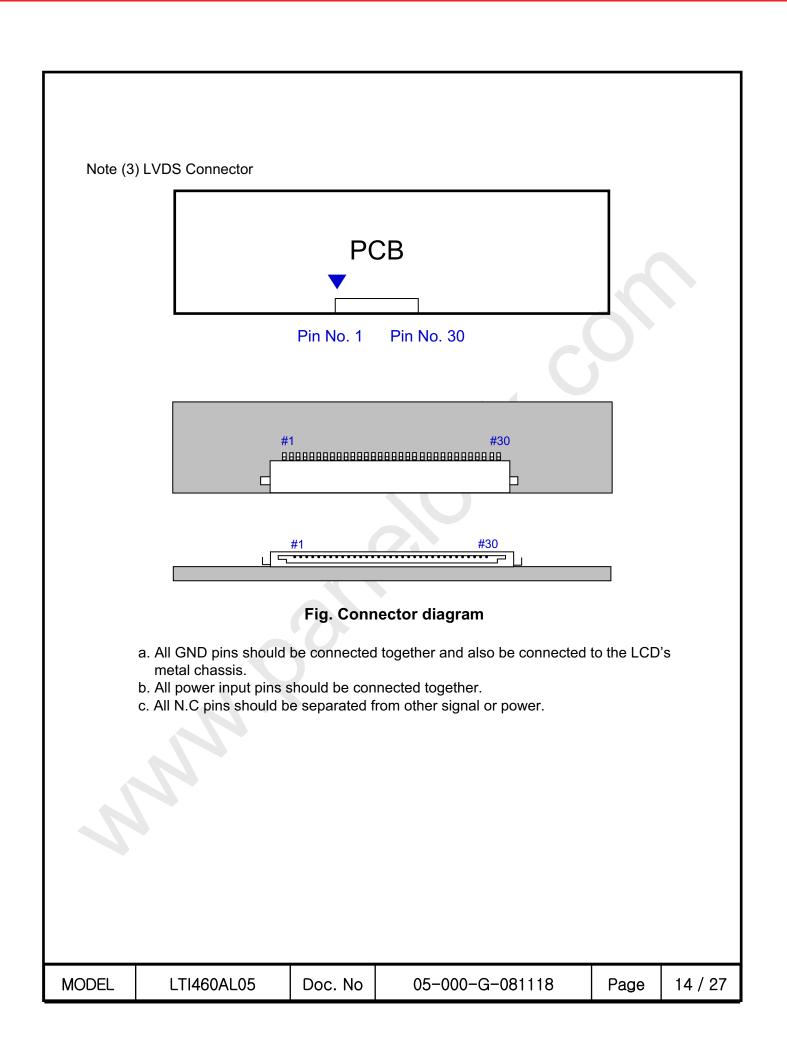
Note (1) No Connection : These pins are only for SAMSUNG internal purpose.

(2) LVDS Option : High (3.3 V) \rightarrow Normal LVDS format

: Low (GND) or Open (N.C) \rightarrow JEIDA LVDS format Sequence :On = VDD(T1) \geq LVDS Option \geq Interface Signal(T2) Off = Interface Signal(T3) \geq LVDS Option \geq VDD

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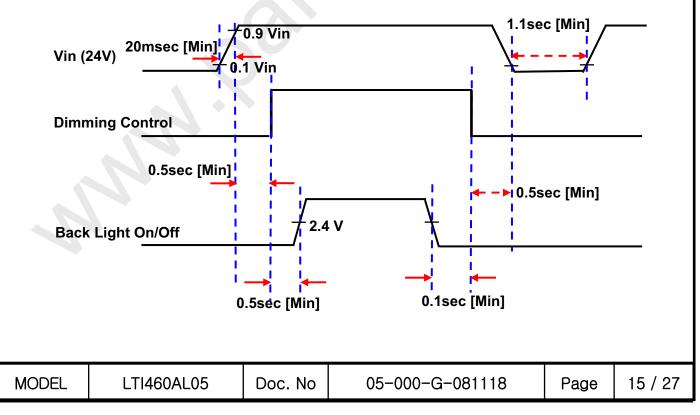


5.2 Inverter Input Pin Configuration

Connector : JST, S14B-PHA-SM3-TB

1 2	Vin (24V) Vin (24V)
2	Vin (24V)
_	
3	Vin (24V)
4	Vin (24V)
5	Vin (24V)
6	GND
7	GND
8	GND
9	GND
10	GND
11	No Connection
12	Backlight On /Off [On: 2.4 ~ 5.25 V, Off: 0 ~ 0.8 V]
13	Dimming Control [0V: Min, 3.3V: Max]
14	No Connection

5.3 Inverter Input Power Sequence



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- 5.4 LVDS Interface
 - LVDS Receiver : Tcon (merged)
 - Data Format (JEIDA & Normal)

- Data	a Format (JEIDA	& Normal)		Default LV	OS Option : J	EIDA	
		LVDS pin		JEIDA -DATA	VESA -D	ATA	
		TxIN/RxOU	ГО	R2	R0		
		TxIN/RxOU	۲1	R3	R1		
		TxIN/RxOU	[2	R4	R2		
Tx	OUT/RxIN0	TxIN/RxOU	F3	R5	R3		
		TxIN/RxOU	Γ4	R6	R4		
		TxIN/RxOU	Г6	R7	R5		
		TxIN/RxOU	[7	G2	G0		
		TxIN/RxOU	Г8	G3	G1		
		TxIN/RxOU	Г9	G4	G2		
		TxIN/RxOUT	12	G5	G3		
Tx	OUT/RxIN1	TxIN/RxOUT	13	G6	G4		
	TxOUT/RxIN1	TxIN/RxOUT	14	G7	G5		
		TxIN/RxOUT	15	B2	B0		
		TxIN/RxOUT	18	В3	B1		
		TxIN/RxOUT	19	B4	B2		
		TxIN/RxOUT	20	B5	B3		
		TxIN/RxOUT	21	B6	B4		
Tx	OUT/RxIN2	TxIN/RxOUT	22	B7	B5 HSYNC		
		TxIN/RxOUT	24	HSYNC			
		TxIN/RxOUT	25	VSYNC	VSYNC		
		TxIN/RxOUT	26	DEN	DEN		
	Ν	TxIN/RxOUT	27	R0	R6		
		TxIN/RxOU	ſ5	R1 R7			
		TxIN/RxOUT	10	G0	G6		
Tx	OUT/RxIN3	TxIN/RxOUT	11	G1	G7		
		TxIN/RxOUT	16	B0	B6		
		TxIN/RxOUT	17	B1	B7		
		TxIN/RxOUT	23	RESERVED	RESER	/ED	
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	DISPLAY									1		D	ATA S	SIGN	AL .											GRAY
COLOR	(8bit)	R0	R1	R2	RI R3	ED R4	R5	R6	R7	G0	G1	G2	GRE G3	EEN G4	G5	G6	G7	B0	B1	B2	BL B3	UE B4	B5	B6	B7	SCALE LEVEL
	BLACK	0	0	0	0	0	0	0	0	0	0	02	0	0	0	0	0	0	0	0	0	0	в5 0	0	0	_
ł	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
-	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	_
BASIC COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	_
-	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
ł	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	_
ł	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
ł		1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	DARK	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY SCALE	↑ DANK	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			R3~
OF		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			R3~ R252
RED	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
ŀ	DARK ↑ LIGHT	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~
OF		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G252
SILLIN		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
Ī	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	В0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
GRAY SCALE	Î	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~
OF BLUE	Ļ	:	:	:	:	:	:			:	:	••	:		:			:	:	:	:	:	:			B252
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

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6. Interface Timing

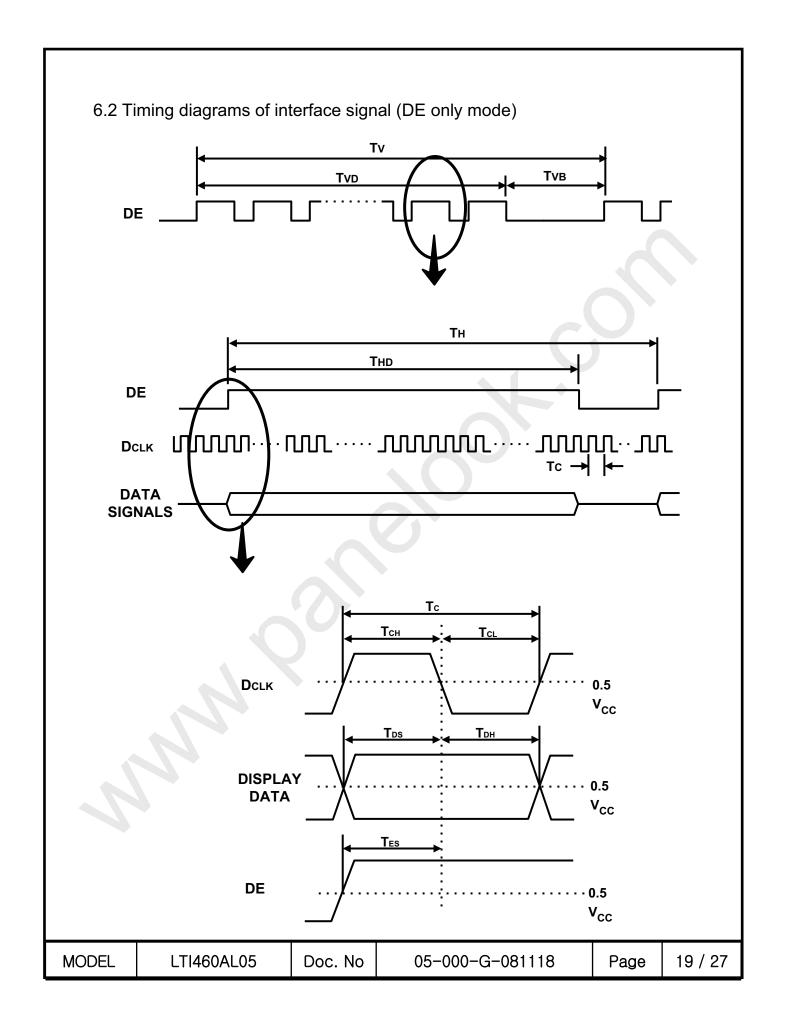
6.1 Timing Parameters (DE only mode)

Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
Clock		1/T _c	65	80	85	MHz	-
Hsync	Frequency	F _H	43	50	53	KHz	-
Vsync		F _v	-	60	-	Hz	-
Vertical	Active Display Period	T _{VD}	-	768	-	Lines	-
Display Term	Vertical Total	T _v	773	838	1500	Lines	-
Horizontal	Active Display Period	T _{HD}	-	1366	-	Clocks	-
Display Term	Horizontal Total	Т _н	1568	1600	2000	Clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

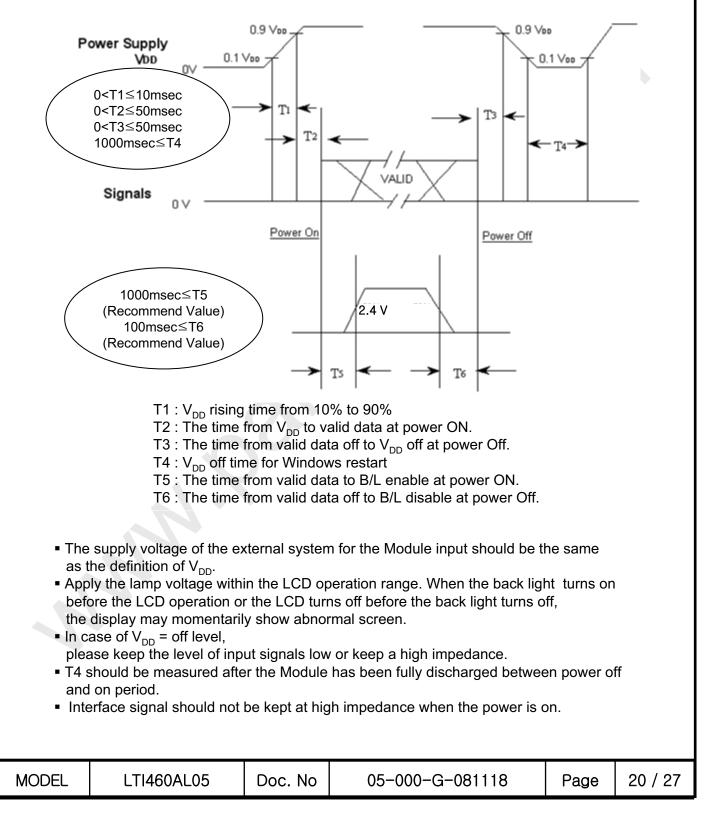
Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

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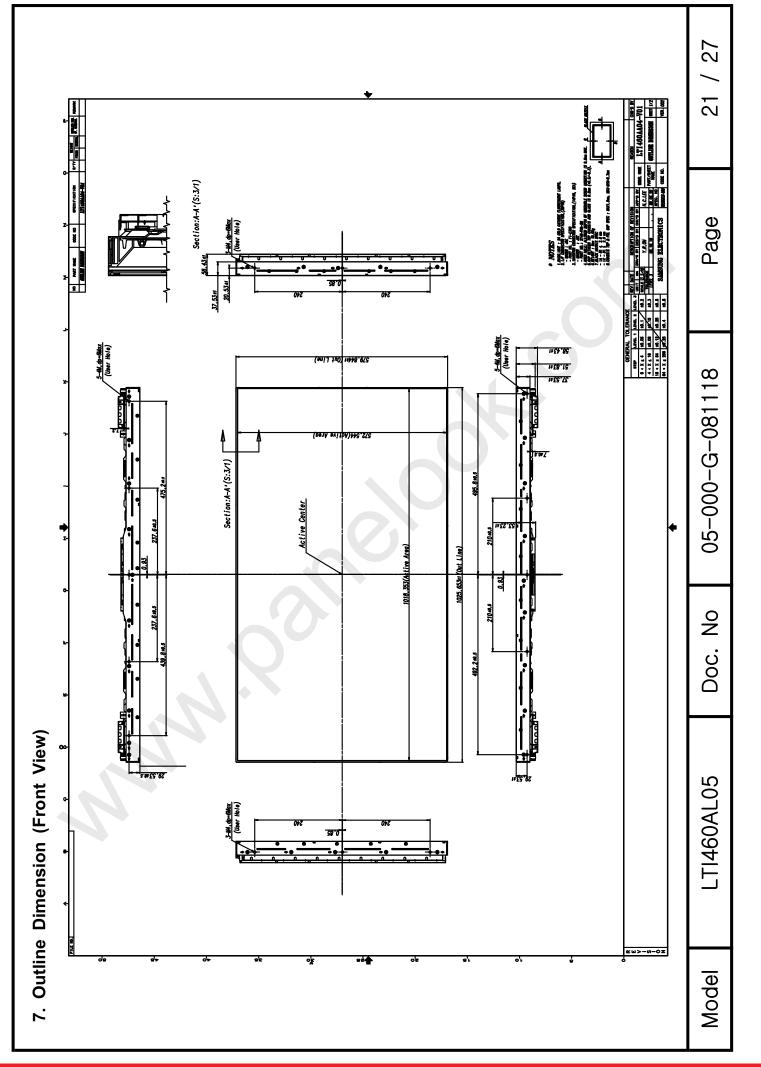


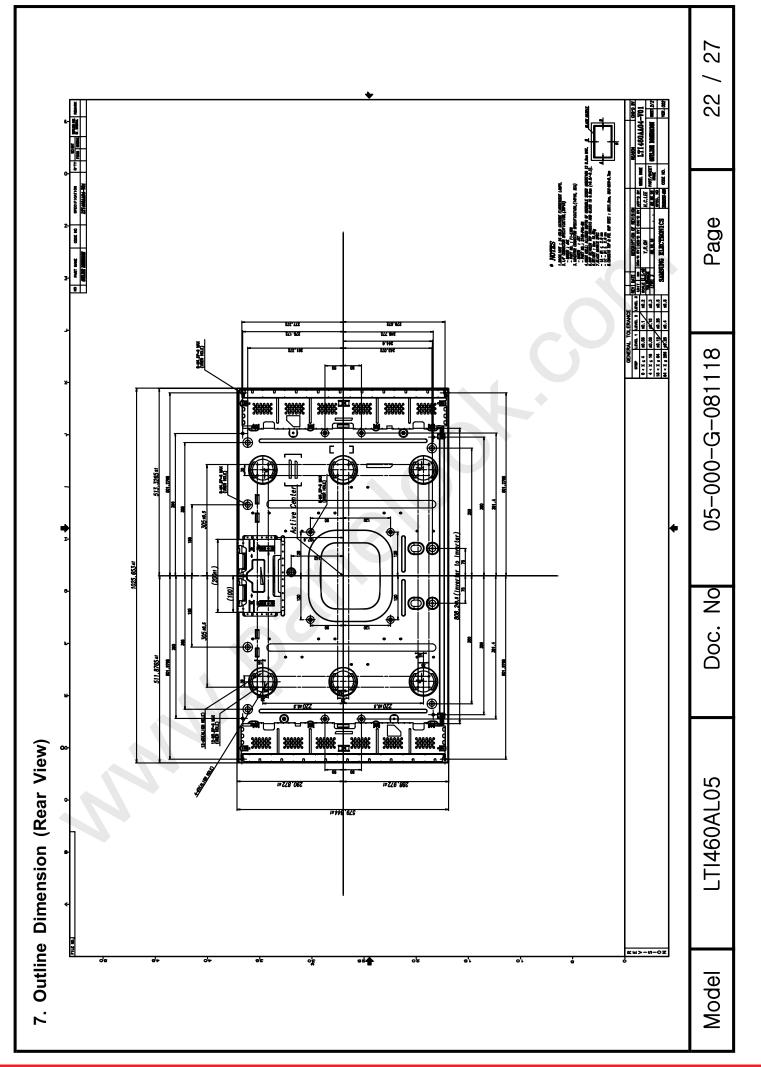


To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.

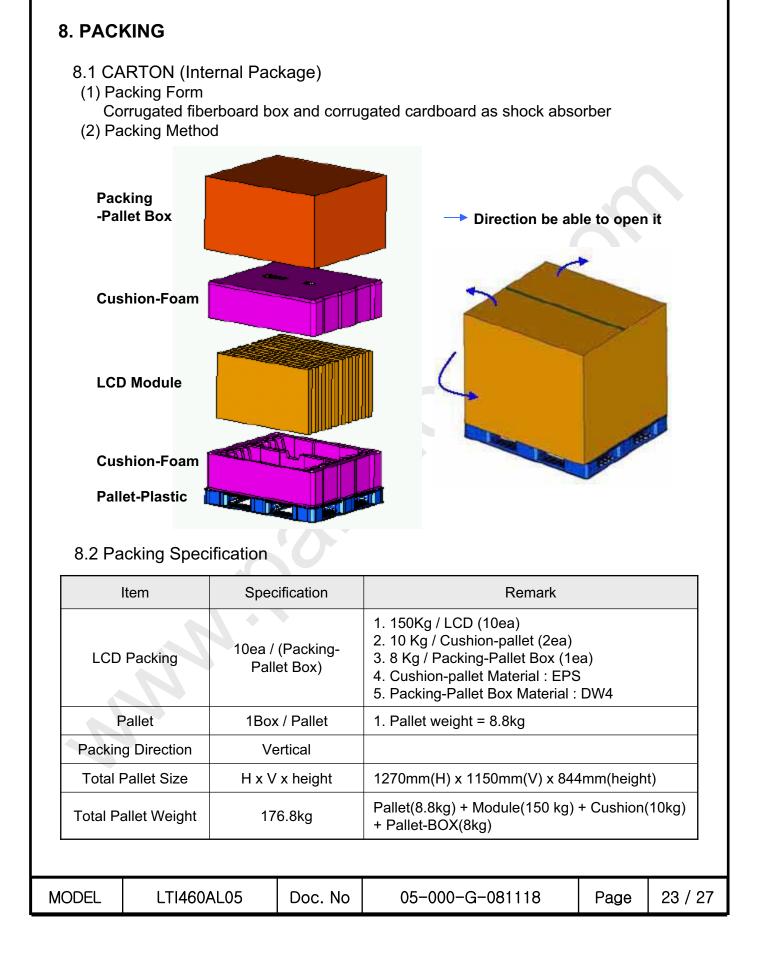


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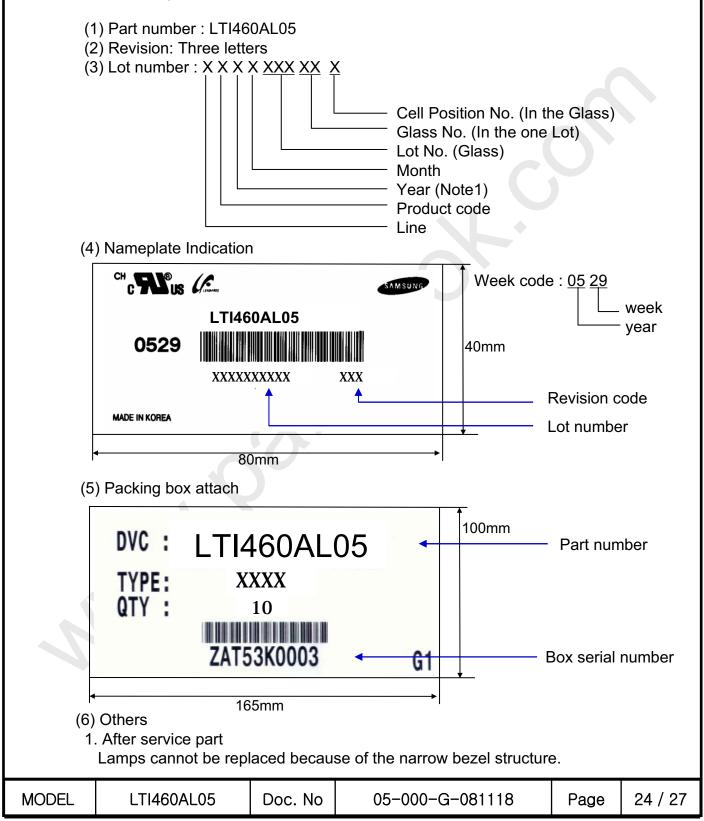


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9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.



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10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (I) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.

(o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35° C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers.
 Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp and may require higher startup voltage(Vs).
- 10.4 Operation Condition Guide
 - (a) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 20±15℃
 - Humidity : 55±20%
 - Display pattern : continually changing pattern (Not stationary)
 - (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Module should be turned clockwise (regular front view perspective) when used in portrait mode
- (c) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (d) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
 Otherwise the Madula may be demonded

Otherwise the Module may be damaged.

- (e) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen. To avoid image sticking, it is recommended to use a screen saver.
- (f) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (g) Please contact SEC in advance when you display the same pattern for a long time.

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