

SAMSUNG

ELECTRONICS

Product Information



Product Information

SAMSUNG TFT-LCD

MODEL NO. : LTN150XG-L08

LCD Product Planning Group 1, Marketing Team

Samsung Electronics Co . , LTD.



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GENERAL DESCRIPTION

DESCRIPTION

LTN150XG-L08 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight system. The resolution of a 15.0" contains 1,024 x 768 pixels and can display up to 262,144 colors. 6 O'clock direction is the Optimum viewing angle.

FEATURES

- Thin and light weight
- High contrast ratio
- XGA (1024x768 pixels) resolution
- Low power consumption
- DE (Data enable) only mode.
- 3.3V LVDS Interface
- On board EDID chip, SPWG-B style
- Pb-free product

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	304.128(H)X228.096(V) (15.0"diagonal)	mm	
Driver element	a-si TFT active matrix		
Display colors	262,144		
Number of pixel	1024 x 768 (XGA)	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.297(H) x 0.297(V)	mm	
Display Mode	Normally white(TN)		
Surface treatment	Haze 40, Hard-Coating 2H ,ARC150T		

MECHANICAL INFORMATION

ITEM		MIN.	TYP.	MAX.	NOTE
Module size (mm)	Horizontal (H)	316.8	317.3	317.8	
	Vertical (V)	241.4	242.0	242.6	
	Depth (D)	-	5.7	6.0	(1)
Weight(g)		-	510	530	

Note (1) Measurement condition of outline dimension

. Equipment : Vernier Calipers

. Push Force : 500g · f (minimum)

1. ELECTRICAL ABSOLUTE RATINGS**(1) TFT LCD MODULE**

$V_{DD} = 3.3V$, $V_{SS} = GND = 0V$

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	$V_{DD} - 0.3$	$V_{DD} + 0.3$	V	(1)
Logic Input Voltage	V_{IN}	$V_{DD} - 0.3$	$V_{DD} + 0.3$	V	(1)

Note (1) Within T_a ($25 \pm 2^\circ C$)

(2) BACK-LIGHT UNIT

$T_a = 25 \pm 2^\circ C$

Item	Symbol	Min.	Max.	Unit	Note
Lamp Current	I_L	2.0	7.0	mA _{rms}	(1)
Lamp frequency	F_L	40	80	kHz	(1)

Note 1) Permanent damage to the device may occur if maximum values are exceeded

Functional operation should be restricted to the conditions described under normal operating conditions.

2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state

Measuring equipment : TOPCON BM-5A

* Ta = 25±2°C , Vcc=3.3V, fv= 60Hz, fDCLK=65MHz, IL= 6.0mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit
Contrast Ratio (5 Points)		CR	Normal Viewing Angle $\phi = 0$ $\theta = 0$	-	300	-	-
Response Time at Ta (Rising + Falling)		T _R +T _T		-	10	20	msec
Average Luminance of White (5 Points)		Y _{L,AVE}		170	200	-	cd/m ²
Color Chromaticity (CIE)	Red	R _X		0.539	0.569	0.599	-
		R _Y		0.302	0.332	0.362	
	Green	G _X		0.282	0.312	0.342	
		G _Y		0.514	0.544	0.574	
	Blue	B _X		0.119	0.149	0.179	
		B _Y		0.102	0.132	0.162	
	White	W _X		0.285	0.313	0.341	
		W _Y		0.299	0.329	0.359	
Viewing Angle	Hor.	θ _L	CR ≥ 10	40	45	Degrees	
		θ _H		40	45		
	Ver.	φ _H		10	20		
		φ _L		40	45		
13 Points White Variation		δ _L		50	-	-	%

3. ELECTRICAL CHARACTERISTICS

Product Information

3.1 TFT LCD MODULE

Ta = 25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V _{DD}	3.0	3.3	3.6	V	
Differential Input Voltage for LVDS Receiver Threshold	High	V _{IH}	-	-	+100	mV	
	Low	V _{IL}	-100	-	-	mV	
Vsync Frequency		f _v	-	60	-	Hz	
Hsync Frequency		f _H	-	48.36	-	KHz	
Main Frequency		f _{DCLK}	-	65	-	MHz	
Rush Current		I _{RUSH}	-	-	1.5	A	
Current of Power Supply	White	I _{DD}	-	330	-	mA	
	Mosaic		-	360	-	mA	
	Max Pattern		-	480	510	mA	

3.2 BACK-LIGHT UNIT

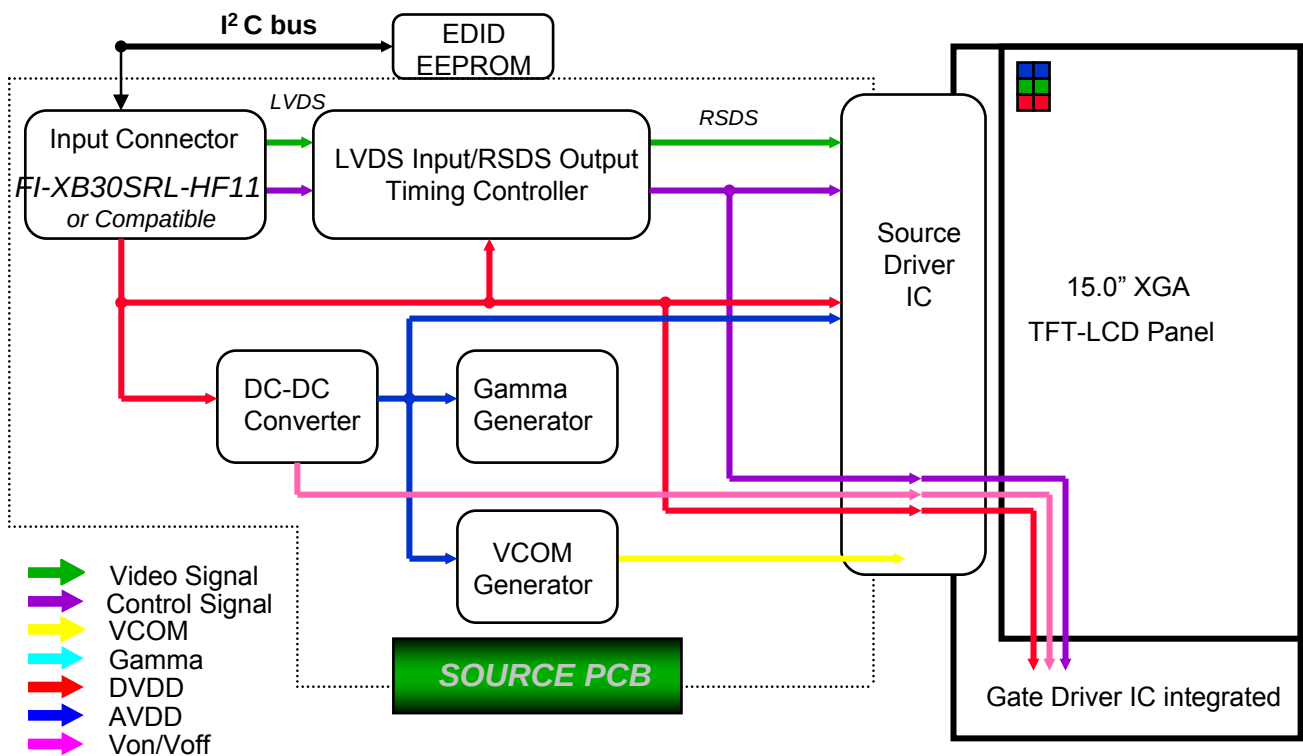
The backlight system is an edge - lighting type with a single CCFL (Cold Cathode Fluorescent Lamp).
The characteristics of a single lamp are shown in the following tables.

Ta=25 ± 2 °C

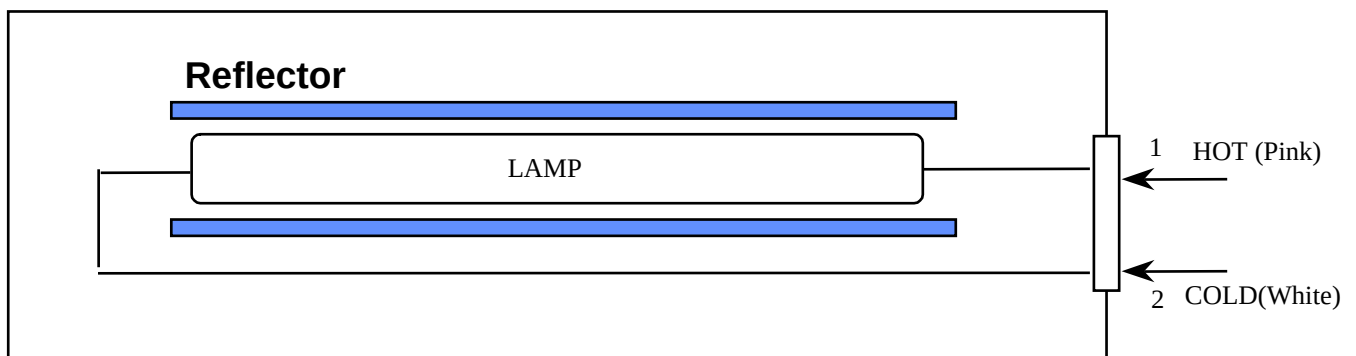
Item	Symbo l	Min.	Typ.	Max.	Unit	Note
Lamp Current	I _L	3.0	6.0	6.5	mArms	
Lamp Voltage	V _L		660	-	Vrms	I _L =6.0mA
Frequency	f _L	50	60	65	KHz	
Power Consumption	P _L		3.96		W	I _L =6.0mA
Operating Life Time	Hr	10,000			Hour	
Startup Voltage	V _s	-	-	1200	Vrms	25°C
				1500	Vrms	0°C
Lamp startup time		-	-	1.0	sec	

4. BLOCK DIAGRAM

4.1 TFT LCD Module



4.2 BACKLIGHT UNIT



Note) The output of the inverter may change according to the material of the reflector.

5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power LVDS, Connector : JAE, FI-XB30SRL-HF11 or Compatible
Mating Connector : JAE, FI-X30M or Compatible

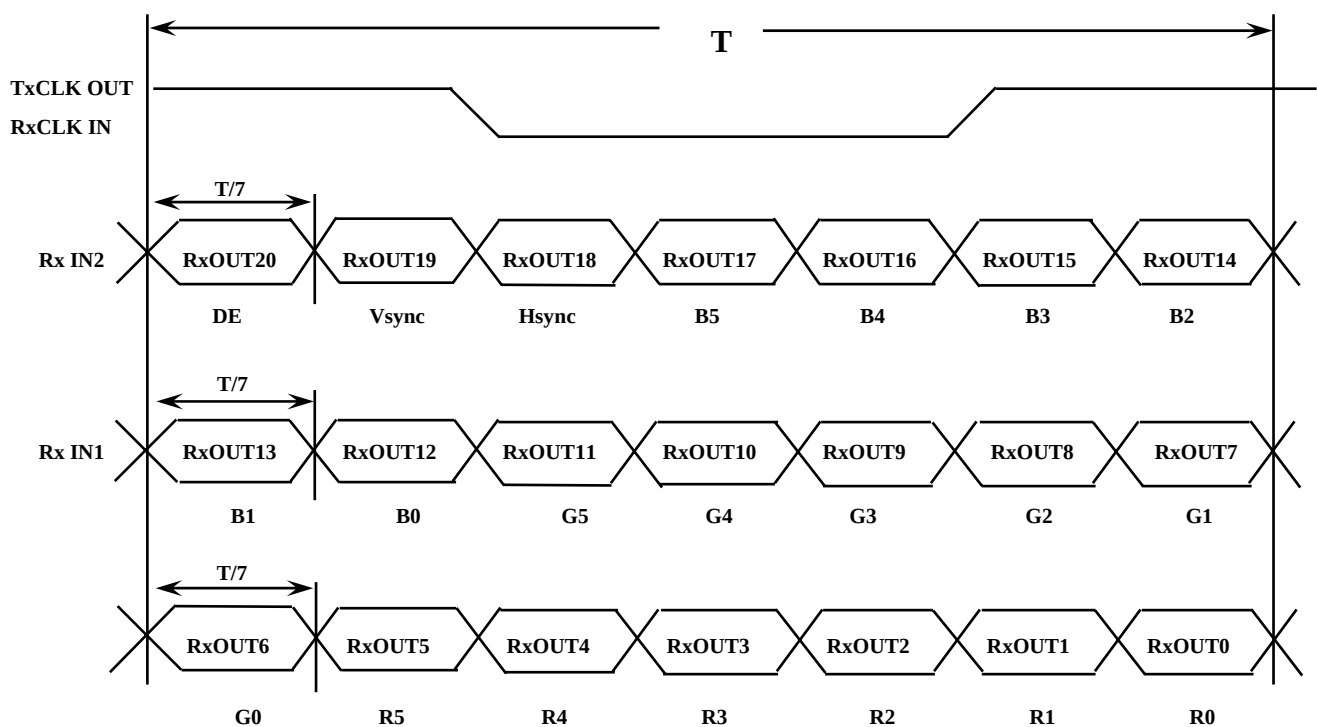
PIN NO	SYMBOL	FUNCTION	POLARITY	REMARK
1	Vss	Ground		
2	VDD	POWER SUPPLY +3.3V		
3	VDD	POWER SUPPLY +3.3V		
4	VEEDID	DDC 3.3V Power		
5	BIST	Panel BIST control		
6	CLKEDID	DDC Clock		
7	DATAEDID	DDC data		
8	O_RxIN0-	LVDS Differential Data INPUT (Odd R0-R5,G0)	Negative	
9	O_RxIN0+	LVDS Differential Data INPUT (Odd R0-R5,G0)	Positive	
10	GND	Ground		
11	O_RxIN1-	LVDS Differential Data INPUT (Odd G1-G5,B0-B1)	Negative	
12	O_RxIN1+	LVDS Differential Data INPUT (Odd G1-G5,B0-B1)	Positive	
13	GND	Ground		
14	O_RxIN2-	LVDS Differential Data INPUT (Odd B2-B5,Sync,DE)	Negative	
15	O_RxIN2+	LVDS Differential Data INPUT (Odd B2-B5,Sync,DE)	Positive	
16	GND	Ground		
17	O_RxCLK-	LVDS Differential Data INPUT (Odd Clock)	Negative	
18	O_RxCLK+	LVDS Differential Data INPUT (Odd Clock)	Positive	
19	GND	Ground		
20	NC	NC		
21	NC	NC		
22	NC	NC		
23	NC	NC		
24	NC	NC		
25	NC	NC		
26	NC	NC		
27	NC	NC		
28	NC	NC		
29	NC	NC		
30	NC	NC		

5.2 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1 or Compatible

Pin NO.	Symbol	Color	Function
1	HOT	Pink	High Voltage
2	COLD	White	Low Voltage

5.3 Timing Diagrams of LVDS For Transmission

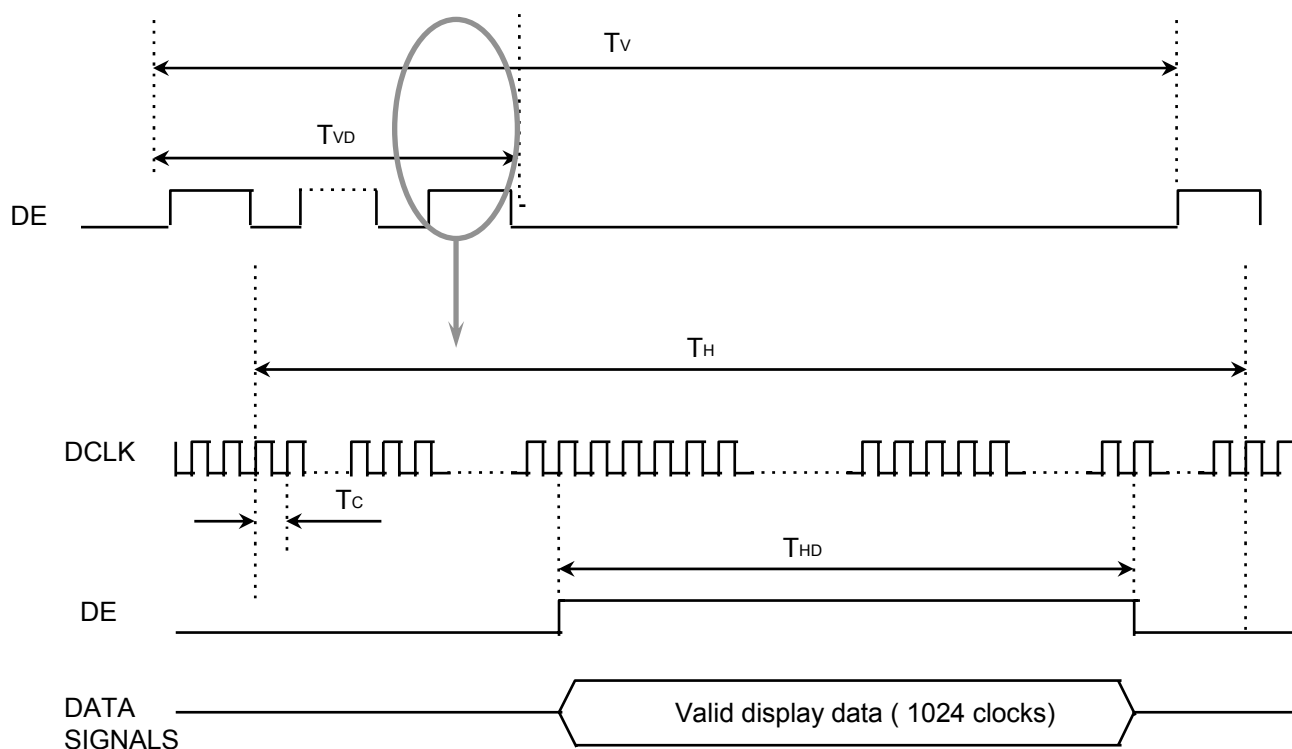


6. INTERFACE TIMING

6.1 Timing Parameters

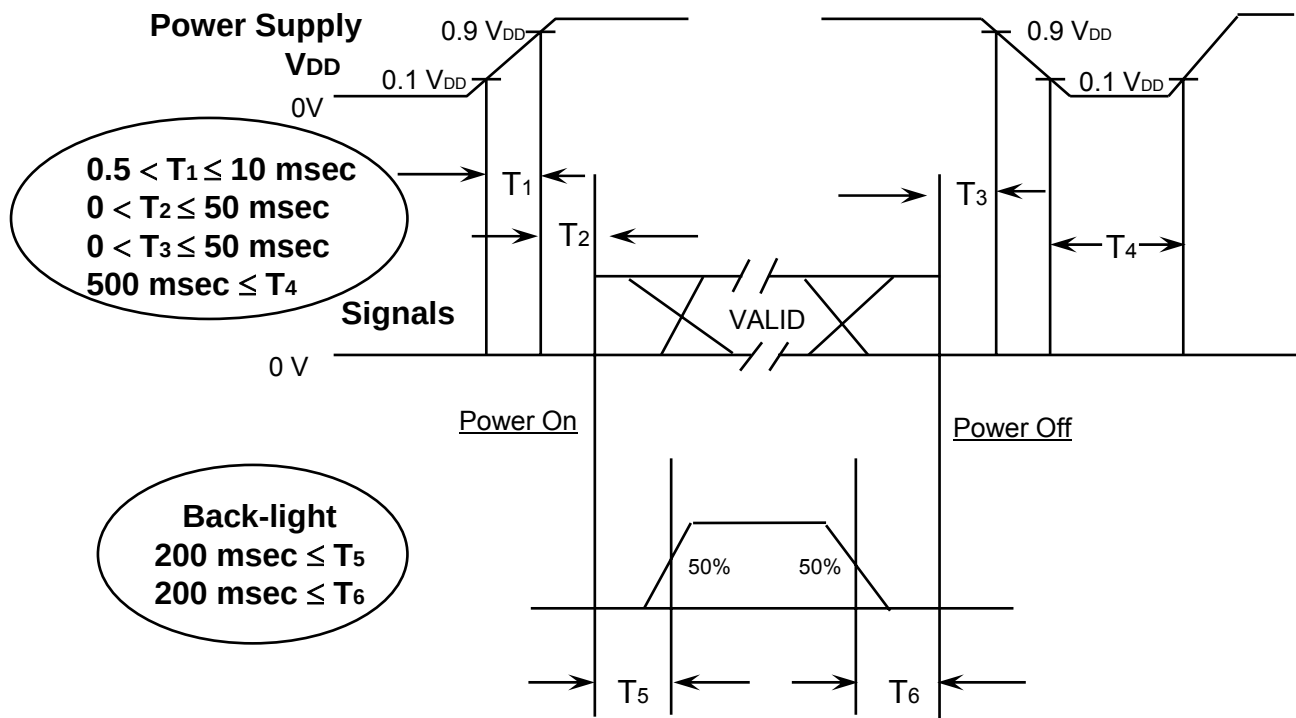
Signal	Item	Symbol	Min	Typ	Max	Unit	Note
Frame Frequency	Cycle	T_V	-	806	-	Line	
Vertical Active Display time	Display period	T_{VD}	-	768	-	Line	
One line Scanning time	Cycle	T_H	-	1344	-	Clock	
Horizontal Active Display time	Display period	T_{HD}	-	1024	-	Clock	

6.2 Timing diagrams of interface signal



6.3 Power ON/OFF Sequence

: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

- T1 : V_{DD} rising time from 10% to 90%
- T2 : The time from V_{DD} to valid data at power ON.
- T3 : The time from valid data off to V_{DD} off at power Off.
- T4 : V_{DD} off time for Windows restart
- T5 : The time from valid data to B/L enable at power ON.
- T6 : The time from valid data off to B/L disable at power Off.

NOTE.

- (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD} .
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of $V_{DD} = \text{off level}$, please keep the level of input signals on the low or keep a high impedance.
- (4) T4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

7. MECHANICAL OUTLINE DIMENSION

[Refer to the next page]

