SAMSUNG ELECTRONICS

Approval

LEAD-FREE

TO :

DATE : Dec. 01. 2009

SAMSUNG TFT-LCD

MODEL NO : LTN160AT03-0

NOTE : Extension code [-0]
→ LTN160AT03-**0**
Surface type [Anti-Glare]

Any modification is not allowed without SEC's permission

APPROVED BY :

PREPARED BY : *Mia So*

Application engineering part, DS Solution
Samsung Electronics Co., Ltd.

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CONTENTS

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REVISION HISTORY

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Date	Revision No.	Page	Summary
Dec. 01. 2009	A00	All	The approval specification of LT160AT03-0 was issued first.

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GENERAL DESCRIPTION

DESCRIPTION

LTN160AT03-0 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as switching devices. This model is composed of a TFT LCD panel, a driver circuit and a backlight system. The resolution of a 16.0" contains 1366 x 768 pixels and can display up to 262,144 colors. 6 O'clock direction is the Optimum viewing angle.

FEATURES

- Thin and light weight
- High contrast ratio, high aperture structure
- 1366 x 768 pixels resolution (16:9)
- Fast Response Time
- Low power consumption
- LED BLU Structure
- DE (Data enable) only mode
- 3.3V LVDS Interface
- On board EDID chip
- Pb-free product

APPLICATIONS

- Notebook PC
- If the usage of this product is not for PC application, but for others, please contact SEC

GENERAL INFORMATION

Item	Specification	Unit	Note
Display area	353.4525 (H) x 198.72(V) (16.0"diagonal)	mm	
Driver element	a-Si TFT active matrix		
Display colors	262,144		
Number of pixel	1366 x 768	pixel	16 : 9
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.25875 (H) x 0.25875 (V) (TYP.)	mm	
Display Mode	Normally white		
Surface treatment	Haze 0, Hardness 3H		

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Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal (H)	364.5	365	365.5	mm	
	Vertical (V)	213.5	214	214.5	mm	
	Depth (D)	-	-	6.0	mm	(1)
Weight		-	-	480	g	

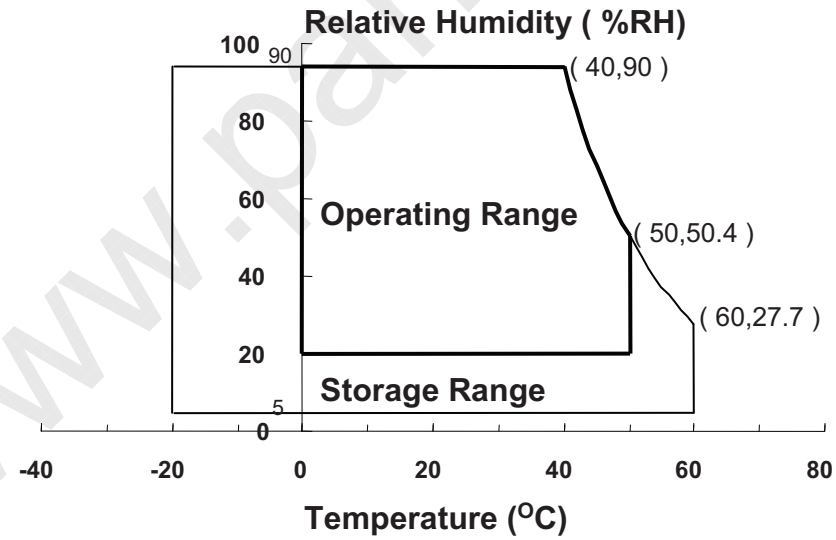
Note (1) Measurement condition of outline dimension
. Equipment : Vernier Calipers
. Push Force : 500g ·f (minimum)

1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Storage temperate	TSTG	-20	60	°C	(1)
Operating temperate (Temperature of glass surface)	TOPR	0	50	°C	(1)
Shock (non-operating)	Snop	-	240	G	(2),(4)
Vibration (non-operating)	Vnop	-	2.41	G	(3),(4)

Note (1) Temperature and relative humidity range are shown in the figure below.
95 % RH Max. (40 °C ≥ Ta)
Maximum wet - bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation



(2) 2ms, half sine wave, one time for ±X, ±Y, ± Z.
(3) 5 - 500 Hz, random vibration, 30min for X, Y, Z.
(4) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be hard and rigid enough so that the Module would not be twisted or bent by the fixture.

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1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

V_{DD} =3.3V, V_{SS} = GND = 0V

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	V_{DD} – 0.3	V_{DD} + 0.3	V	(1)
Logic Input Voltage	V_{DD}	V_{DD} – 0.3	V_{DD} + 0.3	V	(1)

Note (1) Within Ta (25 ± 2 °C)

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2. OPTICAL CHARACTERISTICS

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (5). Measuring equipment : TOPCON SR-3

* Ta = 25 ± 2 °C, V_{DD}=3.3V, fv= 60Hz, f_{DCLK} = 70.43 MHz, I_L = 19mA

Item		Symbol	Condition	Min.	Typ.	Max	Unit	Note
Contrast Ratio (5 Points)		CR	Normal Viewing Angle $\phi = 0$ $\theta = 0$	300	500	-	-	(1), (2), (5)
Response Time at Ta (Rising + Falling)		T_{RT}		-	16	25	msec	(1), (3)
Average Luminance of White (5 Points)		$Y_{L,AVE}$		250	300	-	cd/m^2	I_L=19mA (1), (4)
Color Chromaticity (CIE)	Red	R_X		0.610	0.640	0.670	-	
		R_Y		0.312	0.342	0.372		
	Green	G_X		0.302	0.332	0.362		
		G_Y		0.591	0.622	0.652		
	Blue	B_X		0.121	0.151	0.181		
		B_Y		0.022	0.052	0.082		
	White	W_X		0.283	0.313	0.343		
		W_Y		0.299	0.329	0.359		
Viewing Angle	Hor.	θ_L	CR ≥ 10	55	60	-	Degrees	(1), (5) SR-3
		θ_R		55	60	-		
	Ver.	ϕ_H		45	50	-		
		ϕ_L		45	50	-		
Viewing Angle	Hor.	θ_L	CR ≥ 100	30	-	-	Degrees	(1), (5) SR-3
		θ_R		30	-	-		
	Ver.	ϕ_H		10	-	-		
		ϕ_L		20	-	-		
Color Gamut				-	72	-	%	
13 Points White Variation		δ_L		-	-	1.7	-	(6)

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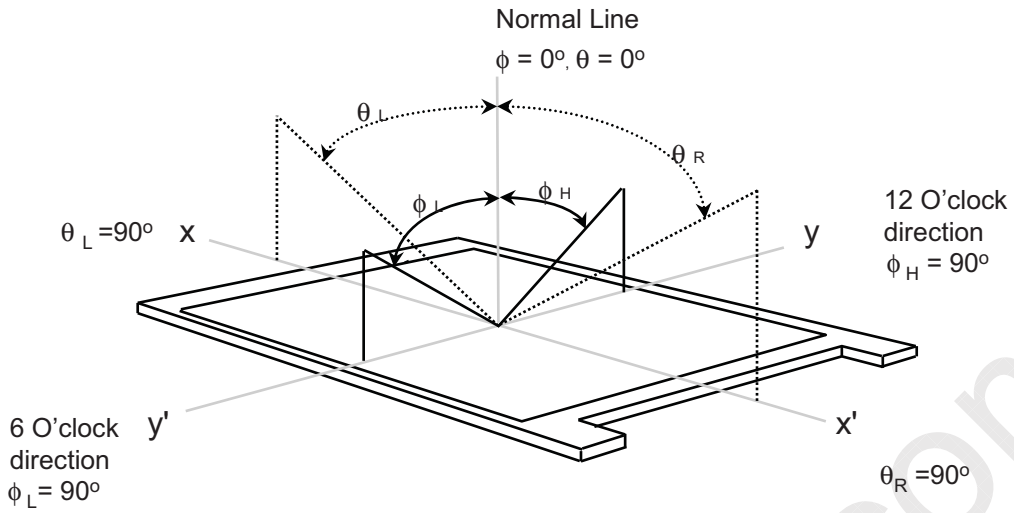
Note 1) Definition of Viewing Angle : Viewing angle range(10 ≤ C/R)

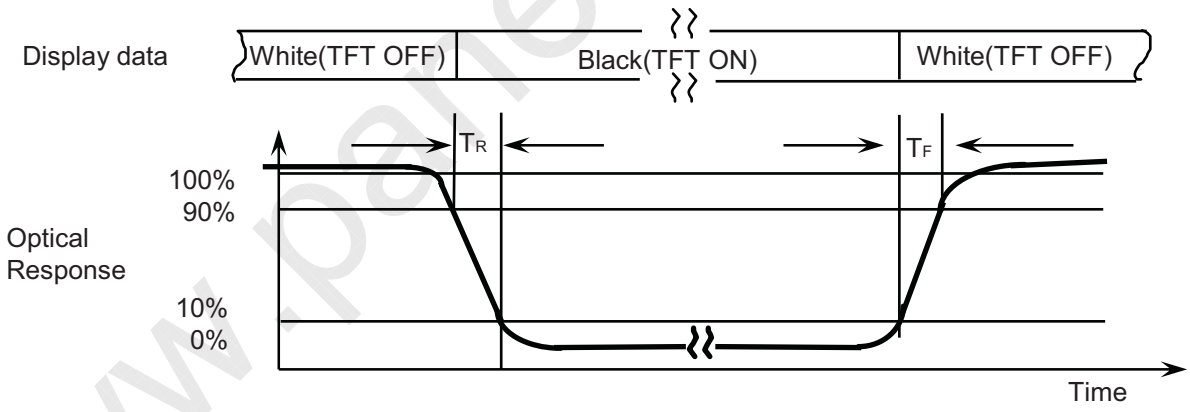


Note 2) Definition of Contrast Ratio (CR) : Ratio of gray max (Gmax) ,gray min (Gmin) at 5 points(4, 5, 7, 9, 10)

$$CR = \frac{CR(4) + CR(5) + CR(7) + CR(9) + CR(10)}{5}$$

Points : ④ , ⑤ , ⑦ , ⑨ , ⑩ at the figure of Note (6).

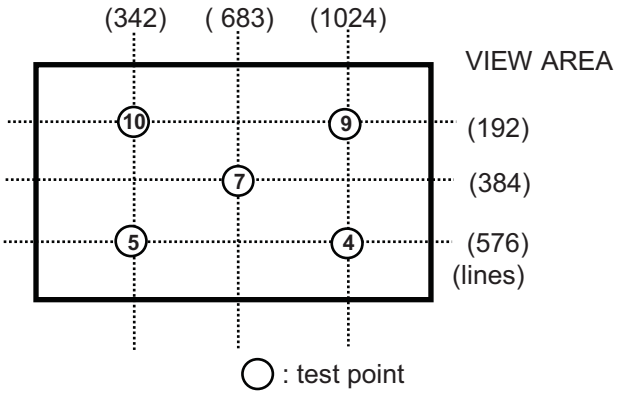
Note 3) Definition of Response time :



Note 4) Definition of Average Luminance of White : measure the luminance of white at 5 points.

Average Luminance of White ($Y_{L,AVE}$)

$$Y_{L,AVE} = \frac{Y_{L4} + Y_{L5} + Y_{L7} + Y_{L9} + Y_{L10}}{5}$$



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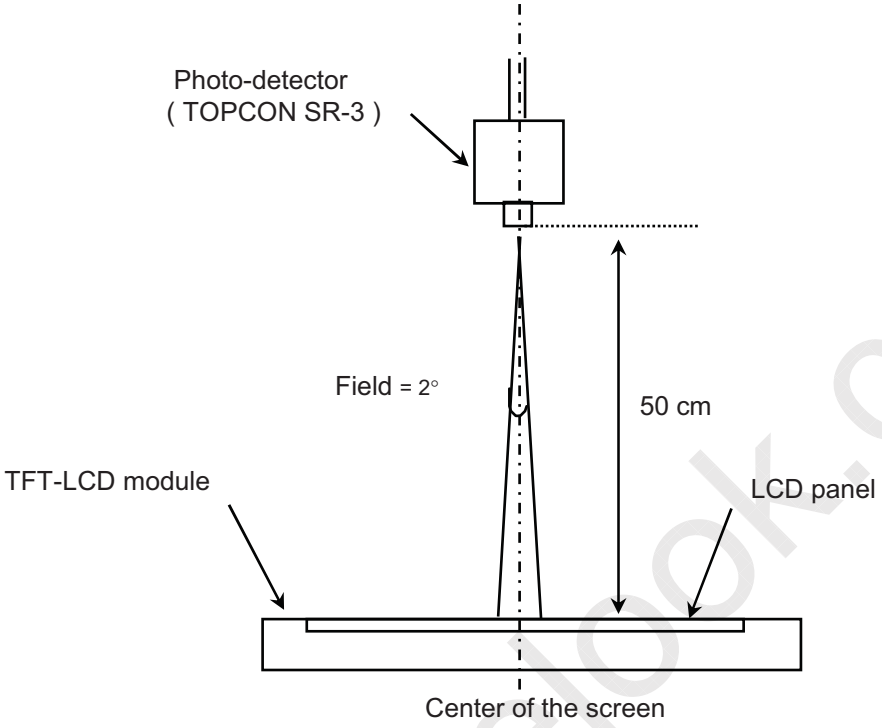
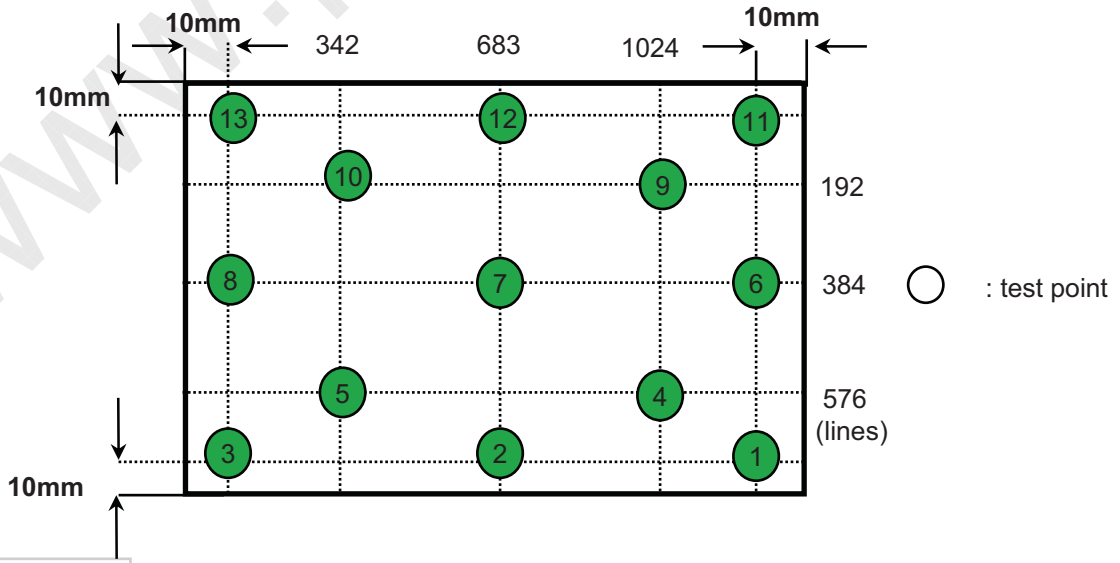
Note 5) After stabilizing and leaving the panel alone at a given temperature for 30 min , the measurement should be executed. Measurement should be executed in a stable, windless,and dark room.
30 min after lighting the backlight. This should be measured in the center of screen.
LED current : 19.0mA
Environment condition : Ta = 25 ± 2 °C



[Optical characteristics measurement setup]

Note 6) Definition of 13 points white variation (δ_L), [① ~ ⑬]

$$\delta_L = \frac{\text{Maximum luminance of 13 points}}{\text{Minimum luminance of 13 points}}$$



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3. ELECTRICAL CHARACTERISTICS

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3.1 TFT LCD MODULE

Ta= 25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	3.0	3.3	3.6	V	
Differential Input Voltage for LVDS Receiver Threshold	High	V_{IH}	-	-	+100	mV	V_{CM} = +1.2V
	Low	V_{IL}	-100	-	-	mV	
Vsync Frequency		fv	-	60	-	Hz	
Hsync Frequency		f_H	-	47.39	-	KHz	
Main Frequency		f_{DCLK}	-	72.33	-	MHz	
Rush Current		I_{RUSH}	-	-	1.5	A	(4)
Current of Power Supply	White	I_{DD}	-	450	-	mA	(2),(3)*a
	Mosaic		-	500	-	mA	(2),(3)*b
	V. Stripe		-	600	700	mA	(2),(3)*c

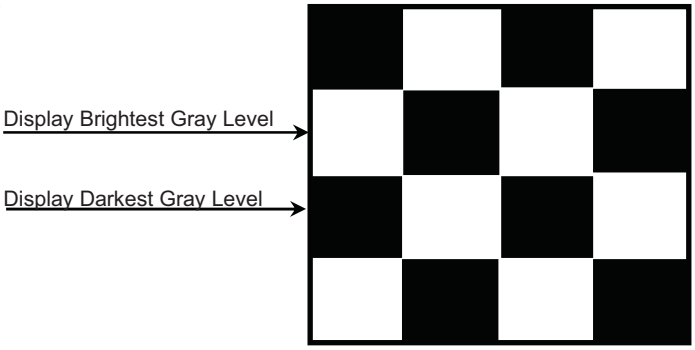
Note (1) Display data pins and timing signal pins should be connected.(GND = 0V)

(2) f_V = 60Hz, f_{DCLK} = 70.43 MHZ, V_{DD} = 3.3V , DC Current.

(3) Power dissipation pattern

*a) White Pattern

*b) Mosaic Pattern



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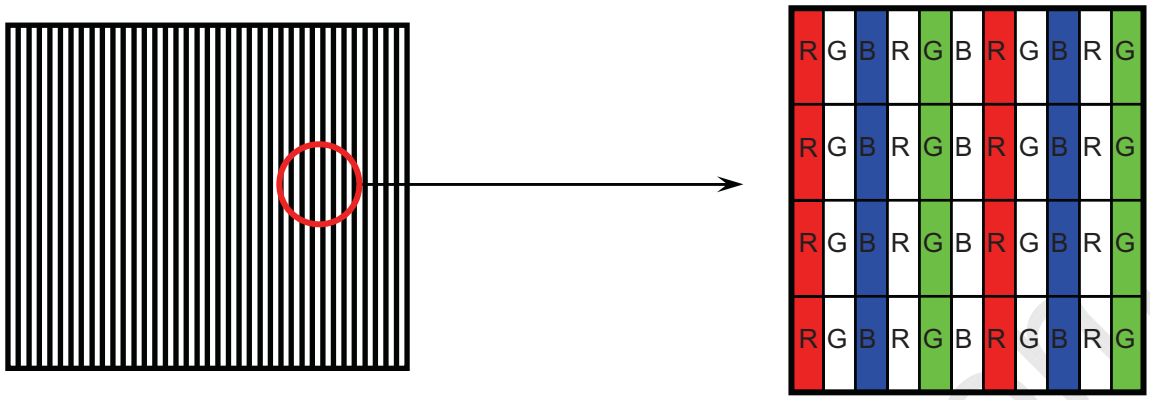
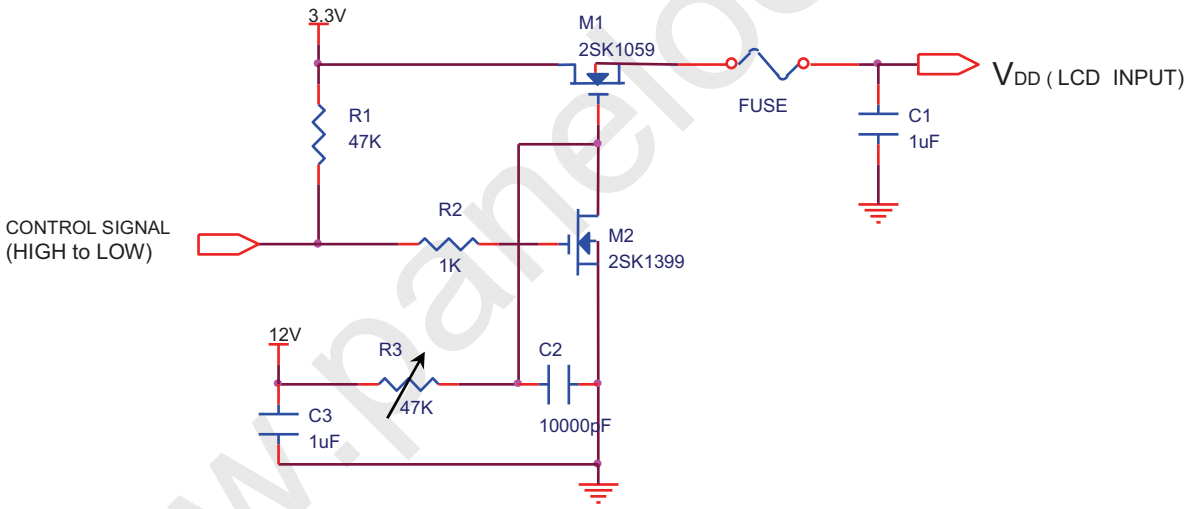
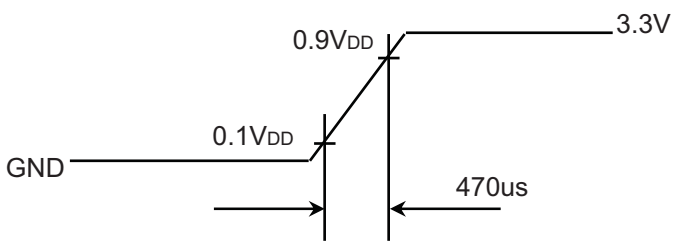
*c) 1dot Vertical stripe pattern



4) Rush current measurement condition



VDD rising time is 470us



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3.2 BACK-LIGHT UNIT

Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Forward Current	IF	-	20	-	mA	
LED Forward Voltage	VF	-	3.3	3.45	V	
LED Array Voltage	VP	-	28.8	31.05	V	VF X 9 LEDs
Power Consumption	P	-	4.8	6.0	W	IF X VF X 72 LEDs (w/o Converter)

3.3 LED Driver

- LED Driver Manufacturer : Max17061 (Maxim)

Ta= 25 ± 2 °C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Input Voltage	V_{in}	7.5	12	21	V	
Input Current	I	-	1.24	2	mA	
Operating Frequency	F_o	0.9	1.0	1.1	MHz	
PWM Input Frequency	F_{PWM}	5	10	50	kHz	
PWMI Duty Cycle	D	98	-	-	%	
Output Current (each LED string)	I_{out}	28.9	.	31.1	mA	Vin=8~21V, RISET = 133kohm
		19.3	.	20.7	mA	Vin=8~21V, RISET = 133kohm
		14.4	.	15.6	mA	Vin=8~21V, RISET = 133kohm

Note - Test Equipment : Fluke 45

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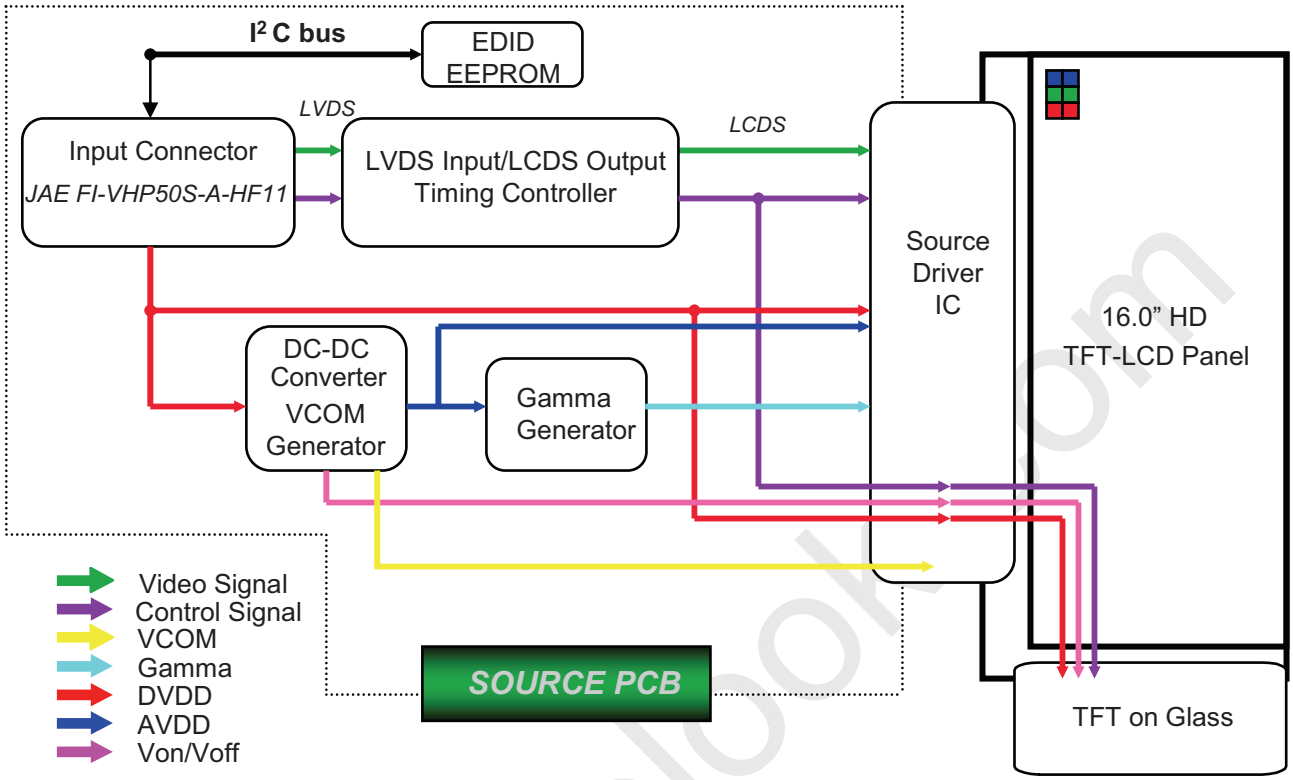
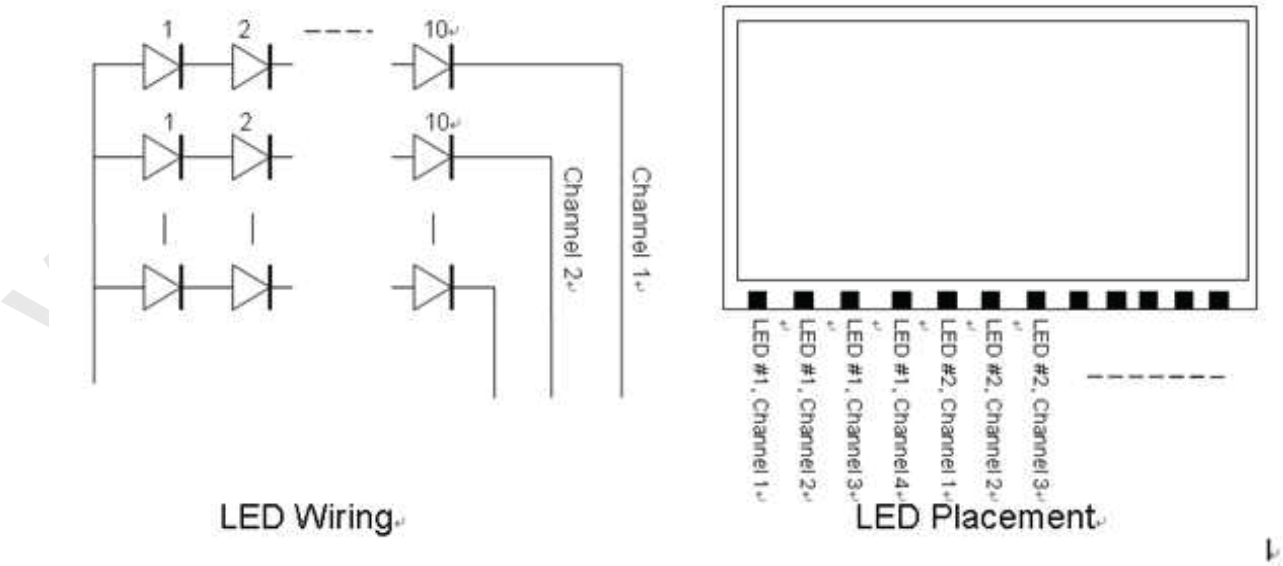
4. BLOCK DIAGRAM

4.1 TFT LCD Module



4.2 LED connection and placement



LED Wiring

LED Placement

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5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power (LVDS, Connector : JAE FI-VHP50S-A-HF11)

Pin	Signal	Description
1	Test Loop	Test Loop (only to pin 30)
2	VEEDID	EDID 3.3V power
3	VSS	Ground (Panel logic, BL logic)
4	N/C	No Connect
5	CLK EEDID	EDID clock
6	DATA EEDID	EDID data
7	N/C	No Connect
8	VSS	Ground (Panel logic, BL logic)
9	Odd_Rin0-	- LVDS differential data input (R0-R5, G0)
10	Odd_Rin0+	+ LVDS differential data input (R0-R5, G0)
11	VSS1	Ground – Shield LVDS Ch1
12	Odd_Rin1-	- LVDS differential data input (G1-G5, B0-B1) (odd pixels)
13	Odd_Rin1+	+ LVDS differential data input (G1-G5, B0-B1) (odd pixels)
14	VSS2	Ground – Shield LVDS Ch2
15	Odd_Rin2-	- LVDS differential data input (B2-B5, HS, VS, DE) (odd pixels)
16	Odd_Rin2+	+ LVDS differential data input (B2-B5, HS, VS, DE) (odd pixels)
17	VSS3	Ground – Shield LVDS Ch3
18	Odd_ClkIN-	- LVDS differential clock input (odd pixels)
19	Odd_ClkIN+	+ LVDS differential clock input (odd pixels)
20	Test Loop	Test Loop (only to pin 1)
21	N/C	No Connect
22	N/C	No Connect
23	N/C	No Connect
24	N/C	No Connect
25	N/C	No Connect

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5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Input Signal & Power (LVDS, Connector : JAE FI-VHP50S-A-HF11)

Pin	Signal	Description
26	N/C	No Connect
27	N/C	No Connect
28	N/C	No Connect
29	N/C	No Connect
30	N/C	No Connect
31	CONNTST	Connector test (this pin connected to pin 50 only)
32~33	VDD	Logic power 3.3V (Panel logic, BL logic)
34	TEST (BIST_EN)	Panel Self Test
35	+5V_ALW	
36~37	VSS	
38	PWM_BL	PWM brightness control
39~42	VBL-	LED power return
43	N/C	No Connect
44~47	VBL+	7V - 20V LED power
48	SMB_DATA	SMBus Data
49	SMB_CLK	SMBus Clk
50	CONNTST	Connector test (this pin to be connected to pin 31 only)

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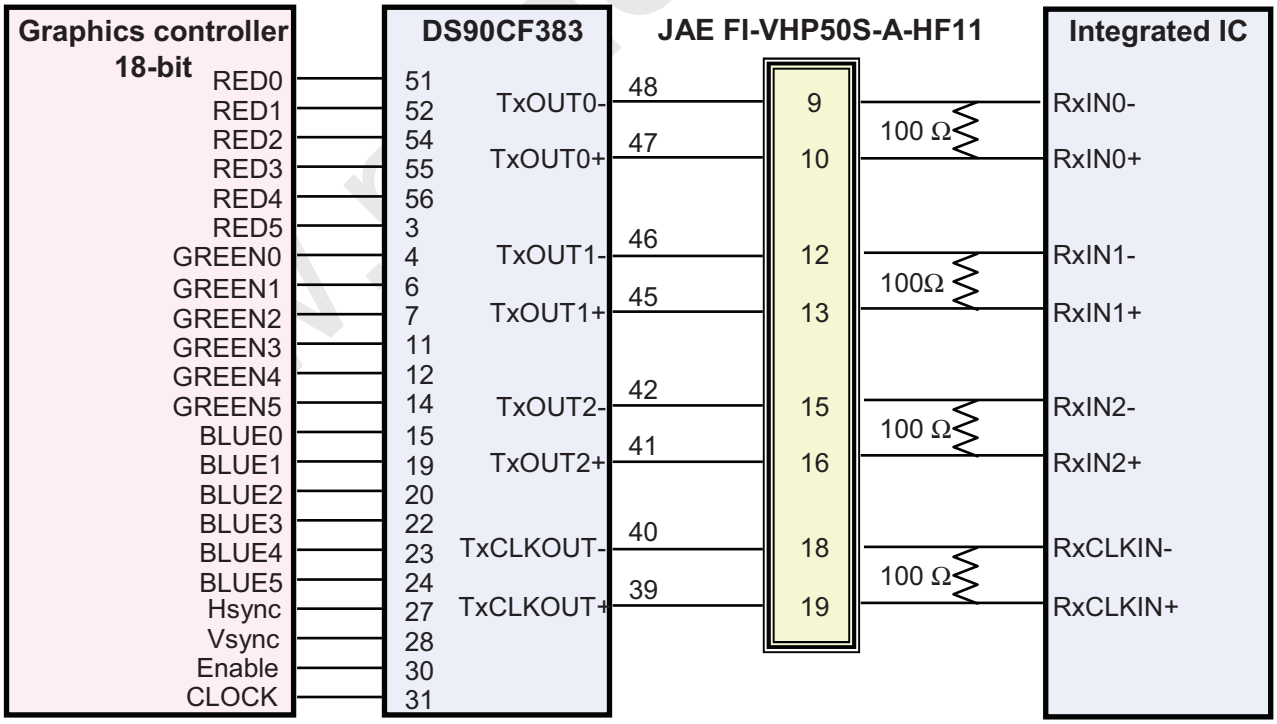
5.2 LVDS Interface : Transmitter DS90CF363 or Compatible

Pin No.	Name	RGB Signal	Pin No.	Name	RGB Signal
51	TxIN0	R0	14	TxIN14	G5
52	TxIN1	R1	15	TxIN15	B0
54	TxIN2	R2	19	TxIN18	B1
55	TxIN3	R3	20	TxIN19	B2
56	TxIN4	R4	22	TxIN20	B3
3	TxIN6	R5	23	TxIN21	B4
4	TxIN7	G0	24	TxIN22	B5
6	TxIN8	G1	27	TxIN24	Hsync
7	TxIN9	G2	28	TxIN25	Vsync
11	TxIN12	G3	30	TxIN26	DE
12	TxIN13	G4	31	TxCLKIN	Clock

LVDS INTERFACE



Note : The LCD Module uses a 100ohm resistor between positive and negative lines of each receiver input.

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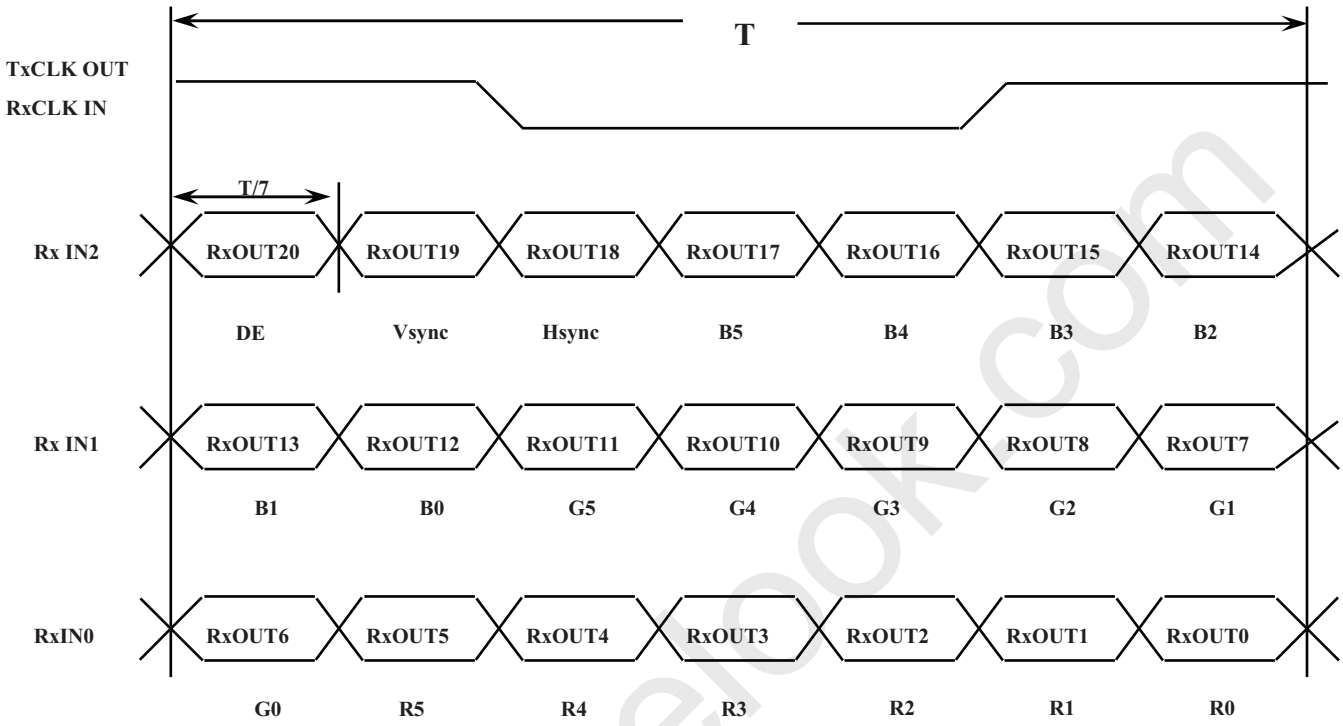
5.3 Timing Diagrams of LVDS For Transmission

LVDS Receiver : Integrated T-CON



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5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

Color	Display	Data Signal																		Gray Scale Level
		Red						Green						Blue						
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	45	B5	
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	-
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	-
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	-
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	-
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1	-
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	-
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
Gray Scale Of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	Dark	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
	↑	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R61
	Light	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R62
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	R63
Gray Scale Of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	Dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	G1
	↑	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	G2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	G61
	Light	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	G62
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	G63
Gray Scale Of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	Dark	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B1
	↑	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	B2
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B60
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	B61
	Light	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	B62
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B63

Note 1) Definition of gray :
Rn: Red gray, Gn: Green gray, Bn: Blue gray (n=gray level)
Note 2)Input signal: 0 =Low level voltage, 1=High level voltage

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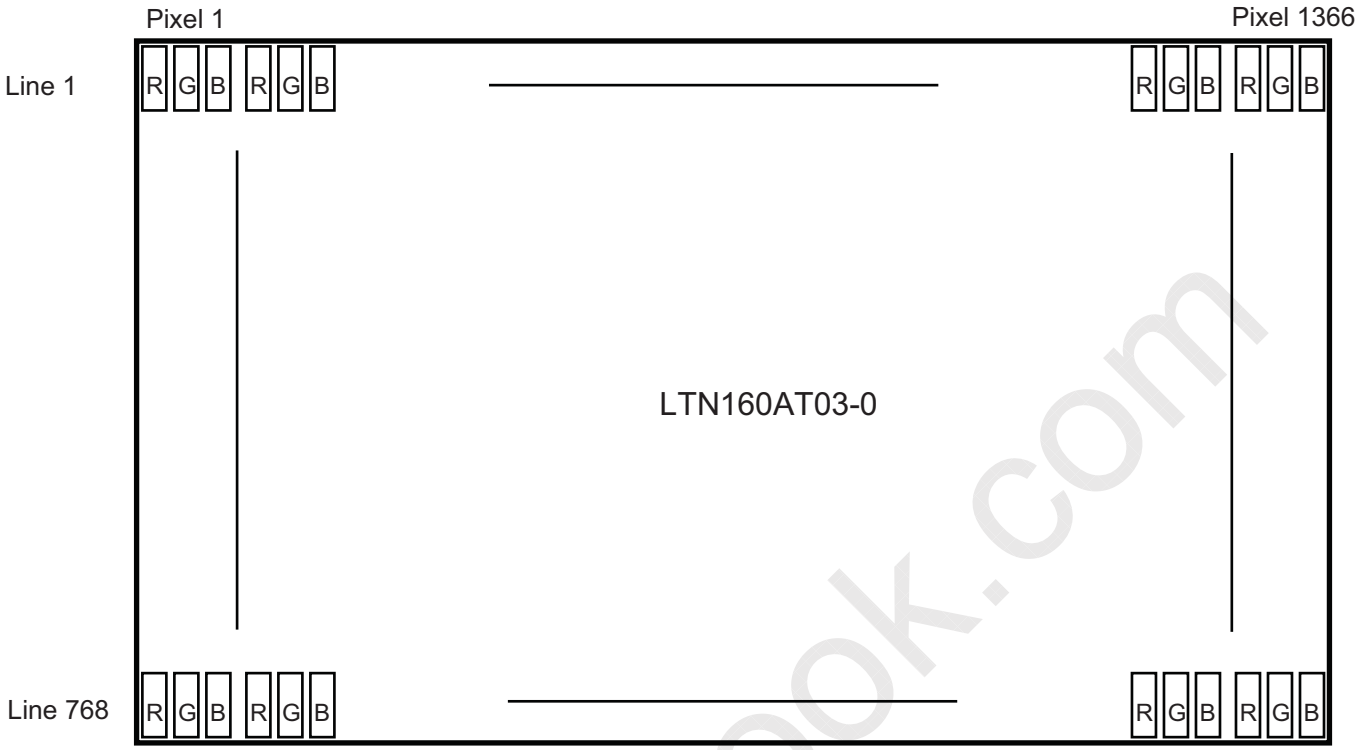
5.5 Pixel Format in the display



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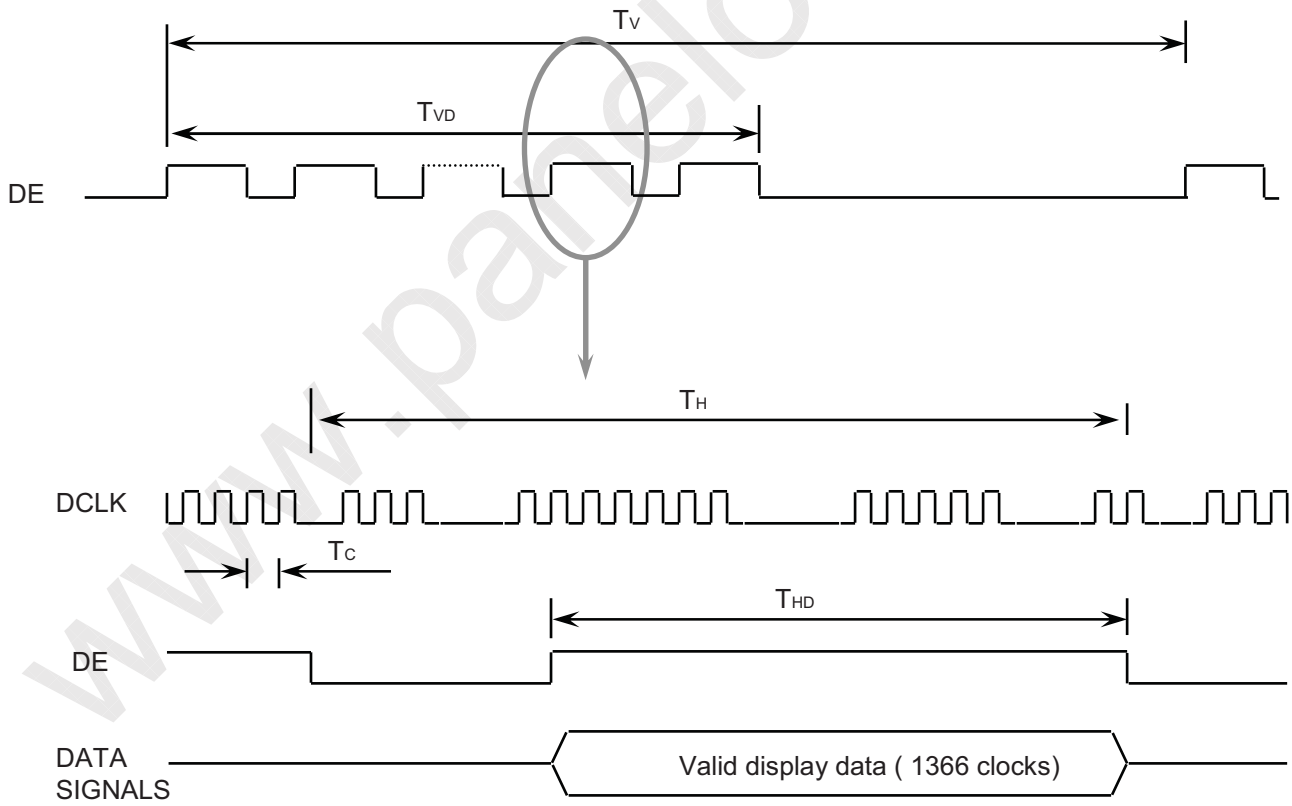
6. INTERFACE TIMING

6.1 Timing Parameters

Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Frame Frequency	Cycle	TV	-	790	-	Lines	-
Vertical Active Display Term	Display Period	TVD	-	768	-	Lines	-
One Line Scanning Time	Cycle	TH	-	1526	-	Clocks	-
Horizontal Active Display Term	Display Period	THD	-	1366	-	Clocks	-

6.2 Timing diagrams of interface signal



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6.3 Power ON/OFF Sequence

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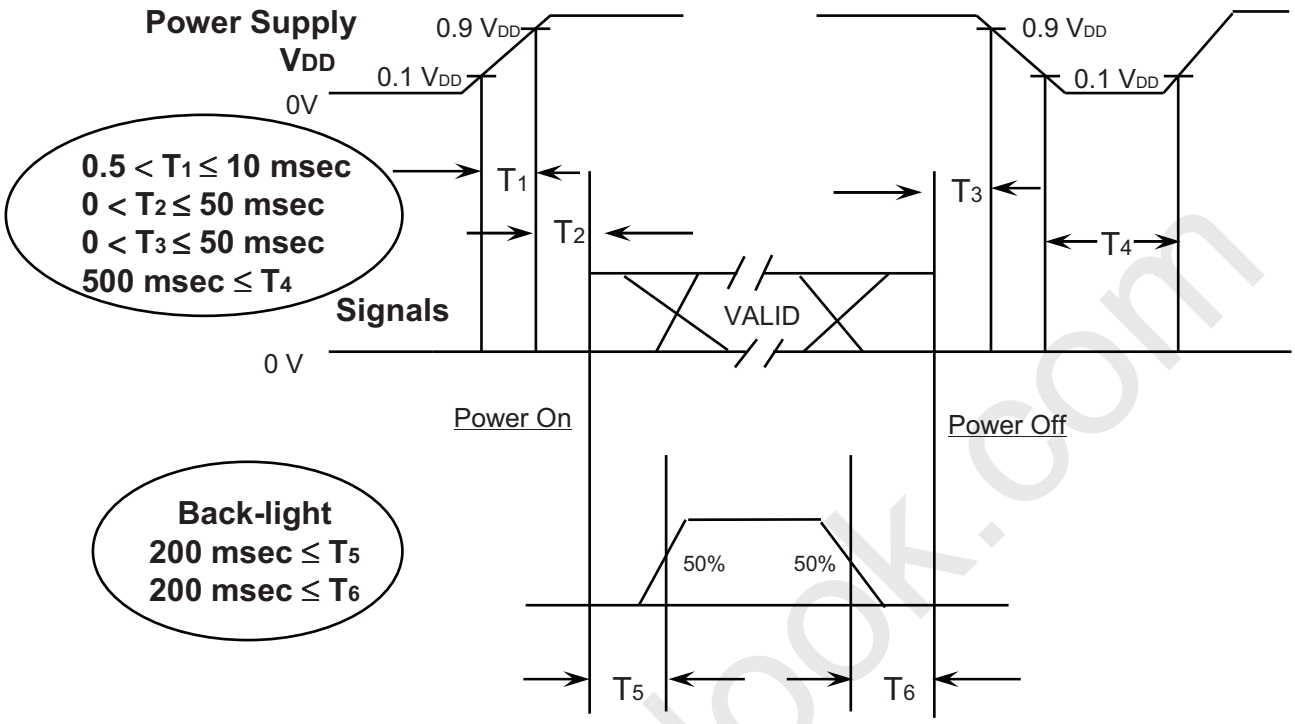
: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Power ON/OFF Sequence

T1 : Vdd rising time from 10% to 90%
T2 : The time from Vdd to valid data at power ON.
T3 : The time from valid data off to Vdd off at power Off.
T4 : Vdd off time for Windows restart
T5 : The time from valid data to B/L enable at power ON.
T6 : The time from valid data off to B/L disable at power Off.

NOTE.

(1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD}.

(2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.

(3) In case of V_{DD} = off level, please keep the level of input signals on the low or keep a high impedance.

(4) T4 should be measured after the module has been fully discharged between power off and on period.

(5) Interface signal shall not be kept at high impedance when the power is on.

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7. MECHANICAL OUTLINE DIMENSION

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It will be attached with PDF file

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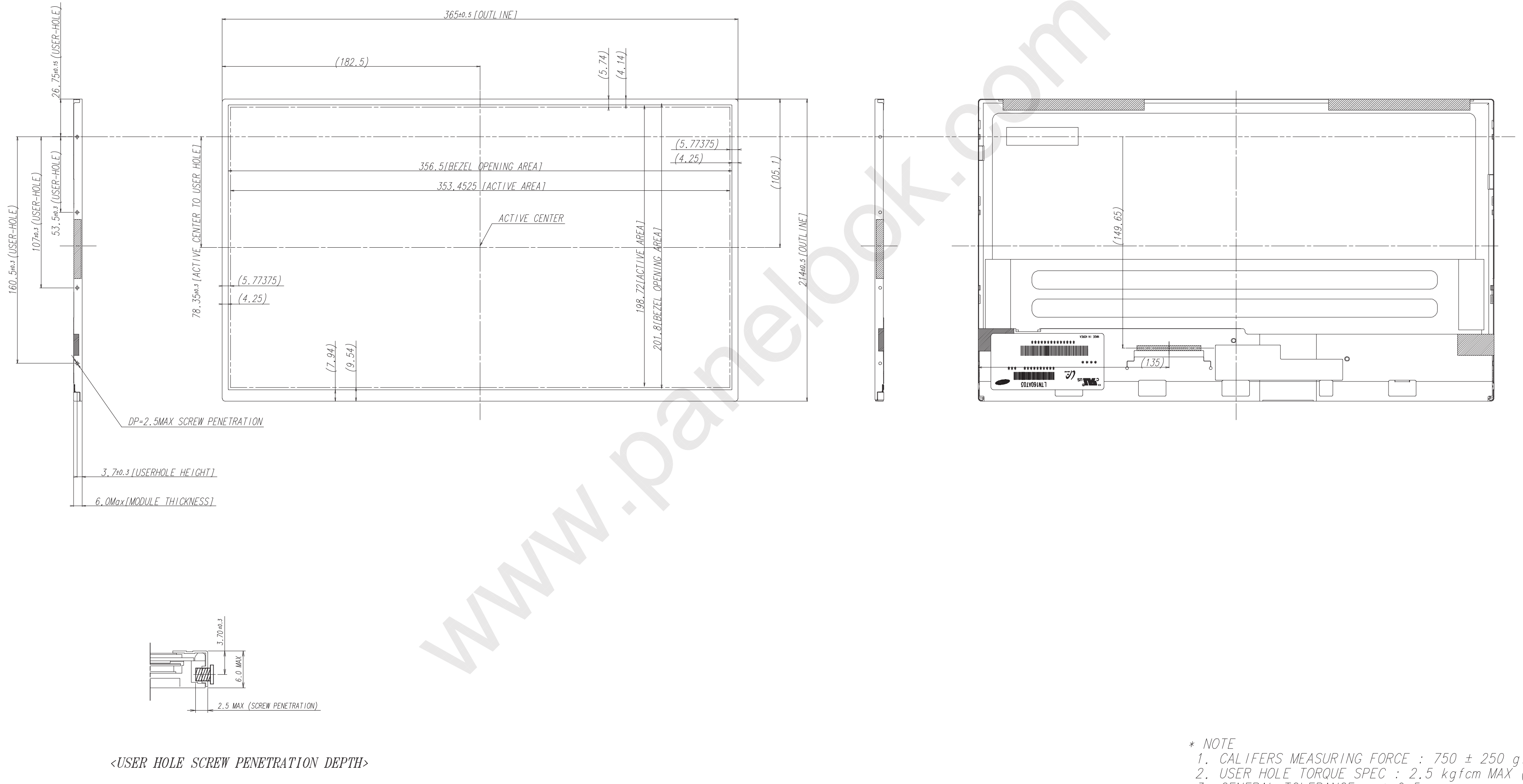

* NOTE
1. CALIFERS MEASURING FORCE : 750 ± 250 gf
2. USER HOLE TORQUE SPEC : 2.5 kgfcm MAX (5TIMES)
3. GENERAL TOLERANCE : ± 0.5

REV	DATE	DESCRIPTION OF REVISION	REASON	CHG'D BY
UNIT mm / SCALE 1/1 / GENERAL TOLERANCE ±0.5	DRA'N BY J.A.LEE 2007.11.09	DES'D BY S.C.JEONG 2007.11.09	CHK'D BY / APP'D BY S.J.LEE 2007.11.09	MODEL NAME 160HD (W-LED)
SAMSUNG ELECTRONICS			PART/SHEET NAME	OUTLINE DIMENSION

REVISION

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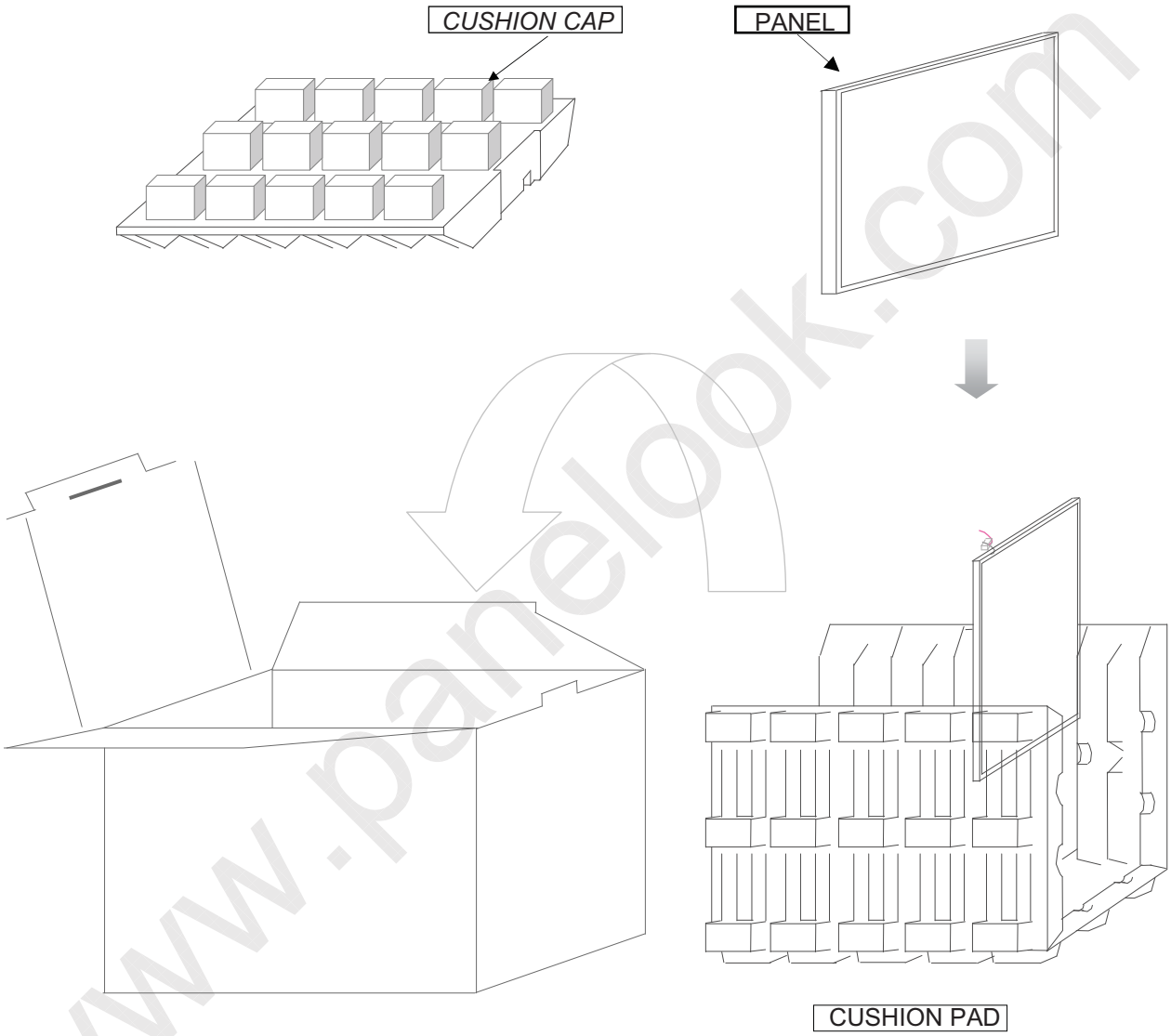
8. PACKING

1. CARTON(Internal Package)

(1) Packing Form
Corrugated Cardboard box and Corrupad form as shock absorber

(2) Packing Method



Note 1)Total Weight : Approximately 10 kg
2) Acceptance number of piling : 5 sets
3) Carton size : 463(W) x 359(L) x 333(H) mm

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No	Part name	Quantity
1	Static electric protective sack	10
2	Packing case (Inner box) included shock absorber	1 set
3	Pictorial marking	2 pcs
4	Carton	1 set

9. MARKINGS & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Parts number : LTNXXXXXXX-XXX

- Product code. + Customer code. → extension code.
- Generation
- Tech, information
- Resolution
- Size

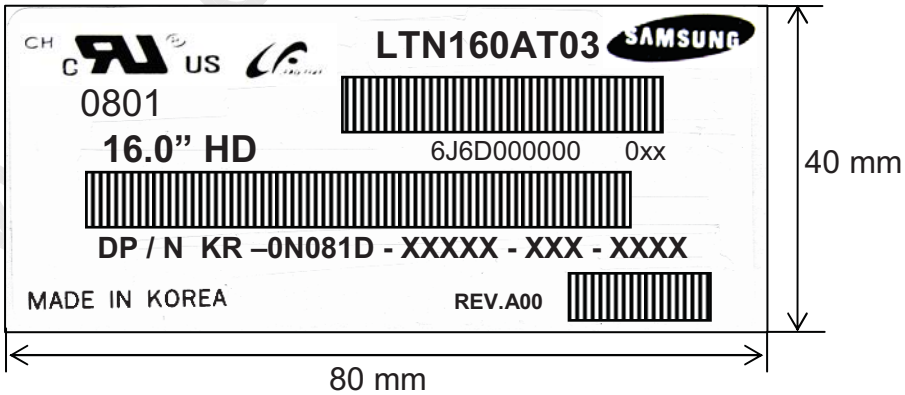
(2) Revision : Three letters

(3) Lot number : X Y 7 A XXX XX X

- Cell Position No.(In the one Glass)
- Glass No.(In the one Lot)
- Lot No.(Glass)
- Month
- Year(Note 1)
- Product Code
- Line

NOTE 1). This code indicating year is omitted in the products of KIHEUNG site.

(5) Nameplate Indication(Following example is only for reference)



Parts name : LTN160AT03-0
Lot number : 6J6D000000
Inspected work week : 0801 Number ("**0N081D**" is for 160AT03-0)
DP/N : Dell Part
REV.A00 : Product Revision Code

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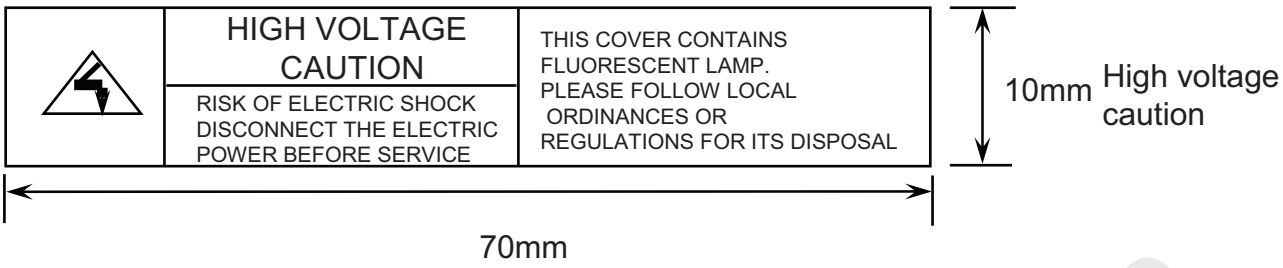
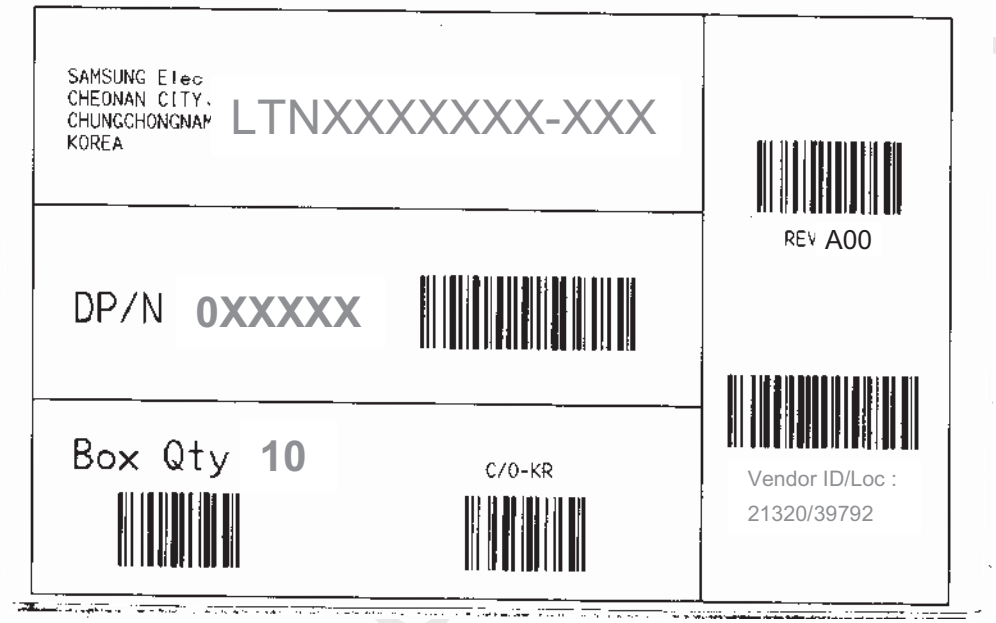
This HIGH VOLTAGE CAUTION is carved in mold frame



(6) Packing small box attach (Following example is only for reference)



0XXXXX : DELL P/N

(7) Packing box Marking : Samsung TFT-LCD Brand Name

wise view™

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10. GENERAL PRECAUTIONS

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1. Handling

(a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.

(b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module.

(c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.

(d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.

(e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(f) The desirable cleaners are water, IPA (Isoprophyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.

(h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.

(i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(j) Do not disassemble the module.

(k) Do not pull or fold the lamp wire.

(l) Do not adjust the variable resistor which is located on the back side.

(m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(n) Pins of I/F connector shall not be touched directly with bare hands.

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2. STORAGE

(a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.

(b) Do not store the TFT-LCD module in direct sunlight.

(c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

3. OPERATION

(a) Do not connect,disconnect the module in the " Power On" condition.

(b) Power supply should always be turned on/off by following item 6.3 " Power on/off sequence ".

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The standard limited warranty is only applicable when the module is used for general notebook applications. If used for purposes other than as specified, SEC is not to be held reliable for the defective operations. It is strongly recommended to contact SEC to find out fitness for a particular purpose.

4. OTHERS

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, so on) Otherwise the module may be damaged.

(d) If the module displays the same pattern continuously for a long period of time,it can be the situation when the image "sticks" to the screen.

(e) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

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	Byte (hex)	Field Name and Comments	Value (hex)	Value (binary)
Header	0	Header	00	00000000
	1	Header	FF	11111111
	2	Header	FF	11111111
	3	Header	FF	11111111
	4	Header	FF	11111111
	5	Header	FF	11111111
	6	Header	FF	11111111
	7	Header	00	00000000
Vendor / Product EDID Version	8	EISA manufacture code = 3 Character ID	4C	01001100
	9	EISA manufacture code (Compressed ASCII)	A3	10100011
	0A	Panel Supplier Reserved – Product Code	41	01000001
	0B	Panel Supplier Reserved – Product Code	54	01010100
	0C	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0D	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0E	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	0F	LCD module Serial No - Preferred but Optional ("0" if not used)	00	00000000
	10	Week of manufacture	00	00000000
	11	Year of manufacture	12	00010010
	12	EDID structure version # = 1	01	00000001
	13	EDID revision # = 3	03	00000011
Display Parameters	14	Video I/P definition = Digital I/P	90	10010000
	15	Max H image size = (Rounded to cm)	23	00100011
	16	Max V image size = (Rounded to cm)	14	00010100
	17	Display gamma = (gamma ×100)-100 = Example: (2.2 × 100) – 100 = 120	78	01111000
	18	Feature support (no DPMS, Active off, RGB, timing BLK 1)	0A	00001010
Panel Color Coordinates	19	Red/Green Low bit (RxRy/GxGy)	87	10000111
	1A	Blue/White Low bit (BxBy/WxWy)	F5	11110101
	1B	Red X Rx = 0.xxx	94	10010100
	1C	Red Y Ry = 0.xxx	57	01010111
	1D	Green X Gx = 0.xxx	4F	01001111
	1E	Green Y Gy = 0.xxx	8C	10001100
	1F	Blue X Bx = 0.xxx	27	00100111
	20	Blue Y By = 0.xxx	27	00100111
	21	White X Wx = 0.xxx	50	01010000
	22	White Y Wy = 0.xxx	54	01010100
Established Timings	23	Established timings 1 (00h if not used)	00	00000000
	24	Established timings 2 (00h if not used)	00	00000000
	25	Manufacturer's timings (00h if not used)	00	00000000

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Standard Timing ID	26	Standard timing ID1 (01h if not used)	01	00000001
	27	Standard timing ID1 (01h if not used)	01	00000001
	28	Standard timing ID2 (01h if not used)	01	00000001
	29	Standard timing ID2 (01h if not used)	01	00000001
	2A	Standard timing ID3 (01h if not used)	01	00000001
	2B	Standard timing ID3 (01h if not used)	01	00000001
	2C	Standard timing ID4 (01h if not used)	01	00000001
	2D	Standard timing ID4 (01h if not used)	01	00000001
	2E	Standard timing ID5 (01h if not used)	01	00000001
	2F	Standard timing ID5 (01h if not used)	01	00000001
	30	Standard timing ID6 (01h if not used)	01	00000001
	31	Standard timing ID6 (01h if not used)	01	00000001
	32	Standard timing ID7 (01h if not used)	01	00000001
	33	Standard timing ID7 (01h if not used)	01	00000001
	34	Standard timing ID8 (01h if not used)	01	00000001
	35	Standard timing ID8 (01h if not used)	01	00000001
Timing Descripter #1	36	Pixel Clock/10,000 (LSB)	83	10000011
	37	Pixel Clock/10,000 (MSB)	1B	00011011
	38	Horizontal Active = xxxx pixels (lower 8 bits)	56	01010110
	39	Horizontal Blanking (Thbp) = xxxx pixels (lower 8 bits)	78	01111000
	3A	Horizontal Active/Horizontal blanking (Thbp) (upper4:4 bits)	50	01010000
	3B	Vertical Active = xxxx lines	00	00000000
	3C	Vertical Blanking (Tvbp) = xxxx lines (DE Blanking typ. for DE only panels)	16	00010110
	3D	Vertical Active : Vertical Blanking (Tvbp) (upper4:4 bits)	30	00110000
	3E	Horizontal Sync, Offset (Thfp) = xxxx pixels	30	00110000
	3F	Horizontal Sync, Pulse Width = xxxx pixels	20	00100000
	40	Vertical Sync, Offset (Tvfp) = xx lines Sync Width = xx lines	25	00100101
	41	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
	42	Horizontal Image Size =xxx mm	61	01100001
	43	Vertical image Size = xxx mm	C6	11000110
	44	Horizontal Image Size / Vertical image size	10	00010000
	45	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	46	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	47	Bit[7] 0: Non-interlace, 1: Interlace Bit[6:5] 00: Normal display, no strero, XX: See table xx for definition Bit[4:3] 00: Analog composite, 01: Bipolar analog composite, 10: Digital composite, 11: Digital separate Bit[2:1] :The interpretation of bits 2 and 1 is dependent on the decode of bits 4 and 3 - see Table 3.18. Bit[0] :See Table VESA EDID spec for definition Referenced Default = 1Ah	1A	00011010
Timing Descripter #2	48	Pixel Clock/10,000 (LSB)	83	10000011
	49	Pixel Clock/10,000 (MSB)	1B	00011011
	4A	Horizontal Active = xxxx pixels (lower 8 bits)	56	01010110
	4B	Horizontal Blanking (Thbp) = xxxx pixels (lower 8 bits)	78	01111000
	4C	Horizontal Active/Horizontal blanking (Thbp) (upper4:4 bits)	50	01010000
	4D	Vertical Active = xxxx lines	00	00000000
	4E	Vertical Blanking (Tvbp) = xxxx lines (DE Blanking typ. for DE only panels)	16	00010110
	4F	Vertical Active : Vertical Blanking (Tvbp) (upper4:4 bits)	30	00110000
	50	Horizontal Sync, Offset (Thfp) = xxxx pixels	30	00110000
	51	Horizontal Sync, Pulse Width = xxxx pixels	20	00100000
	52	Vertical Sync, Offset (Tvfp) = xx lines Sync Width = xx lines	25	00100101
	53	Horizontal Vertical Sync Offset/Width upper 2 bits	00	00000000
	54	Horizontal Image Size =xxx mm	61	01100001
	55	Vertical image Size = xxx mm	C6	11000110
	56	Horizontal Image Size / Vertical image size	10	00010000
	57	Horizontal Border = 0 (Zero for Notebook LCD)	00	00000000
	58	Vertical Border = 0 (Zero for Notebook LCD)	00	00000000
	59	Bit[7] 0: Non-interlace, 1: Interlace Bit[6:5] 00: Normal display, no strero, XX: See table xx for definition Bit[4:3] 00: Analog composite, 01: Bipolar analog composite, 10: Digital composite, 11: Digital separate Bit[2:1] :The interpretation of bits 2 and 1 is dependent on the decode of bits 4 and 3 - see Table 3.18. Bit[0] :See Table VESA EDID spec for definition Referenced Default = 1Ah	1A	00011010

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Timing Descripter #3 Dell specific information	5A	Flag	00	00000000
	5B	Flag	00	00000000
	5C	Flag	00	00000000
	5D	Data Type Tag: Alphanumeric Data String (ASCII)	FE	11111110
	5E	Flag	00	00000000
	5F	Dell P/N 1st Character	4E	01001110
	60	Dell P/N 2nd Character	30	00110000
	61	Dell P/N 3rd Character	38	00111000
	62	Dell P/N 4th Character	31	00110001
	63	Dell P/N 5th Character	44	01000100
	64	LCD Supplier EEDID Revision # Bit[7] : 0=X, 1=A Bit[6:0] : 00, 01, 02… for SST 10, 11, 12… for PT 20, 21, 22… for ST 00, 01, 02… for X-Build (if Bit[7]=1)	80	10000000
	65	Manufacturer P/N	31	00110001
	66	Manufacturer P/N	36	00110110
	67	Manufacturer P/N	30	00110000
	68	Manufacturer P/N	41	01000001
	69	Manufacturer P/N	54	01010100
	6A	Manufacturer P/N	0A	00001010
	6B	Manufacturer P/N (If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
Timing Descripter #4	6C	Flag	00	00000000
	6D	Flag	00	00000000
	6E	Flag	00	00000000
	6F	Data Type Tag: Manufacturer Specified Data 00	00	00000000
	70	Flag	00	00000000
	71	SMBUS Value = XX nits	00	00000000
	72	SMBUS Value = XX nits	00	00000000
	73	SMBUS Value = XX nits	00	00000000
	74	SMBUS Value = XX nits	00	00000000
	75	SMBUS Value = XX nits	00	00000000
	76	SMBUS Value = XXX nits	00	00000000
	77	SMBUS Value = XXX nits	00	00000000
	78	SMBUS Value = max nits (Typically = 00h, XXX nits)	00	00000000
	79	Bit[7:3] Reserved Bit[2] 0: No RTC support, 1: RTC support Bit[1:0] 00: reserved, 01: single LVDS, 10: dual LVDS, 11: reserved 01h single channel LVDS, no RTC support 02h dual channel LVDS, no RTC support 05h single channel LVDS, with RTC support 06h dual channel LVDS, with RTC support	01	00000001
	7A	BIST Enable: Yes = '01' No = '00'	01	00000001
	7B	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	0A	00001010
	7C	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
	7D	(If <13 char, then terminate with ASCII code 0Ah, set remaining char = 20h)	20	00100000
Checksum	7E	Extension flag (# of optional 128 EDID extension blocks to follow, Typ = 0)	00	00000000
	7F	Checksum (The 1-byte sum of all 128 bytes in this EDID block shall = 0)	04	00000100