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* Revision History

Date	Rev. No	Page	Summary
Jan. 29, 2010	000	-	First Issued

General Description

Description

LTY[Z]320HM02 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 32.0“ is 1920 x 1080 and this model can display up to 16.7million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and High Definition TV (HDTV).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, high aperture ratio, fast response time
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle (±89°)
- Full HD (1920 x 1080 pixels) resolution (16:9)
- Low Power consumption
- WLED (White Light Emitting Diode) Backlight
- LVDS (Low Voltage Differential Signaling) interface

General Information

Items	Specification	Unit	Note
Module Size	739.6(H_{TYP}) x 440(V_{TYP})	mm	±1.0mm
	25.6(D_{MAX})		
Weight	5000(Max)	g	
Pixel Pitch	0.12125(H) * 3 x 0.36375(V)	mm	
Active Display Area	698.4(H) x 392.85(V)	mm	
Surface Treatment	Haze 7%, Hard-coating (3H)		
Display Colors	8 bit- 16.7M	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	450	cd/m^2	Typ.

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V_{DD}	GND-0.5	*13*	V	(1)
Storage temperature	T_{STG}	-20	65	℃	(2)
Operating Temperature	T_{OPR}	0	50	℃	(2)
Panel surface temperature	T_{SUR}	0	65	℃	(3)
Shock (non - operating)	S_{NOP}	-	50	G	(4)
Vibration (non - operating)	V_{NOP}	-	1.5	G	(5)

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.
   a. 93.8 % RH Max. (Ta ≤ 40 °C)
   b. Maximum wet-bulb temperature at 40 °C or less. (Ta ≤ 40 °C)
   c. No condensation

(3) Polarizer will not be damaged in this range, even though abnormal visual problems occur in T_{SUR} range.

(4) 11ms, sine wave, one time for ±X, ±Y, ±Z axis

(5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

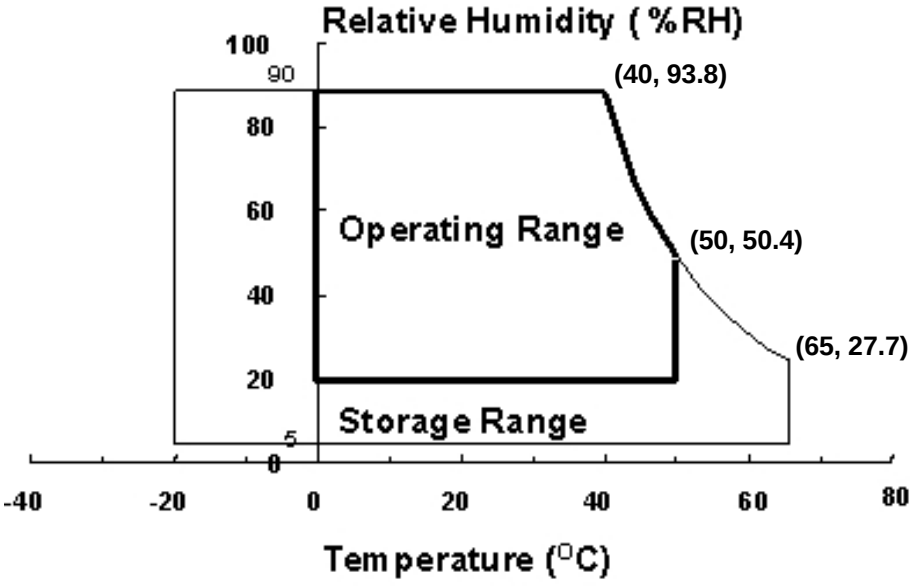


Fig. Temperature and Relative humidity range

2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON BM-7,SPECTRORADIOMETER SR-3
(Ta = 25 ± 2°C, VDD=12V, fv= 60Hz, f_{DCLK}=148.5MHz, Dim = 100%)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal θ**L,R**=0 θ**U,D**=0 Viewing Angle	*2500	5000	-		(3) SR-3
Response Time	Rising	Tr		-	12	70	msec	(5) BM-7
	Falling	Tf		-	8	18		
	G-to-G [AVE]	Tg		-	8	-		
Luminance of White (Center of screen)		Y_L		380	450	-	cd/m^2	(6) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	0.625	TYP. +0.03		(7),(8) SR-3
		Ry			0.340			
	Green	Gx			0.315			
		Gy			0.628			
	Blue	Bx			0.155			
		By			0.045			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	72	-	%	(7) SR-3
Color Temperature		-		-	10000	-	K	(7) SR-3
Gamma		γ		-	2.2	-		
Viewing Angle	Hor.	θ_L	C/R≥10	79	89	-	Degree	(8) SR-3
		θ_R		79	89	-		
	Ver.	θ_U		79	89	-		
		θ_D		79	89	-		
Brightness Uniformity (9 Points)		B_{uni}		-	-	30	%	(4) SR-3

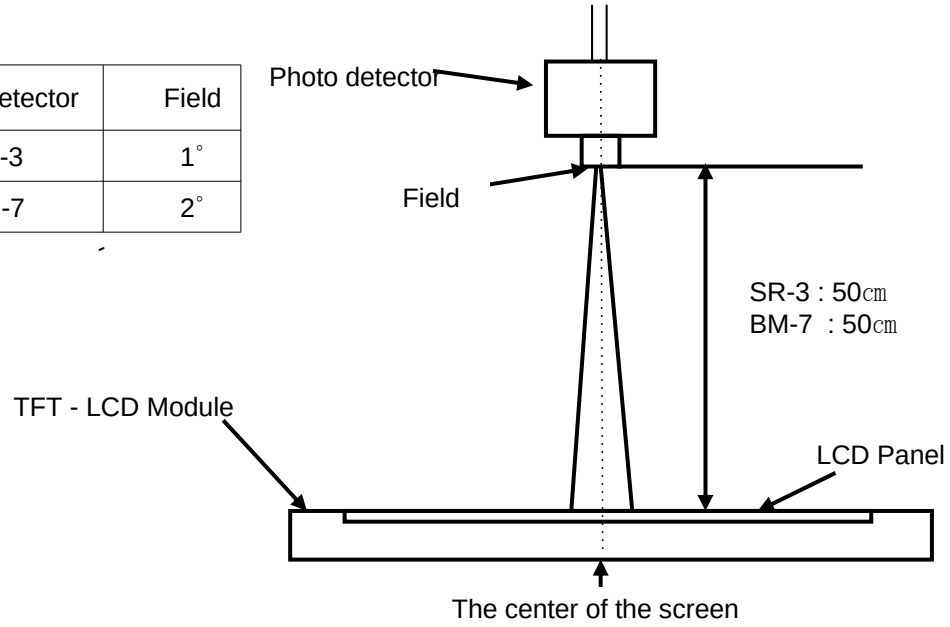
* marked Items Value does not Specification above when “White stain” occurs at Center Point [Point ⑤ of Note 2.]

Note (1) Test Equipment Setup

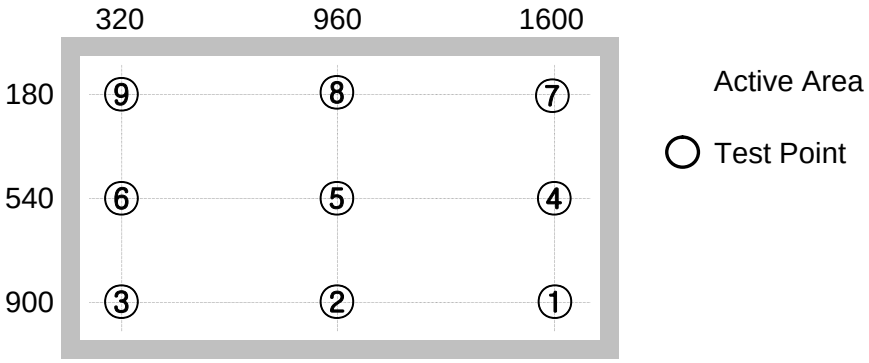
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Single lamp current @ Vdim = 100%
Environment condition : Ta = 25 ± 2 °C

Photo detector	Field
SR-3	1°
BM-7	2°



Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)
: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G\max}{G\min}$$

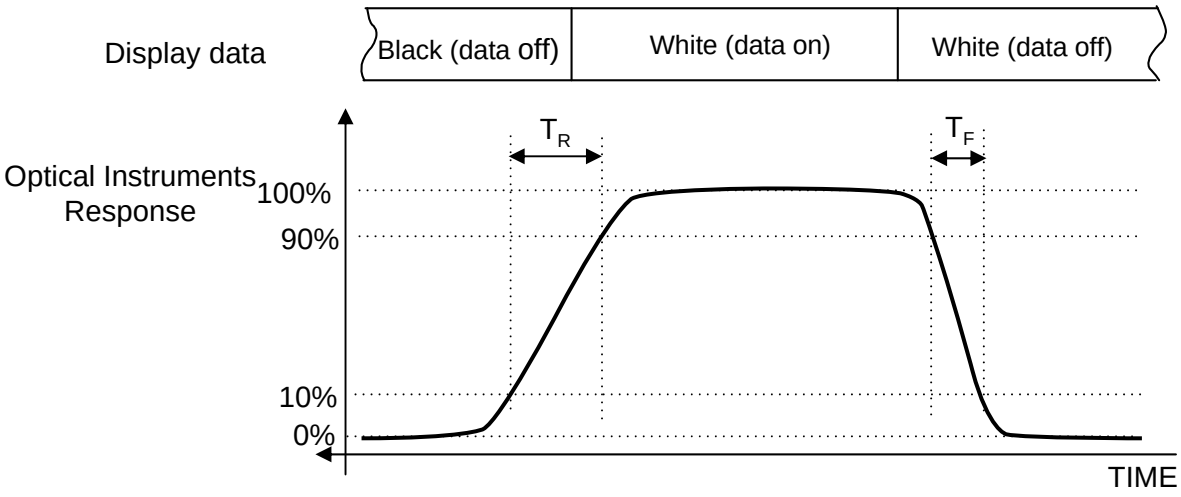
Gmax : Luminance with all pixels white
Gmin : Luminance with all pixels black

Note (4) Definition of 9 points brightness uniformity

$$Buni = 100 * \frac{(B\max - B\min)}{B\max}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

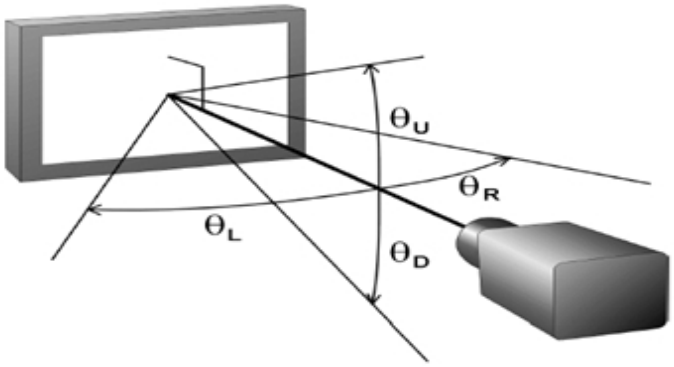
Note (5) Definition of Response time : Sum of Tr, Tf



Note (6) Definition of Luminance of White : Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle
: Viewing angle range (C/R ⩾10)



3. Electrical Characteristics

3.1 TFT LCD Module

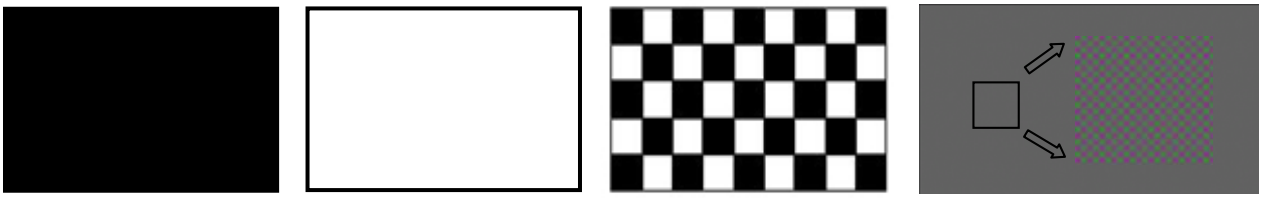
The connector for display data & timing signal should be connected.

Ta = 25°C ± 2 °C

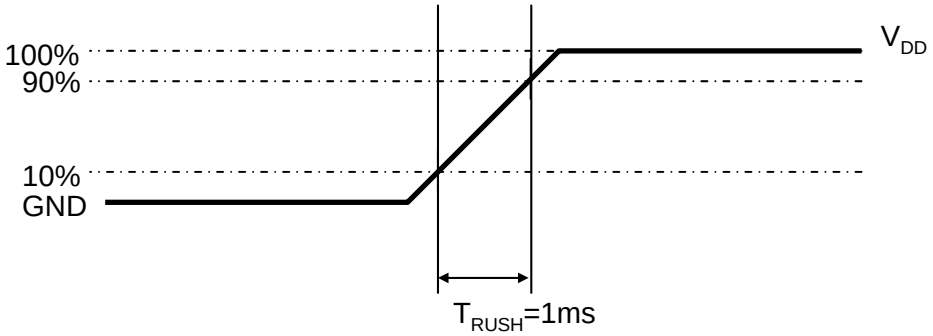
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	11	12	13	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	600	-	mA	(2),(3)
	(b) White		-	810	970		
	(c) Mosaic		-	700	-		
	(4) Max Pattern		-	880	1060		
Vsync Frequency		f_V	47	60	62	Hz	
Hsync Frequency		f_H	50	67.5	73	kHz	
Main Frequency		f_{DCLK}	130	148.5	155	MHz	
Rush Current		I_{RUSH}	-	-	5	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}.
(2) fv=60Hz, f_{DCLK} = 148.5MHz, V_{DD} = 12.0V, DC Current.
(3) Power dissipation check pattern (LCD Module only)

a) Black Pattern b) White Pattern c) Mosaic d)Max. Pattern



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH}. is1ms

3.2 Back Light Unit

The back light unit contains 136 LEDs.
The characteristics of lamps are shown in the following tables.

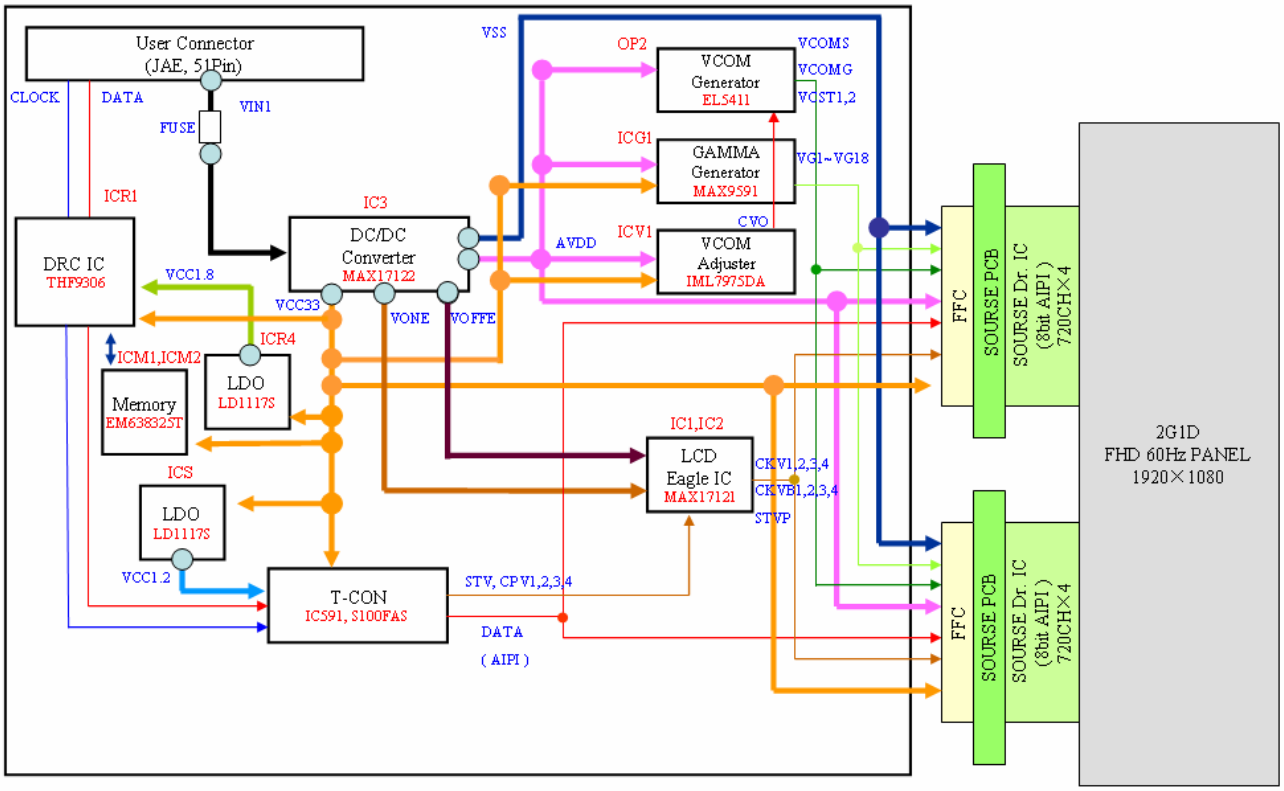
Ta=25 ± 2°C

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time		Hr	30,000	-	-	Hour	(1)
Operating Current	Continuous	Iop	-	-	160	mA	/String
	Impulsive (60Hz/Duty50%)	Iop	-	-	185	mA	@110mA /Duty100%
Operating Voltage	Continuous	Vop	-	-	120	V	34LEDs /110mA@Tj 25℃
	Impulsive (60Hz/Duty50%)	Vop	-	-	130	V	Tj 25℃

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Definition of Operating Voltage : At each Strings, I_{op} = 110.0 mArms (typ.)]

4. Block Diagram



5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector : IS050-C51B-C38(UJU)

Compatible with FI-RE51S-HF(JAE)

PIN No.	Signal	Description	PIN No.	Signal	Description
1	Power	V_{DD}	26	RX1_AP_I	Even LVDS Signal +
2	Power	V_{DD}	27	RX1_BN_I	Even LVDS Signal -
3	Power	V_{DD}	28	RX1_BP_I	Even LVDS Signal +
4	Power	V_{DD}	29	RX1_CN_I	Even LVDS Signal -
5	Power	V_{DD}	30	RX1_CP_I	Even LVDS Signal +
6	N.C.	No Connection	31	GND	GND
7	GND	GND	32	RX1_CLKN_I	Even LVDS Clock-
8	GND	GND	33	RX1_CNKP_I	Even LVDS Clock+
9	GND	GND	34	GND	GND
10	RX0_AN_I	Odd LVDS Signal -	35	RX1_DN_I	Even LVDS Signal -
11	RX0_AP_I	Odd LVDS Signal +	36	RX1_DP_I	Even LVDS Signal +
12	RX0_BN_I	Odd LVDS Signal -	37	N.C.	No Connection
13	RX0_BP_I	Odd LVDS Signal +	38	N.C.	No Connection
14	RX0_CN_I	Odd LVDS Signal -	39	GND	GND
15	RX0_CP_I	Odd LVDS Signal +	40	SCL_C	I2C SCL
16	GND	GND	41	SDA_C	I2C SDA
17	RX0_CLKN_I	Odd LVDS CLK -	42	VSYNC	V Sync Signal
18	RX0_CLKP_I	Odd LVDS CLK +	43	BUS_SW	Bus Switching
19	GND	GND	44	N.C.	No Connection
20	RX0_DN_I	Odd LVDS Signal -	45	N.C.	No Connection
21	RX0_DP_I	Odd LVDS Signal +	46	LUT SEL0	DCC LUT Select 0
22	N.C.	No Connection	47	LUT SEL1	DCC LUT Select1
23	N.C.	No Connection	48	LUT SEL2	DCC LUT Select2
24	GND	GND	49	HSYNC	H Sync Signal
25	RX1_AN_I	Even LVDS Signal -	50	TCON_RDY	T-Con Ready
			51	SEL1	SEC Internal Use Only

■ BUS_SW : I2C BUS Switching

Operation	Description
LOW/OPEN	SDA/SCL LINE BECOME HI-Z
HIGH	USER CAN ACCESS EEPROM

■ DCC Look Up Table Selection

Pin N.O.	48	47	46	Description (Based on DCC On)	
				LUT	%(For Interpolate)
Select bit	0	0	0	For 60Hz	100%
	0	0	1	For 60Hz	100%X1.25
	0	1	0	For 60Hz @ Low Temp.	100%X1.25
	0	1	1	For 60Hz @ Low Temp.	(100%X1.25) X 1.25
	1	0	0	For 50Hz	100%
	1	0	1	For 50Hz	100%X1.25
	1	1	0	0%	
	1	1	1		

■ TCON Ready

Operation	Description
Low → High	Normal Operation
High → Low	Error Operation

Note(1) Pin number starts from Right side

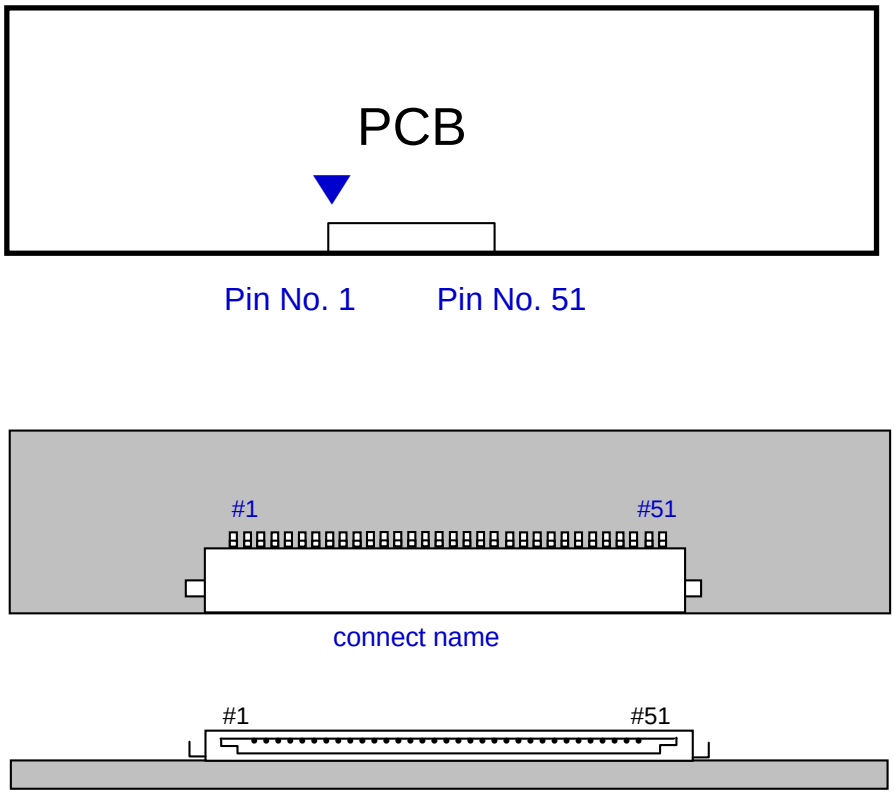


Fig. Connector diagram

a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
b. All power input pins should be connected together.
c. All NC pins should be separated from other signal or power.

5.2 Inverter Input Pin Configuration

■ 4pin :DC Voltage Connecter : 51103-0400 (Molex)

Pin No.	Pin Configuration (FUNCTION)
1	**1 Channel DC Voltage**
2	**2 Channel DC Voltage**
3	**No Connection**
4	**No Connection**

■ 5pin :Feedback Voltage Connecter : 51103-0500 (Molex)

Pin No.	Pin Configuration (FUNCTION)
1	**1 Channel Feedback**
2	**2 Channel Feedback**
3	**No Connection**
4	**No Connection**
5	**No Connection**

5.3 LVDS Interface

	LVDS pin	Odd Data	Even Data
TxOUT/RxIN0	TxIN/RxOUT0	R2	R2
	TxIN/RxOUT1	R3	R3
	TxIN/RxOUT2	R4	R4
	TxIN/RxOUT3	R5	R5
	TxIN/RxOUT4	R6	R6
	TxIN/RxOUT6	R7	R7
	TxIN/RxOUT7	G2	G2
TxOUT/RxIN1	TxIN/RxOUT8	G3	G3
	TxIN/RxOUT9	G4	G4
	TxIN/RxOUT12	G5	G5
	TxIN/RxOUT13	G6	G6
	TxIN/RxOUT14	G7	G7
	TxIN/RxOUT15	B2	B2
	TxIN/RxOUT18	B3	B3
TxOUT/RxIN2	TxIN/RxOUT19	B4	B4
	TxIN/RxOUT20	B5	B5
	TxIN/RxOUT21	B6	B6
	TxIN/RxOUT22	B7	B7
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT28	R0	R0
	TxIN/RxOUT29	R1	R1
	TxIN/RxOUT30	G0	G0
	TxIN/RxOUT31	G1	G1
	TxIN/RxOUT32	B0	B0
	TxIN/RxOUT33	B1	B1
	TxIN/RxOUT34	Reserved	Reserved

5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																								GRAY SCALE LEVEL
		RED								GREEN								BLUE								
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7	
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			R3~ R252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			G3~ G252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2
		:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			B3~ B252
	↓ LIGHT	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

6. EDID Information

6.1 EEPROM Data

EEPROM Data					
No	Item	Spec	Address	Data	Remark
1	Panel Product Vender	XXXX	00	XX	SONY Choice - Note 1
2	Screen Size	XX	01	XX	16進
3	H-Resolution	XXXX	02,03	XX,XX	16進
4	V-Resolution	XXX	04,05	XX,XX	16進
5	Vertical Frequency	XX	06	XX	- Note 2
6	Data Format	XX	07	XX	- Note 3
7	FRC Revision Information	X	ED	XX	ASCII - Note 4
8	Part Number	XXX(X)XXXXXXX	E0~EC	XX,XX,XX,XX,XX,XX, XX,XX,XX,XX,XX,XX ,XX,XX,XX,XX	ASCII - Note 5, 6

※ Note 1.

Data	Panel Vender Code
00	HDLCD(SEC)
01	Others
02	Others
03	Others
04	Others
05	Others

※ Note 2.

Data	V-Frequency
00	50/60Hz
01	100/120HZ
02	200/240Hz

※ Note 3.

Data	Data Format
00	6Bit
01	8Bit
02	10Bit

※ Note 4.
- FRC Revision Code will be Changed from “00” to “41” (A)
  When FRC IC is changed. At First MP. This Code will be “00”

※ Note 5.
- LTY[Z]XXXXXXX Only for SEC Model

※ Note 6.
- Rest of them must be “00” (Null)

6.2 EEPROM Data Map

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	XX	XX	XX	XX	XX	XX	XX	XX	00	00	00	00	00	00	00	00
1	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
2	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
3	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
4	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
5	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
6	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
7	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
8	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
9	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
E	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	XX	00	00
F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

7. Interface Timing

7.1 Timing Parameters (DE only mode)

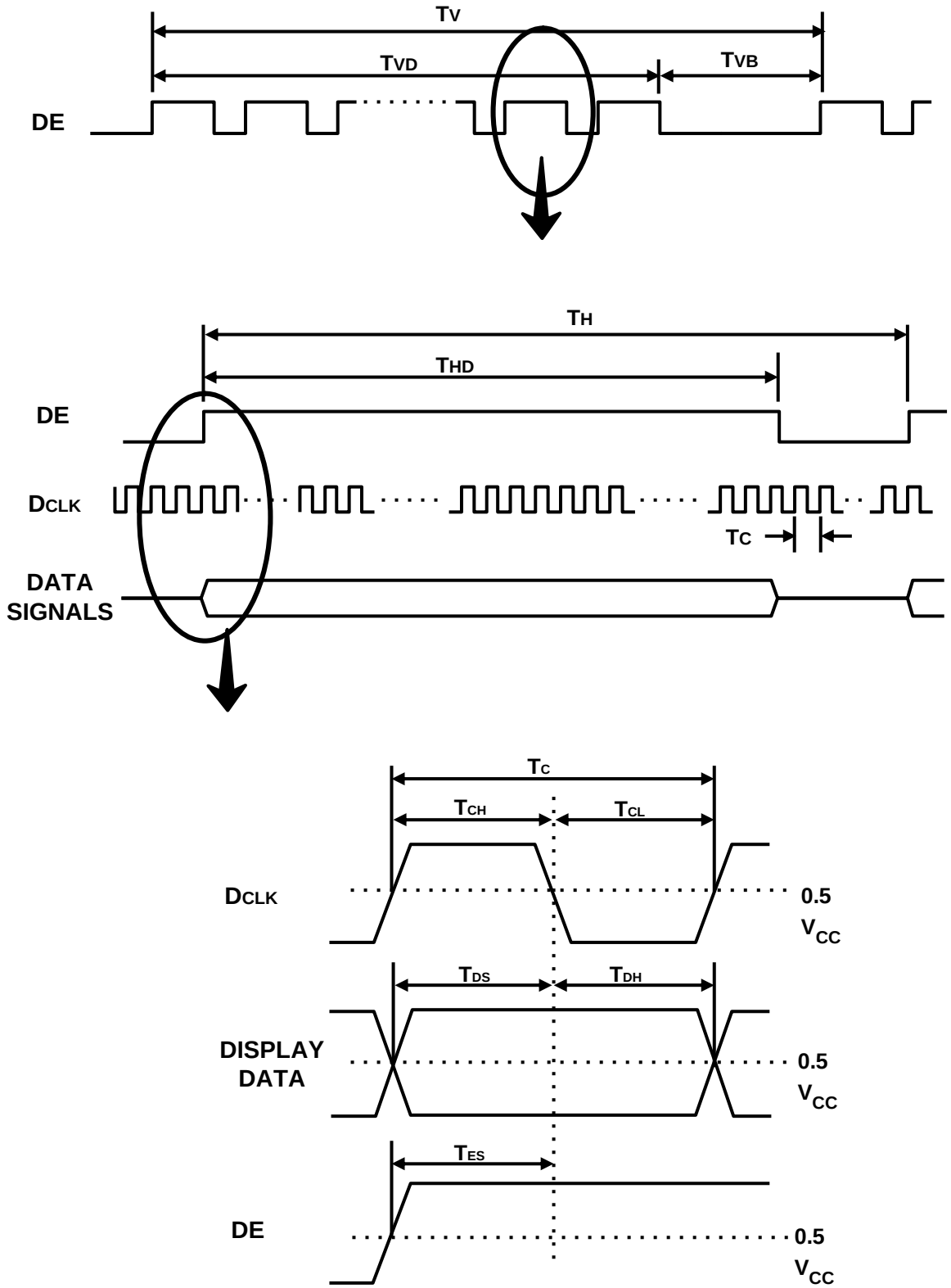
SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	130	148.5	155	MHz	-
Hsync		F_H	50	67.5	73	KHz	-
Vsync		F_V	47	60	62	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	lines	-
	Vertical Total	T_{VB}	1100	1125	1480	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	clocks	-
	Horizontal Total	T_H	2154	2200	2450	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

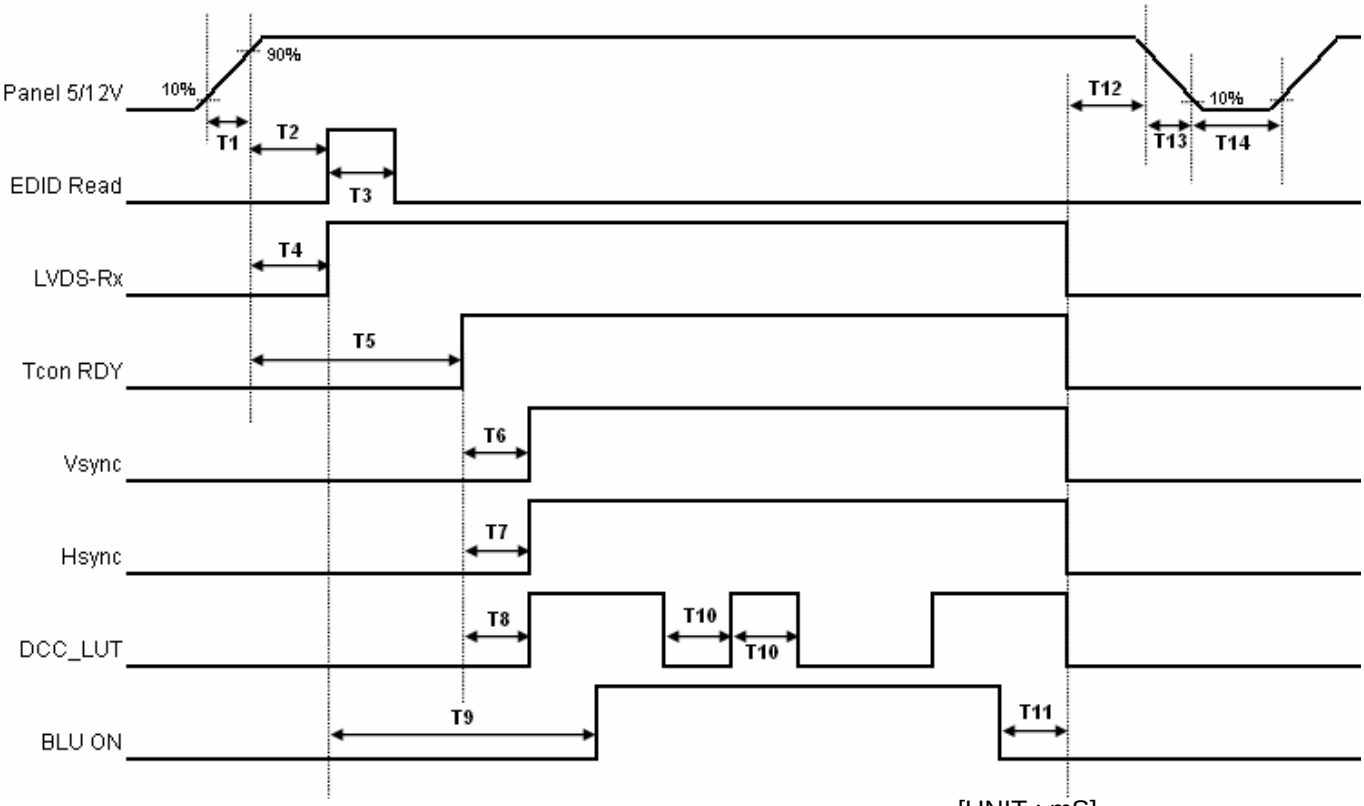
(2) Internal V_{DD} = 3.3V

7.2 Timing diagrams of interface signal (DE only mode)



7.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.

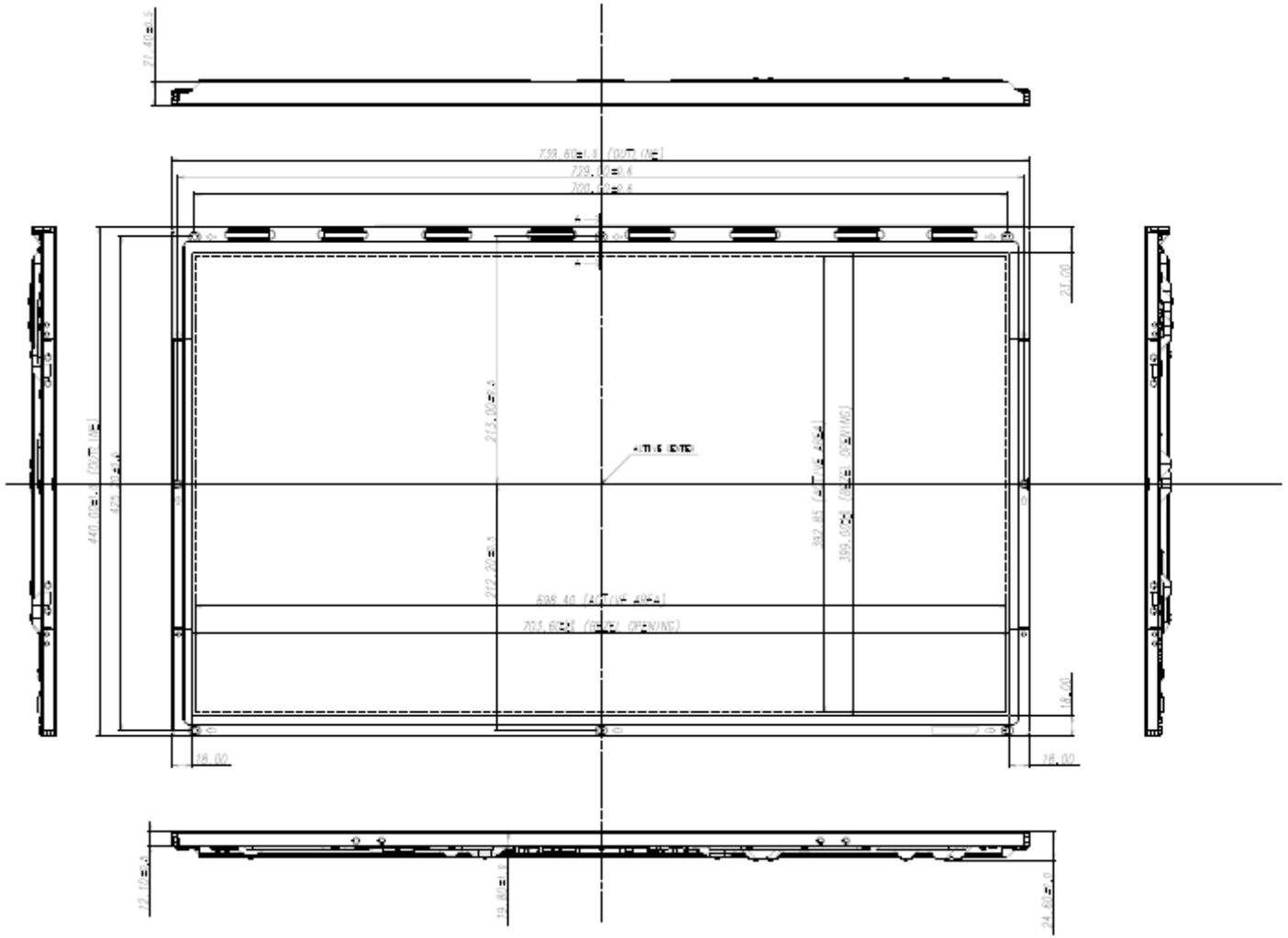


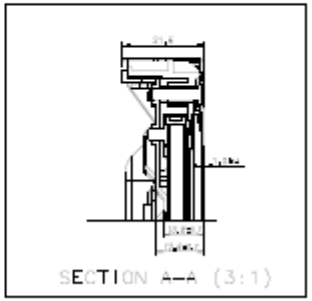
[UNIT : mS]

	Min	Max		Min	Max
T1	0.47	30	T8	30	-
T2	0	100	T9	500	-
T3	0	100	T10	50	-
T4	0.1	50	T11	100	-
T5	220	350	T12	0	50
T6	0	30	T13	0	300
T7	0	30	T14	1000	-

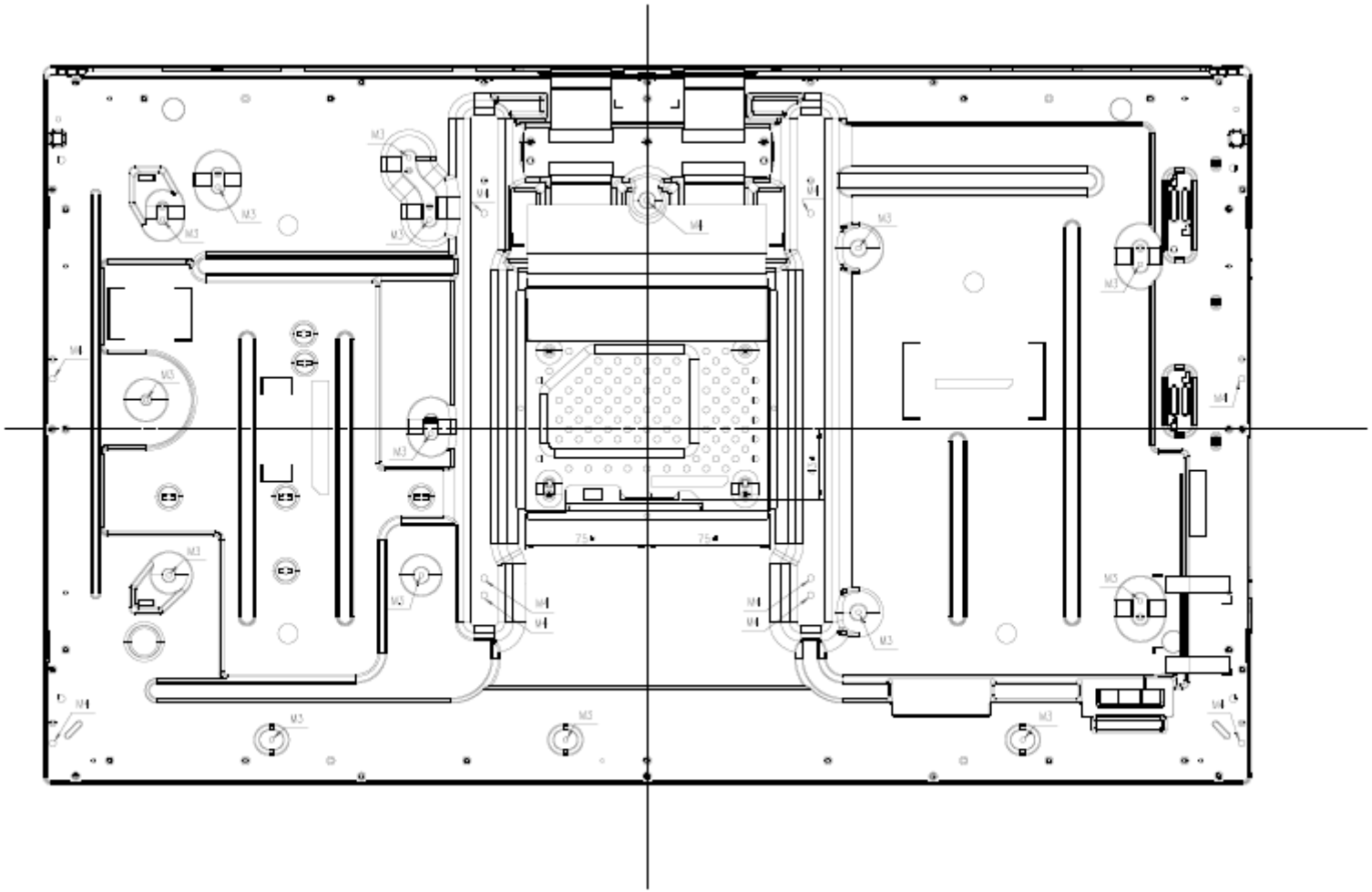
- **The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.**
- **Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.**
- **In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.**
- **T14 should be measured after the Module has been fully discharged between power off and on period.**
- **Interface signal should not be kept at high impedance when the power is on.**

8. Outline dimension (Front view)





Outline dimension (Rear View)



9. EMI Specification

: -3dB at CISPR22 Class B

This EMI Recommendation is recommended to be measured as SET Condition.

10. UL Approval

11. Reliability Test

Item	Test condition	Quantity
Temperature Step Stress	0 ~ 50℃,10Cycle determination	4EA
HTOL	50℃, 1000hr (500hr determination)	8EA
LTOL	0℃, 1000hr (500hr determination)	4EA
RTOL	20 ℃, continue ~	4EA
HTS	70℃, 1000hr (500hr determination)	4EA
LTS	-30℃,1000hr (500hr determination)	4EA
THB	40℃ / 95%RH,1000hr (500hr determination)	4EA
WHTS	60℃ / 75%RH, 1000hr (500hr determination)	4EA
T/C	-20 ℃ ~ 60 ℃, 200cycle (100cycle determination)	4EA
ESD (non-operation)	±10 kV,200㎊/100 Ω,9Point,3times/Point	3EA
ESD(operation)	contact : ±8 kV ,150㎊/330Ω,210Point,1 time/Point non-contact : ±15 kV,150㎊/330Ω,100Point,1 time/Point	3EA
Input Con. ESD	contact : ±2kV,200㎊/100,Input Con.Pin,3 times/Pin	3EA
POWER ON/OFF	30sec (on) / 30sec(off) : 12,000 times	4EA
Vibration	10~300Hz/1.5G/10minSR, XYZ, 30min/axis	3EA
Shock	Condition : 11msec, ±XY Z 1time/axis 50G	3EA
PALLET Vibration	1.05 Grms, 2~200Hz, Random, Z axis 1Hr	1PALLET(18EA)
PALLET Drop	20cm, Bottom, Front, Rear 1times	1PALLET(18EA)

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

HTOL/ LTOL : High/Low Temperature Operating Life,
THB : Temperature Humidity Bias
HTS/LTS : High/Low Temperature Storage
WHTS : Wet High Temperature Storage

12. PACKING

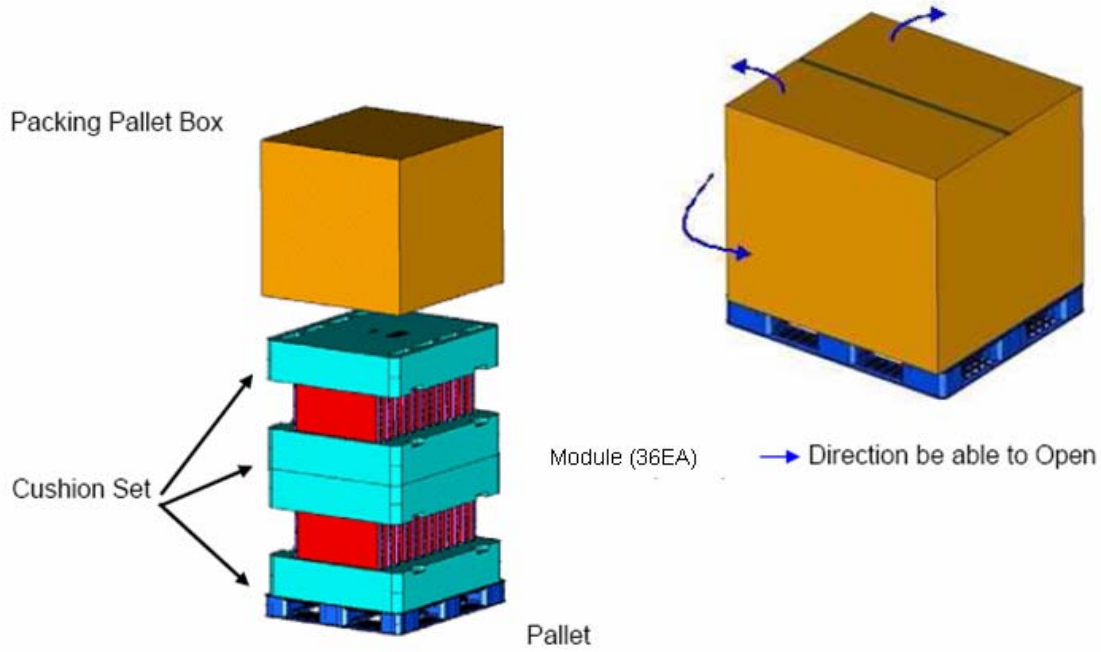
12.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method

Packing Pallet Box



12.2 Packing Specification

Item	Specification	Remark
LCD Packing	36 ea / (Packing-Pallet Box)	1. 180Kg/LCD(36ea) 2. 14kg/Cushion Set(4ea) 3. 6.7kg/Packing-Pallet Box(1ea) 4. Cushion Material : EPS 5. Packing Pallet Box Material : SW
Desiccant (Drier)	2ea/LCD	10g/ea, Cobalt-dichloride-free
Pallet	1 Box / Pallet	Pallet weight : 8kg
Packing Direction	Vertical	-
Total Pallet Size	H x V x height	1150mm x 985mm x 1203mm
Total Pallet Weight	209.42kg	Pallet(8kg) + Module(36 x 5 = 180kg) + Cushion(14kg) + Pallet-BOX(6.7kg) + Desiccant(0.02kg x 36 = 0.72kg)

13. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

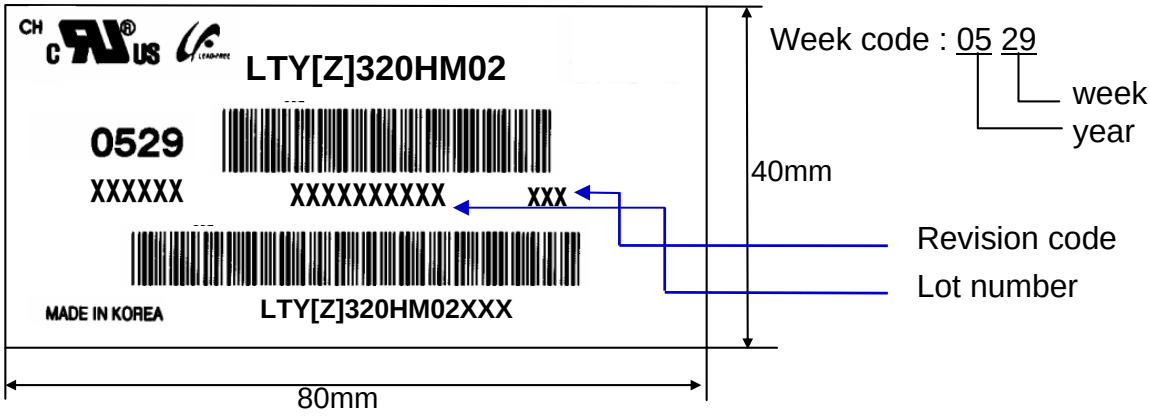
(1) Parts number : LTY[Z]320HM02-XXX

(2) Revision: One letters

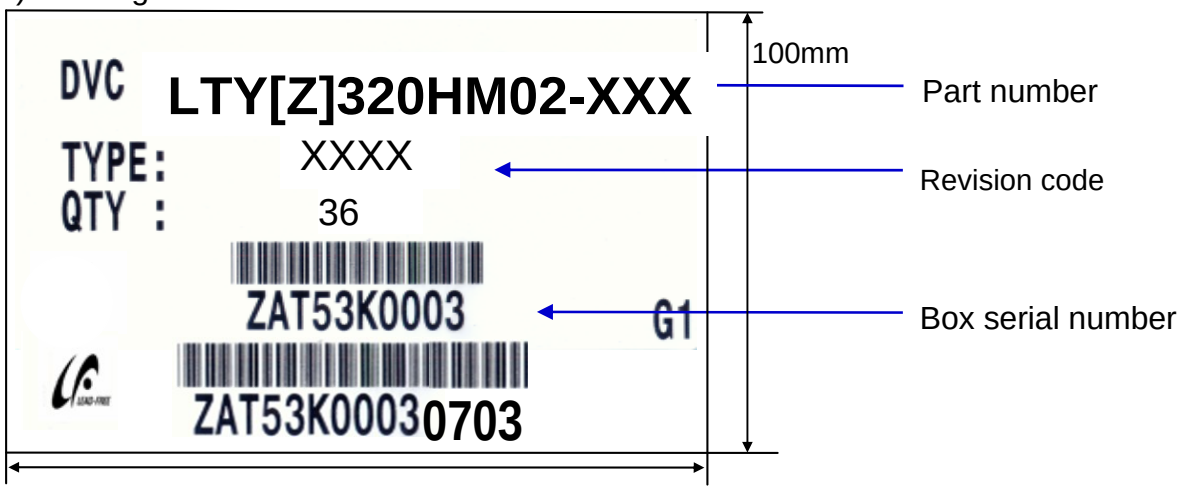
(3) Lot number : X X X X XXX XX X

Cell Position No. (In the Glass)
Glass No. (In the one Lot)
Lot No. (Glass)
Month
Year (Note1)
Product code
Line

(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

14. General Precautions

14.1 Handling

(a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.

(b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.

(c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.

(d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.

(e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.

(f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.

(g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.

(i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.

(j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(k) Do not disassemble the Module.

(l) Do not pull or fold the lamp wire.

(m) Do not adjust the variable resistor located on the Module.

(n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.

(o) Pins of I/F connector should not be touched directly with bare hands.

14.2 Storage

(a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35℃ and relative humidity of less than 70%.

(b) Do not store the TFT-LCD Module in direct sunlight.

(c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

14.3 Operation

(a) No Connection or disconnect the Module in the "Power On" condition.

(b) Power supply should always be turned on/off by the "Power on/off sequence"

(c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

(d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

14.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
- Temperature : 20±15℃
- Humidity : 55±20%
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc.., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

14.5 Others

(a) Ultra-violet ray filter is necessary for outdoor operation.

(b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.

(c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.

(d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.

(e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

(f) Please contact SEC in advance when you display the same pattern for a long time.