

Note(1) Pin number starts from Right side

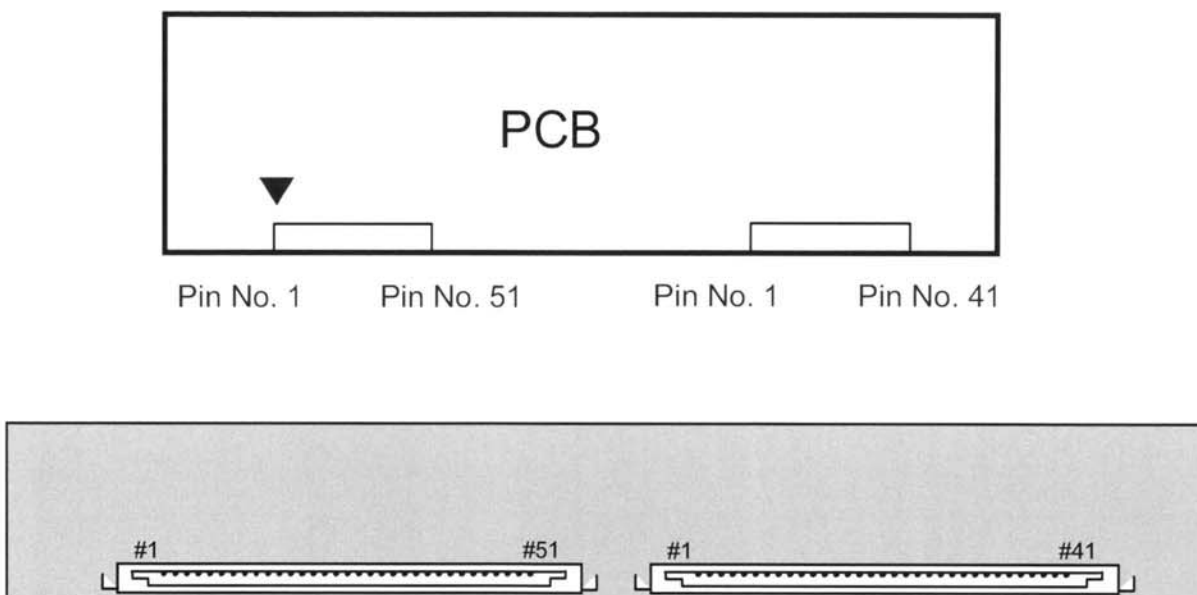


Fig. Connector diagram

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

5.2. Balance Board

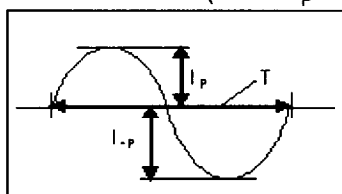
5.2.1 Recommended Operation Condition

Item	Symbol	Recommendation			Unit	Note	Remark
		Min.	Typ.	Max.			
Inverter Frequency	F_{OP}	53	55	57	kHz		Switching Frequency
Dimming Frequency	F_{DIM}	150	160	170	Hz		
Dim Duty Ratio	D_{PWM}	20	-	100	%		Bright Control
Striking Voltage	HV_{STRIKE}	4200	-	-	Vrms	(1)	

Note

Asymmetric ratio of Total Input Current must be less than 10 % ($|I_P - I_{-P}| / (I_{rms@T} < 0.1)$)

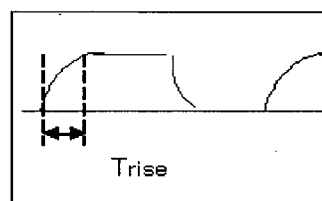
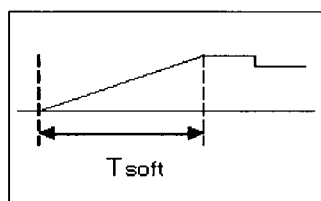
Crest factor must be from 90 % to 110 % ($0.9 < I_P / I_{rms@T/2 \times \sqrt{2}} < 1.1$)



(1) Striking Voltage(HV_{STRIKE}) based on CCFL spec. for ambient temperature.

Soft rising time must be

- at starting time $T_{soft} > 300\text{msec}$
- at PWM dimming condition $T_{rise} < 100\text{usec}$



5.2.2 Balance Board Input Pin Configuration

1. HV Input Connector : MD51SU-2P-13V(Hirose)

PIN NO.	SYMBOL	REMARK
1	HV1	Power Supply for CCFL
2	HV2	Power Supply for CCFL

2. Feed Back Interface : KN30-7P-1.25H(Hirose)

PIN NO.	SYMBOL	REMARK
1	Vcc	12V
2	FB	Feed Back
3	GND	GND
4,5,6,7	LD	Lamp Detection

3. HV Input Connector : BM03-XASS-TF(LF)(SN)(JST)

PIN NO.	SYMBOL	REMARK
1	HV1	Power Supply for CCFL
2	NC	NC
3	HV1	Power Supply for CCFL

4. HV Input Connector : BM04B-XASS-TF(LF)(SN)(JST)

PIN NO.	SYMBOL	REMARK
1	HV2	Power Supply for CCFL
2	NC	NC
3	HV2	Power Supply for CCFL
4	NC	NC

5. Left Right Interface Connector : KN30-10P-1.25H (Hirose)

PIN NO.		SYMBOL	REMARK
Left	Right		
10,9	1,2	GND	Ground
8,7	3,4	VCC	12V
6,5,	5,6	PROT	Protect Interface
4,3,2,1	7,8,9,10	LOOP	JIN Coil Loop

5.2.3 Feedback I/O Specification

ITEM		SYMBOL	Recommendation			UNIT	REMARKS
			Min.	Typ.	Max.		
Supply Voltage		V_{CC}	11	12	15	V	Lamp
Input current Of V_{CC}		I_{OC}	-	-	20	mA	at Recommended Load Condition
Lamp Detection	High (Normal)	V_{LD}	$V_{CC}-1$	-	-	V	@ $V_{CC} = 12 [V]$
	Low (LD)		-	-	1		



5.3 LVDS Interface

	LVDS pin	Odd Data	Even Data
TxOUT/RxIN0	TxIN/RxOUT0	R4	R4
	TxIN/RxOUT1	R5	R5
	TxIN/RxOUT2	R6	R6
	TxIN/RxOUT3	R7	R7
	TxIN/RxOUT4	R8	R8
	TxIN/RxOUT6	R9	R9
	TxIN/RxOUT7	G4	G4
TxOUT/RxIN1	TxIN/RxOUT8	G5	G5
	TxIN/RxOUT9	G6	G6
	TxIN/RxOUT12	G7	G7
	TxIN/RxOUT13	G8	G8
	TxIN/RxOUT14	G9	G9
	TxIN/RxOUT15	B4	B4
	TxIN/RxOUT18	B5	B5
TxOUT/RxIN2	TxIN/RxOUT19	B6	B6
	TxIN/RxOUT20	B7	B7
	TxIN/RxOUT21	B8	B8
	TxIN/RxOUT22	B9	B9
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R2
	TxIN/RxOUT5	R3	R3
	TxIN/RxOUT10	G2	G2
	TxIN/RxOUT11	G3	G3
	TxIN/RxOUT16	B2	B2
	TxIN/RxOUT17	B3	B3
	TxIN/RxOUT23	RESERVED	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0	R0
	TxIN/RxOUT29	R1	R1
	TxIN/RxOUT30	G0	G0
	TxIN/RxOUT31	G1	G1
	TxIN/RxOUT32	B0	B0
	TxIN/RxOUT33	B1	B1
	TxIN/RxOUT34	RESERVED	RESERVED

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5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY	DATA SIGNAL																								GRAY SCALE LEVEL								
		RED									GREEN									BLUE														
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3		B4	B5	B6	B7	B8	B9		
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-		
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-		
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1			
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-	
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	-	
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0		
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	G1023	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	B1023	

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

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6. Interface Timing

6.1 Timing Parameters (DE only mode)

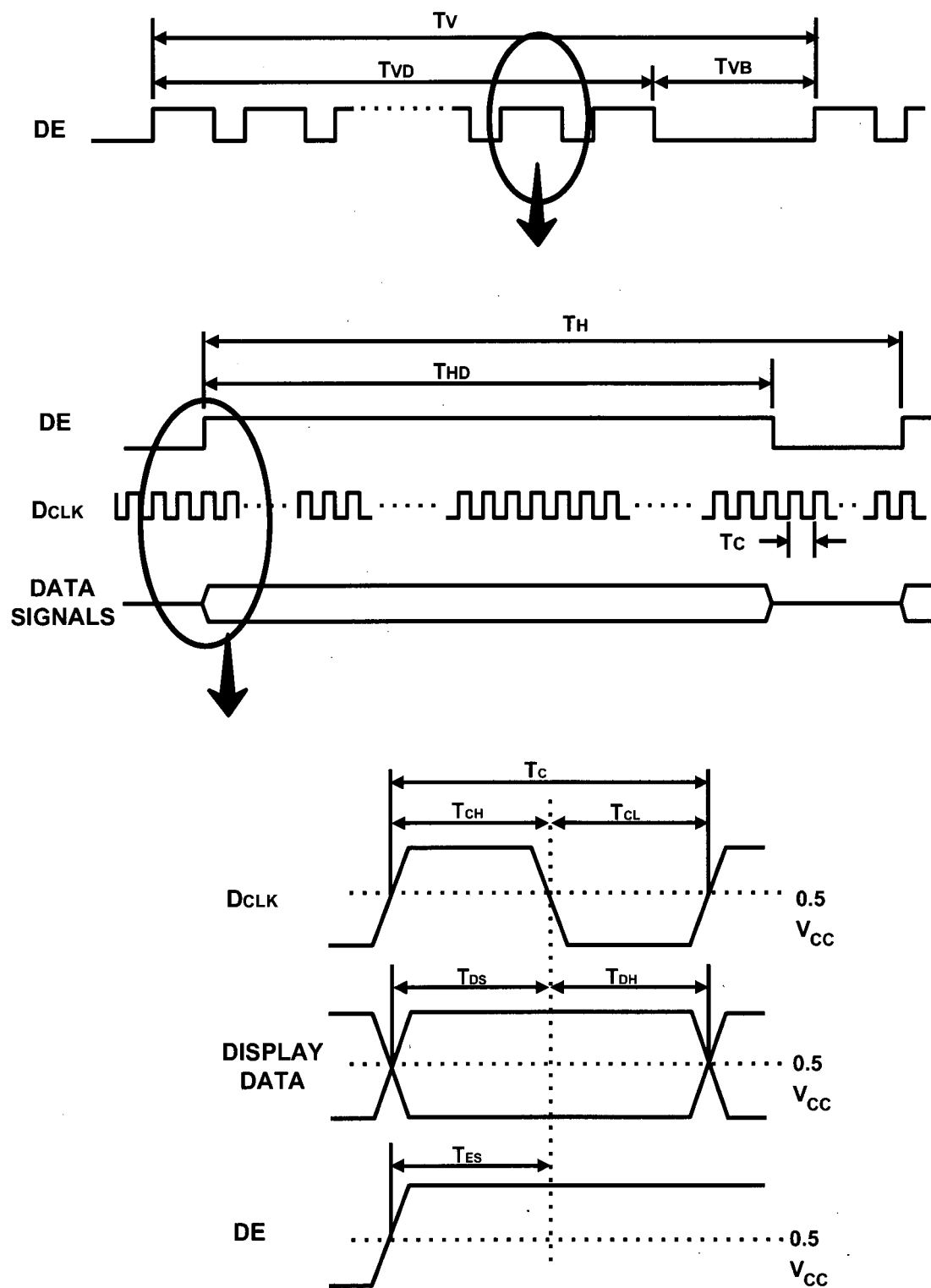
SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	280	297	300	MHz	-
Hsync		F_H	108	135	137	KHz	-
Vsync		F_V	92	120	123	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	lines	-
	Vertical Total	T_{VB}	1086	1125	1480	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	clocks	-
	Horizontal Total	T_H	2056	2200	2616	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

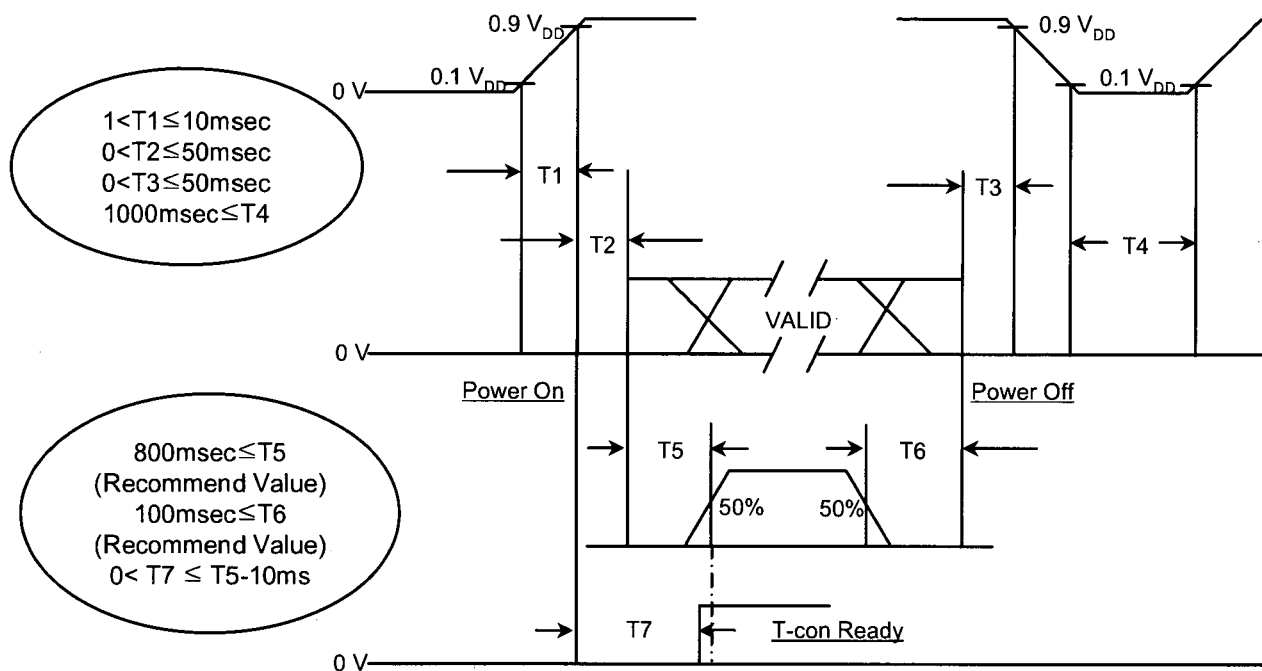
(2) Internal $V_{DD} = 3.3V$

6.2 Timing diagrams of interface signal (DE only mode)



6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%
 T2 : The time from V_{DD} to valid data at power ON.
 T3 : The time from valid data off to V_{DD} off at power Off.
 T4 : V_{DD} off time for Windows restart
 T5 : The time from valid data to B/L enable at power ON.
 T6 : The time from valid data off to B/L disable at power Off.
 T7 : The time from Vin to T-Con Ready

[Valid Data Condition]

1. Input LVDS signals must satisfy "Interface Timing" Specification on p20.
2. LVDS Clock must keep the same frequency.
3. "Temp SEL" signal should not be changed within Tcon Reset and Tcon Ready.
4. Data signal should not input during "Fail Safe Mode".

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

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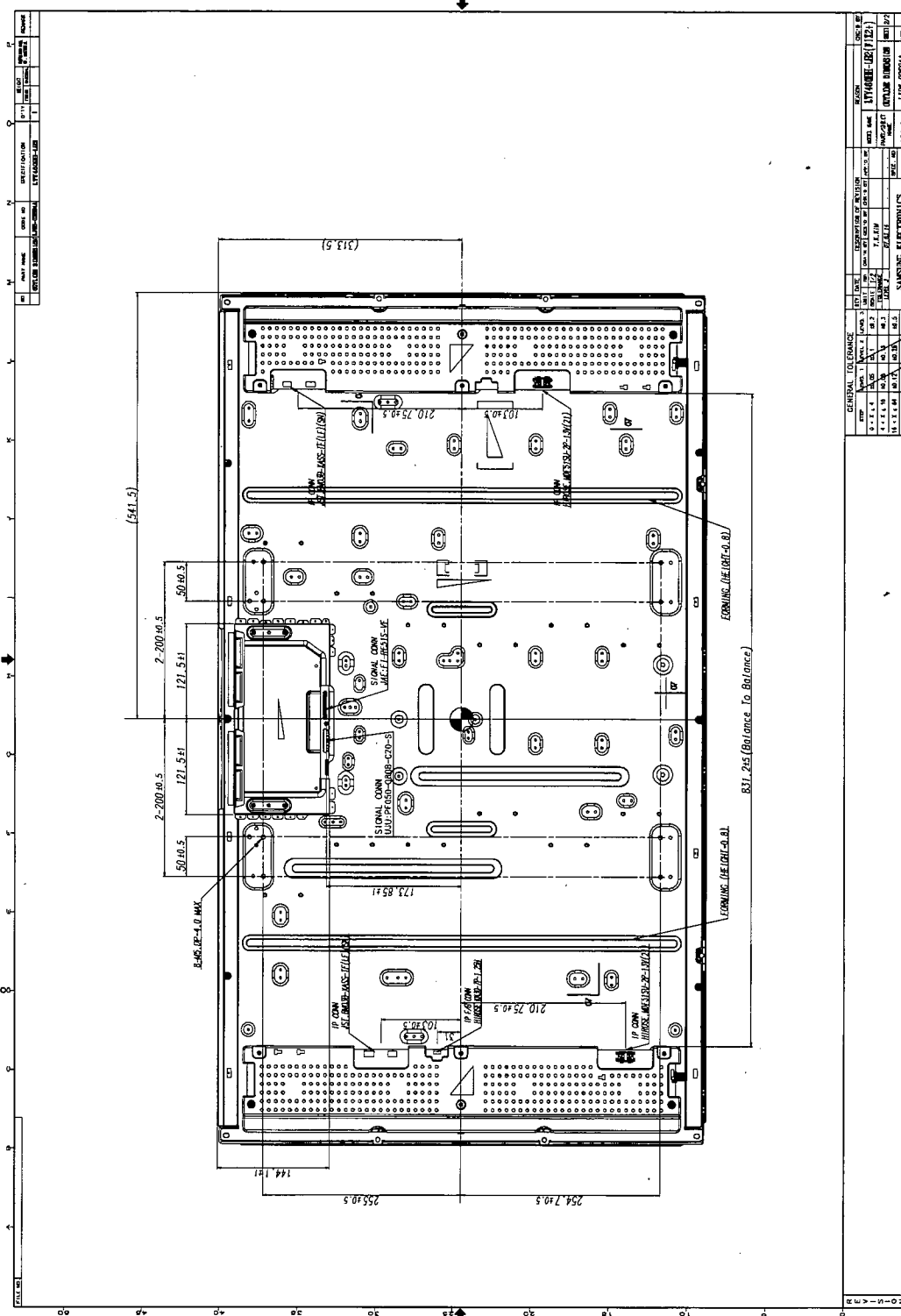
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7. Outline dimension (Front view)

Outline dimension (Rear View)



GENERAL TOLERANCE										REASON		CNC # 07																																																																																																																																																																																																																																																																																																																																													
STEP	WPS 1	WPS 2	WPS 3	WPS 4	WPS 5	WPS 6	WPS 7	WPS 8	WPS 9	WPS 10	WPS 11	WPS 12	WPS 13	WPS 14	WPS 15	WPS 16	WPS 17	WPS 18	WPS 19	WPS 20	WPS 21	WPS 22	WPS 23	WPS 24	WPS 25	WPS 26	WPS 27	WPS 28	WPS 29	WPS 30	WPS 31	WPS 32	WPS 33	WPS 34	WPS 35	WPS 36	WPS 37	WPS 38	WPS 39	WPS 40	WPS 41	WPS 42	WPS 43	WPS 44	WPS 45	WPS 46	WPS 47	WPS 48	WPS 49	WPS 50	WPS 51	WPS 52	WPS 53	WPS 54	WPS 55	WPS 56	WPS 57	WPS 58	WPS 59	WPS 60	WPS 61	WPS 62	WPS 63	WPS 64	WPS 65	WPS 66	WPS 67	WPS 68	WPS 69	WPS 70	WPS 71	WPS 72	WPS 73	WPS 74	WPS 75	WPS 76	WPS 77	WPS 78	WPS 79	WPS 80	WPS 81	WPS 82	WPS 83	WPS 84	WPS 85	WPS 86	WPS 87	WPS 88	WPS 89	WPS 90	WPS 91	WPS 92	WPS 93	WPS 94	WPS 95	WPS 96	WPS 97	WPS 98	WPS 99	WPS 100	WPS 101	WPS 102	WPS 103	WPS 104	WPS 105	WPS 106	WPS 107	WPS 108	WPS 109	WPS 110	WPS 111	WPS 112	WPS 113	WPS 114	WPS 115	WPS 116	WPS 117	WPS 118	WPS 119	WPS 120	WPS 121	WPS 122	WPS 123	WPS 124	WPS 125	WPS 126	WPS 127	WPS 128	WPS 129	WPS 130	WPS 131	WPS 132	WPS 133	WPS 134	WPS 135	WPS 136	WPS 137	WPS 138	WPS 139	WPS 140	WPS 141	WPS 142	WPS 143	WPS 144	WPS 145	WPS 146	WPS 147	WPS 148	WPS 149	WPS 150	WPS 151	WPS 152	WPS 153	WPS 154	WPS 155	WPS 156	WPS 157	WPS 158	WPS 159	WPS 160	WPS 161	WPS 162	WPS 163	WPS 164	WPS 165	WPS 166	WPS 167	WPS 168	WPS 169	WPS 170	WPS 171	WPS 172	WPS 173	WPS 174	WPS 175	WPS 176	WPS 177	WPS 178	WPS 179	WPS 180	WPS 181	WPS 182	WPS 183	WPS 184	WPS 185	WPS 186	WPS 187	WPS 188	WPS 189	WPS 190	WPS 191	WPS 192	WPS 193	WPS 194	WPS 195	WPS 196	WPS 197	WPS 198	WPS 199	WPS 200	WPS 201	WPS 202	WPS 203	WPS 204	WPS 205	WPS 206	WPS 207	WPS 208	WPS 209	WPS 210	WPS 211	WPS 212	WPS 213	WPS 214	WPS 215	WPS 216	WPS 217	WPS 218	WPS 219	WPS 220	WPS 221	WPS 222	WPS 223	WPS 224	WPS 225	WPS 226	WPS 227	WPS 228	WPS 229	WPS 230	WPS 231	WPS 232	WPS 233	WPS 234	WPS 235	WPS 236	WPS 237	WPS 238	WPS 239	WPS 240	WPS 241	WPS 242	WPS 243	WPS 244	WPS 245	WPS 246	WPS 247	WPS 248	WPS 249	WPS 250	WPS 251	WPS 252	WPS 253	WPS 254	WPS 255	WPS 256	WPS 257	WPS 258	WPS 259	WPS 260	WPS 261	WPS 262	WPS 263	WPS 264	WPS 265	WPS 266	WPS 267	WPS 268	WPS 269	WPS 270	WPS 271	WPS 272	WPS 273	WPS 274	WPS 275	WPS 276	WPS 277	WPS 278	WPS 279	WPS 280	WPS 281	WPS 282	WPS 283	WPS 284	WPS 285	WPS 286	WPS 287	WPS 288	WPS 289	WPS 290	WPS 291	WPS 292	WPS 293	WPS 294	WPS 295	WPS 296	WPS 297	WPS 298	WPS 299	WPS 300	WPS 301	WPS 302	WPS 303	WPS 304	WPS 305	WPS 306	WPS 307	WPS 308	WPS 309	WPS 310	WPS 311	WPS 312	WPS 313	WPS 314	WPS 315	WPS 316	WPS 317	WPS 318	WPS 319	WPS 320	WPS 321	WPS 322	WPS 323	WPS 324	WPS 325	WPS 326	WPS 327	WPS 328	WPS 329	WPS 330	WPS 331	WPS 332	WPS 333	WPS 334	WPS 335	WPS 336	WPS 337	WPS 338	WPS 339	WPS 340	WPS 341	WPS 342	WPS 343	WPS 344	WPS 345
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8. EMI Recommendation

: -3dB at CISPR22 Class B

This EMI Recommendation is recommended to be measured at SET Condition.

9. Input Spread Spectrum Specification

	Modulation Rate (Max.)	Modulation Frequency (Min.)	Modulation Frequency (Max.)
Input	±1.2%	50KHz	200Khz

10. UL Approval

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