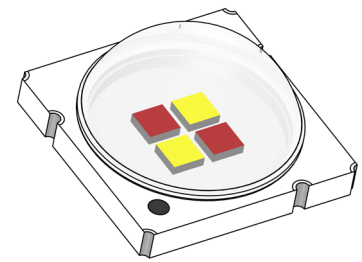


LED ENGIN LuxiGen®

LZ4-00RW08

Industry's most robust high power ceramic package with glass lens for high performance over life.



Applications

- Access Control & Security

Features

- Package: Ceramic package with integrated glass lens
- Chip technology: IR Thinfilm / UX:3
- Typ. Radiation: 90°
- Color: $\lambda_{\text{peak}} = 940 \text{ nm}$ (• infrared (940 nm)); $C_x = 0.335$, $C_y = 0.340$ acc. to CIE 1931 (• white)

Ordering Information

Type	Brightness ¹⁾	Ordering Code
LZ4-00RW08-R755		Q65113A0525
• infrared (940 nm)	• $\Phi_E = 1.6 \dots 2.4 \text{ W}$ ($I_F = 700 \text{ mA}$)	
• white	• $\Phi_V = 356 \dots 556 \text{ lm}$ ($I_F = 700 \text{ mA}$)	

Maximum Ratings

Parameter	Symbol		Values	
			● infrared (940 nm)	● white
Operating Temperature	T_{op}	min.	-40 °C	-40 °C
		max.	145 °C	145 °C
Storage Temperature	T_{stg}	min.	-40 °C	-40 °C
		max.	145 °C	145 °C
Junction Temperature ²⁾	T_j	max.	145 °C	145 °C
Forward Current ²⁾	I_F	max.	1500 mA	1500 mA
Forward Current pulsed infrared: $t \leq 150 \mu s$; $D \leq 0.1$; $T_C = 25 \text{ °C}$; white: $t \leq 10 \text{ ms}$; $D \leq 0.1$; $T_C = 25 \text{ °C}$	$I_{F \text{ pulse}}$	max.	5000 mA	1500 mA
ESD withstand voltage acc. ANSI/ESDA/JEDEC JS-001 (HBM, Class 0)	V_{ESD}		ESD sensitive device	ESD sensitive device
Reverse voltage ³⁾	V_R		Not designed for reverse operation	Not designed for reverse operation

Characteristics

$I_F = 700 \text{ mA}$; $T_C = 25 \text{ °C}$; all chips operated in series

Parameter	Symbol		Values	Values
			● infrared (940 nm)	● white
Chromaticity Coordinate ⁴⁾	C_x	typ.		0.335
	C_y	typ.		0.340
Total radiant flux ⁵⁾	Φ_E	typ.	1.9 W	
Luminous Flux ⁵⁾	Φ_V	typ.		480 lm
Peak Wavelength ⁶⁾ $I_F = 700 \text{ mA}$	λ_{peak}	min.	920 nm	
		typ.	940 nm	
		max.	960 nm	
Viewing angle at 50% I_V	2φ	typ.	95 °	95 °
Forward Voltage ⁷⁾⁵⁾ $I_F = 700 \text{ mA}$	V_F	min.	5.40 V	5.60 V
		typ.	5.60 V	6.90 V
		max.	7.40 V	7.60 V
Reverse current ³⁾	I_R		Not designed for reverse operation	Not designed for reverse operation
Electrical thermal resistance junction/case Value in the first column relates to full package with all chips operated simultaneously.	$R_{\text{thJC elec.}}$	typ.	2.8 K / W	

Brightness Groups

all chips operated in series

- infrared (940 nm)

Group	Total radiant flux ¹⁾ $I_F = 700 \text{ mA}$ min. Φ_E	Total radiant flux ¹⁾ $I_F = 700 \text{ mA}$ max. Φ_E
PQ	1.6 W	2.4 W

Brightness Groups

all chips operated in series

- white

Group	Luminous Flux ¹⁾ $I_F = 700 \text{ mA}$ min. Φ_V	Luminous Flux ¹⁾ $I_F = 700 \text{ mA}$ max. Φ_V
ST	356 lm	556 lm

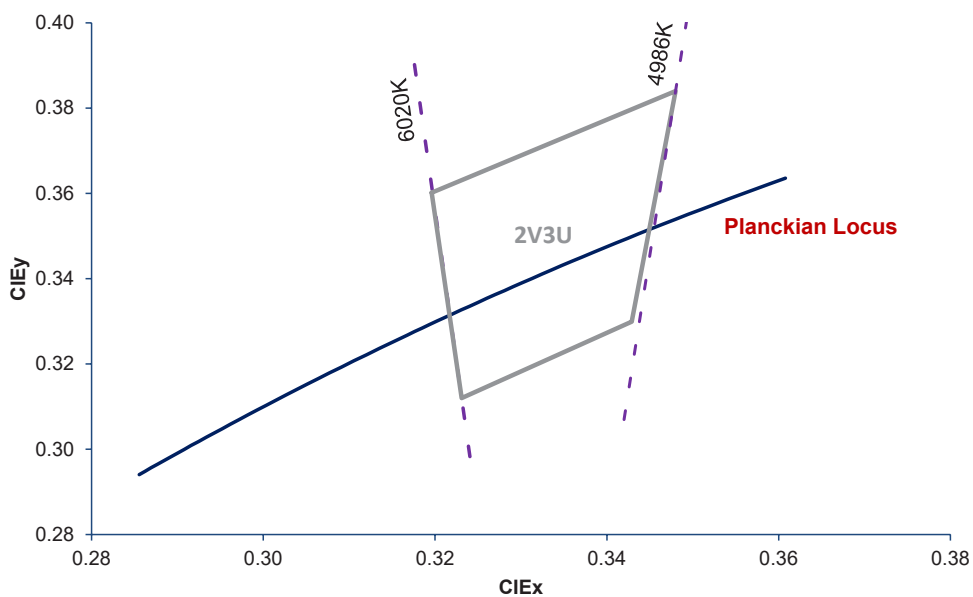
Wavelength Groups

all chips operated in series

- infrared (940 nm)

Group	Peak Wavelength ⁶⁾ $I_F = 700 \text{ mA}$ min. λ_{peak}	Peak Wavelength ⁶⁾ $I_F = 700 \text{ mA}$ max. λ_{peak}
F09	920 nm	960 nm

Chromaticity Coordinate Groups



Chromaticity Coordinate Groups

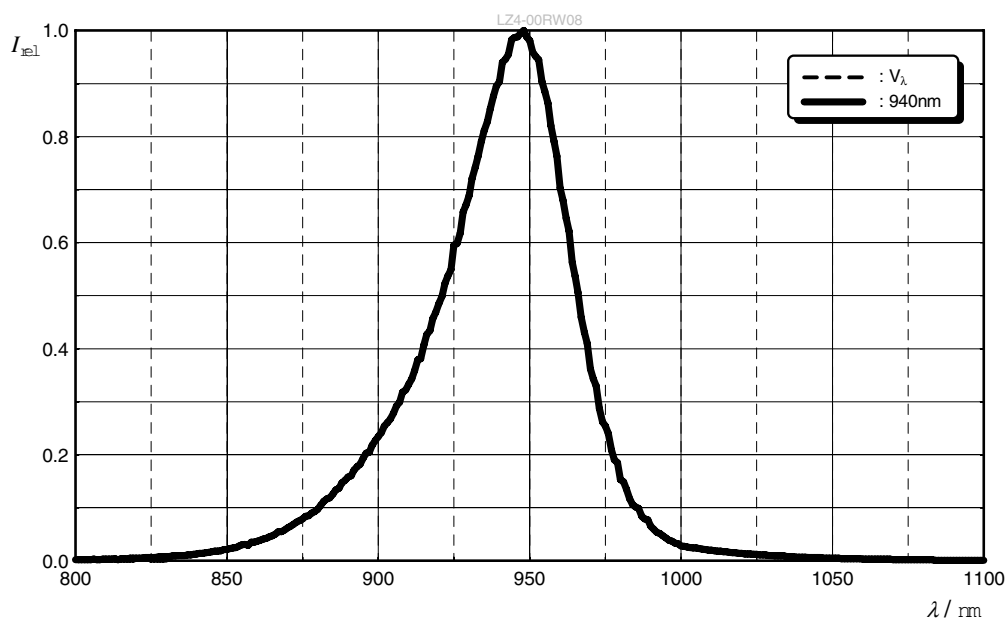
all chips operated in series

- white

Group	Cx	Cy	CCT
2V3U	0.3196	0.3602	
	0.3480	0.3840	
	0.3429	0.3299	
	0.3231	0.3120	

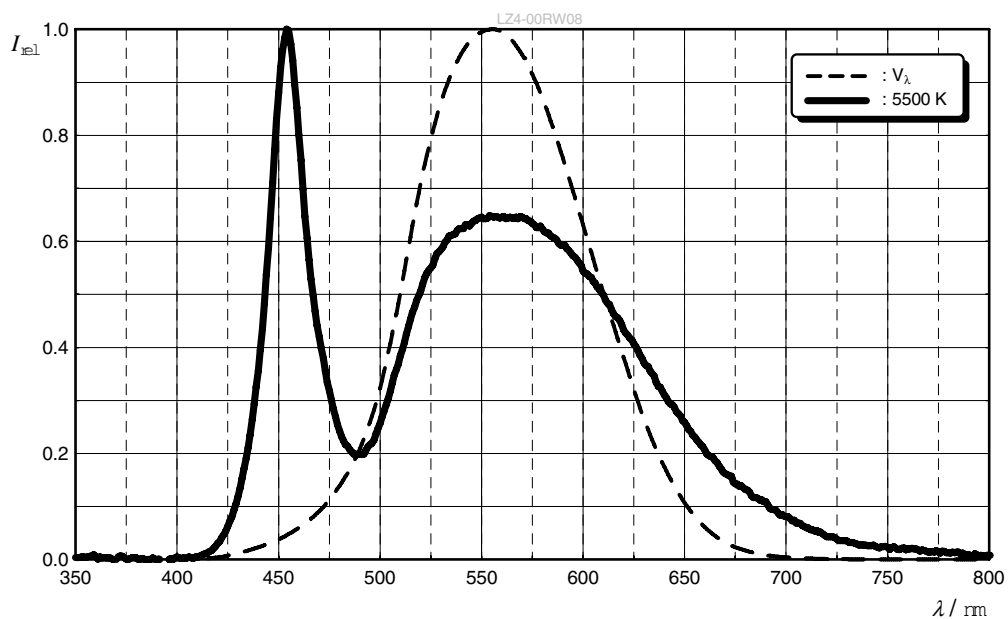
Relative Spectral Emission ⁵⁾

$I_{rel} = f(\lambda)$; $I_F = 700 \text{ mA}$; $T_C = 25 \text{ }^\circ\text{C}$



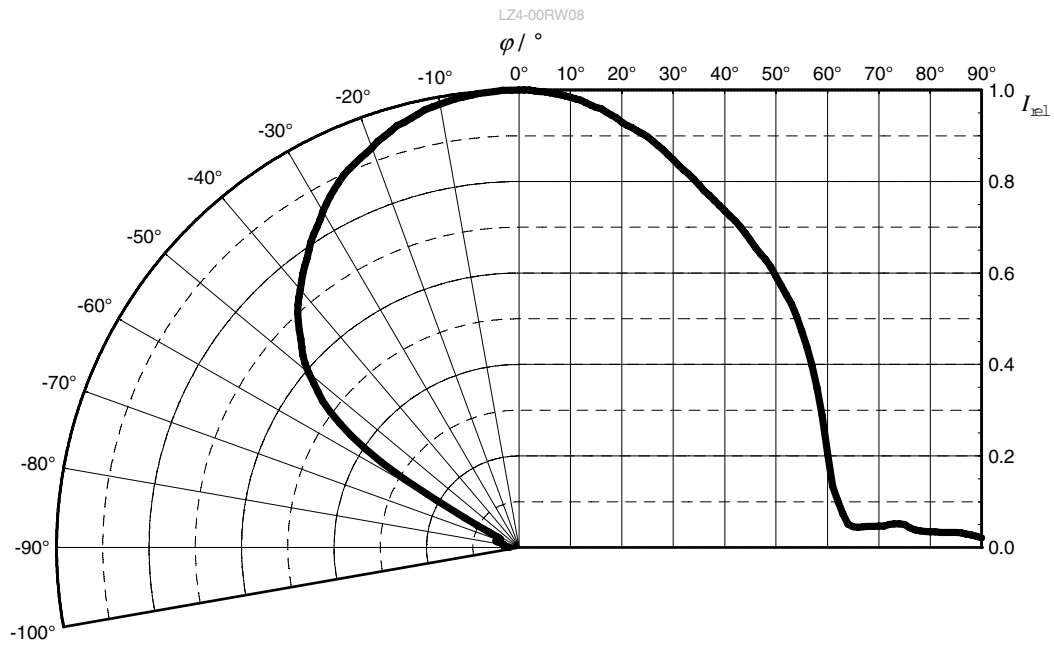
Relative Spectral Emission ⁵⁾

$I_{rel} = f(\lambda)$; $I_F = 700 \text{ mA}$; $T_C = 25 \text{ }^\circ\text{C}$



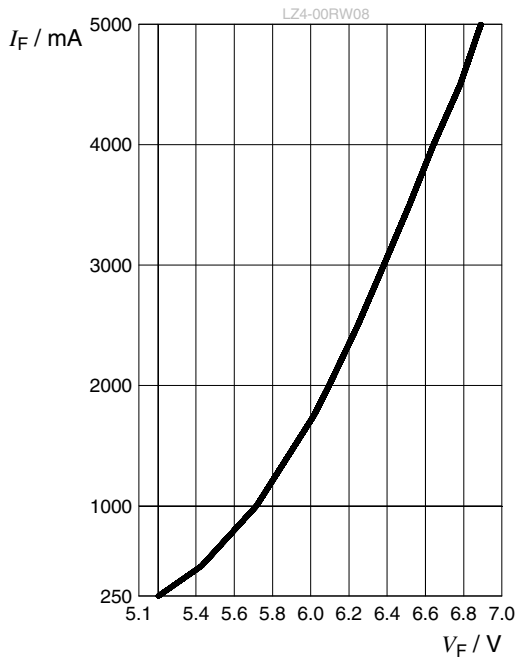
Radiation Characteristics ⁵⁾

$I_{rel} = f(\phi); T_C = 25\text{ °C}$



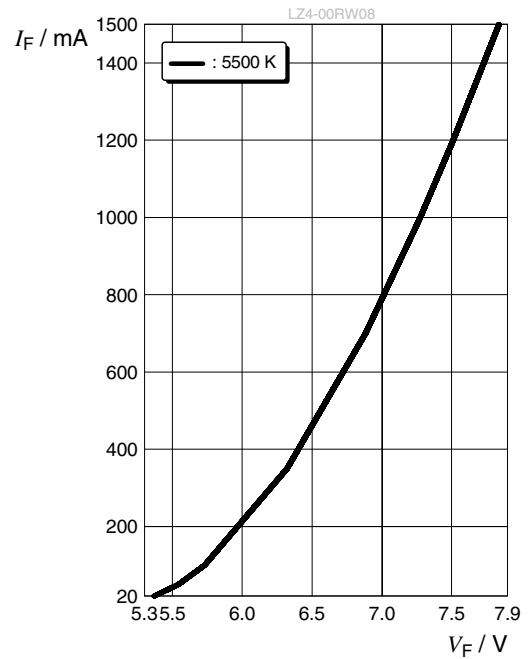
Forward current ⁵⁾

$I_F = f(V_F); T_C = 25\text{ °C}$



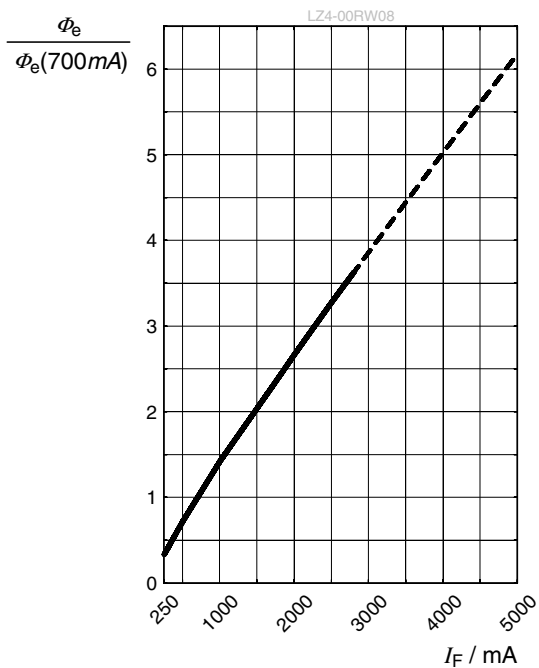
Forward current ⁵⁾

$I_F = f(V_F); T_C = 25\text{ °C}$



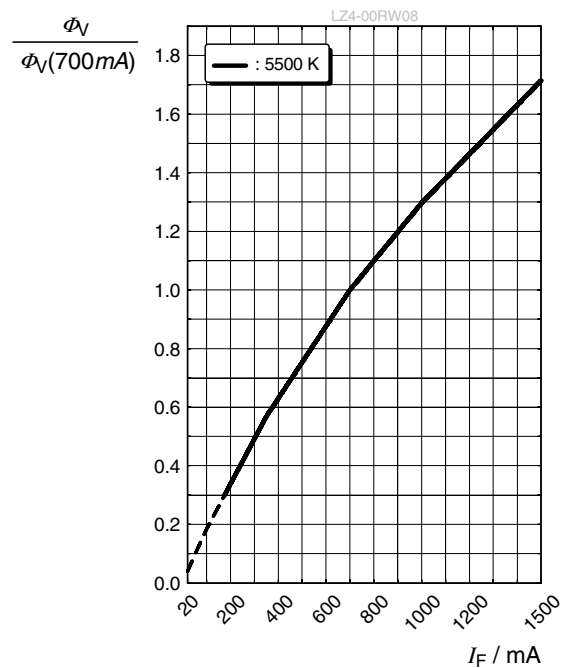
Relative Radiant Power ^{5), 8)}

$\Phi_E / \Phi_E(700\text{ mA}) = f(I_F); T_C = 25\text{ °C}$



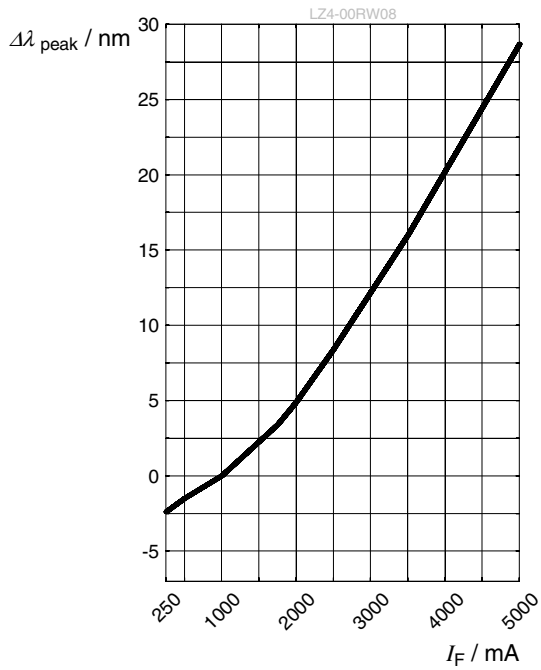
Relative Luminous Flux ^{5), 8)}

$\Phi_V / \Phi_V(700\text{ mA}) = f(I_F); T_C = 25\text{ °C}$



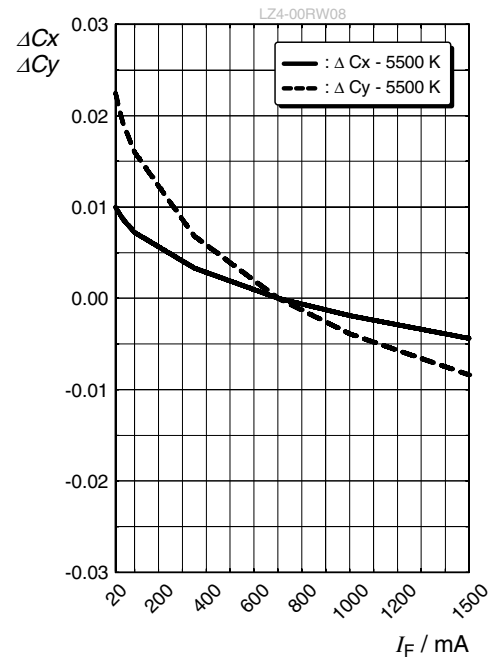
Peak Wavelength ⁵⁾

$$\Delta\lambda_{\text{peak}} = f(I_F); T_C = 25\text{ }^\circ\text{C}$$



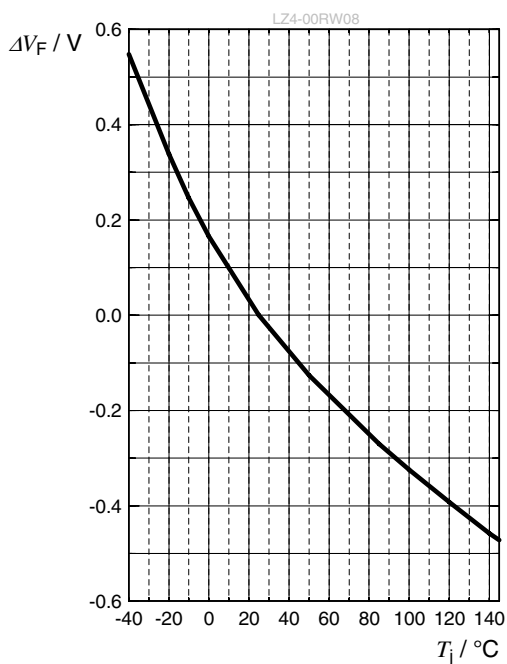
Chromaticity Coordinate Shift ⁵⁾

$$\Delta C_x, \Delta C_y = f(I_F); T_C = 25\text{ }^\circ\text{C}$$



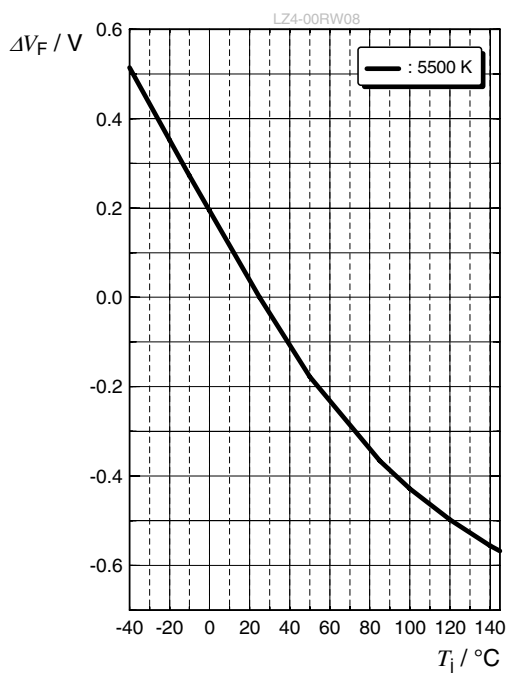
Forward Voltage ⁵⁾

$$\Delta V_F = V_F - V_F(25^\circ\text{C}) = f(T_j); I_F = 700\text{ mA}$$



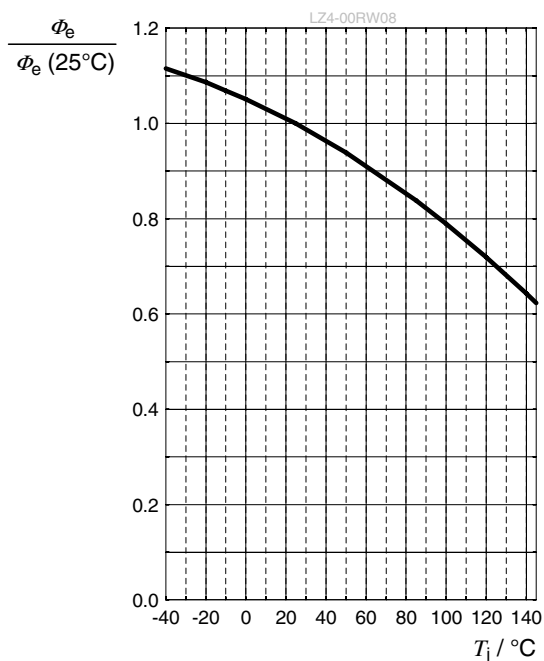
Forward Voltage ⁵⁾

$$\Delta V_F = V_F - V_F(25^\circ\text{C}) = f(T_j); I_F = 700\text{ mA}$$



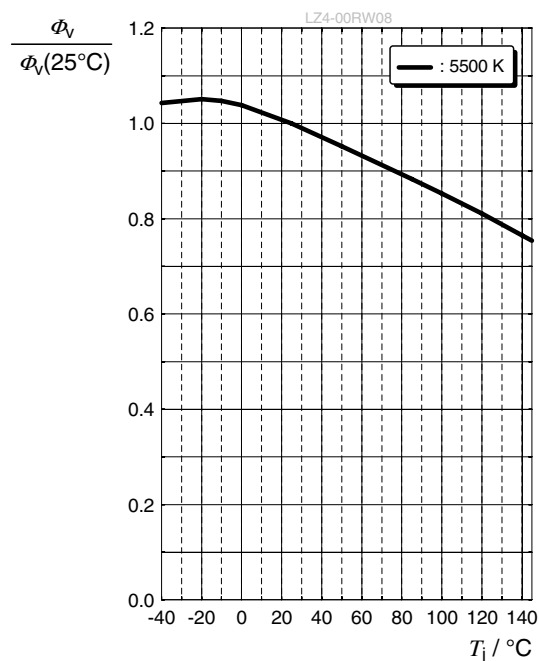
Relative Radiant Power ⁵⁾

$$\Phi_E / \Phi_E(25^\circ\text{C}) = f(T_j); I_F = 700\text{ mA}$$



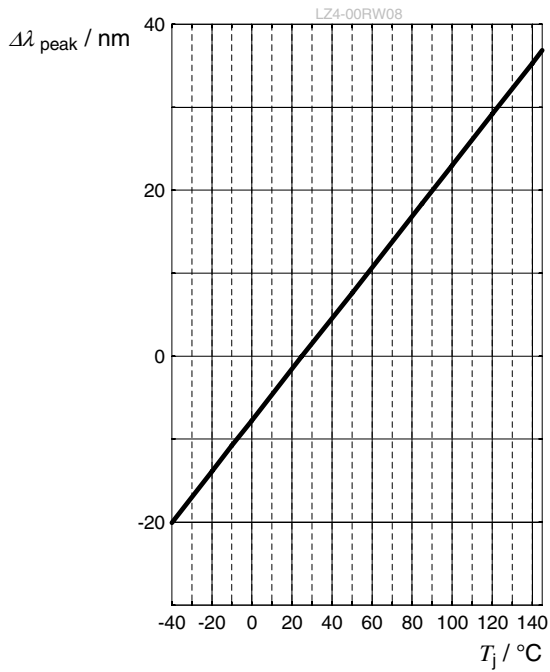
Relative Luminous Flux ⁵⁾

$$\Phi_V / \Phi_V(25^\circ\text{C}) = f(T_j); I_F = 700\text{ mA}$$



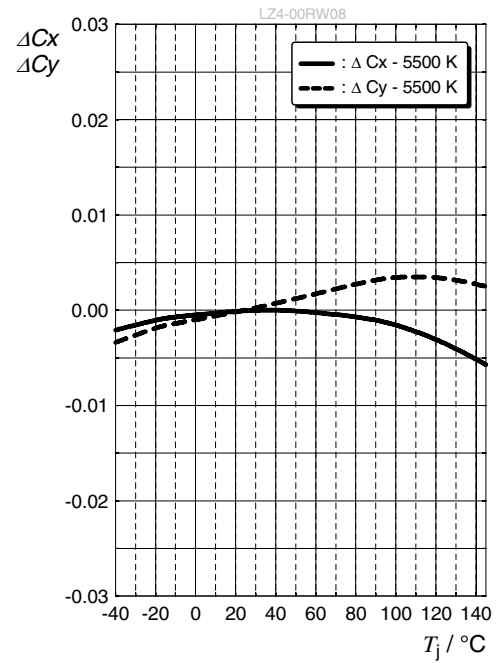
Peak Wavelength ⁵⁾

$$\Delta\lambda_{\text{peak}} = \lambda_{\text{peak}} - \lambda_{\text{peak}}(25\text{ °C}) = f(T_j); I_F = 700\text{ mA}$$



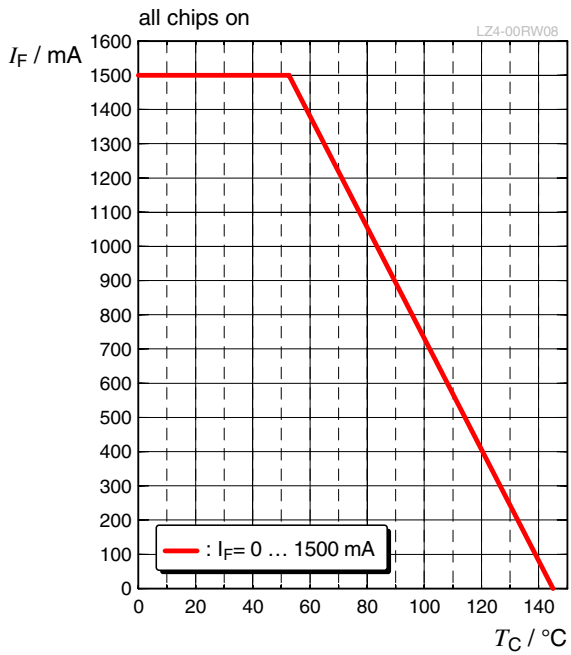
Chromaticity Coordinate Shift ⁵⁾

$$\Delta Cx, \Delta Cy = f(T_j); I_F = 700\text{ mA}$$

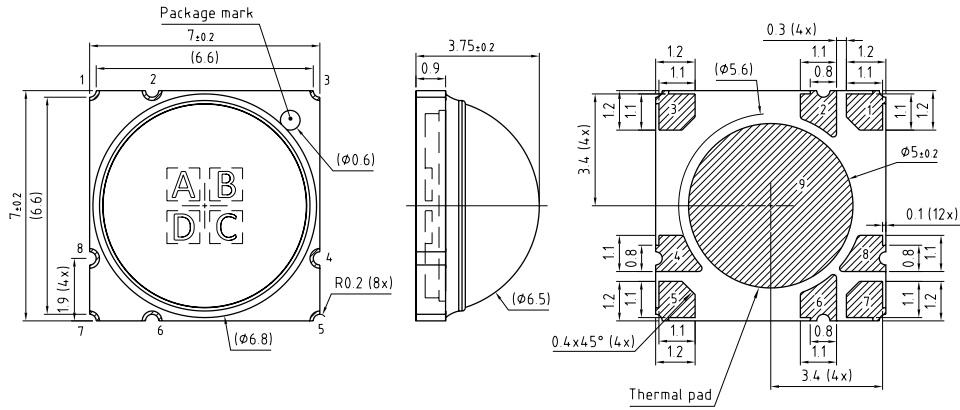


Max. Permissible Forward Current ⁹⁾

$I_F = f(T)$; • infrared (940 nm)



Dimensional Drawing ¹⁰⁾



General tolerance ± 0.1

Pad finish Gold 

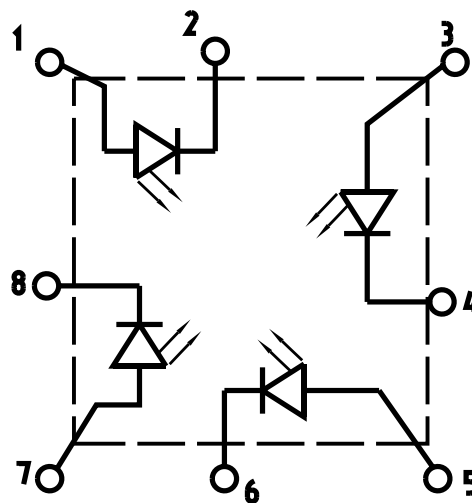
C67062-A0375-A18.-.03

Further Information:

Approximate Weight: 263.0 mg

Electrical Internal Circuit

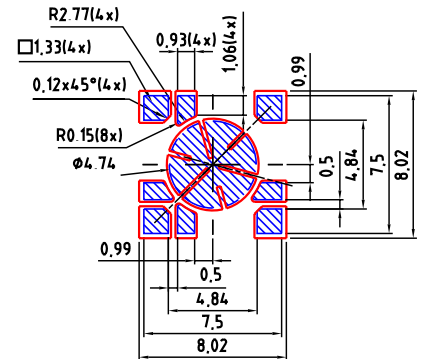
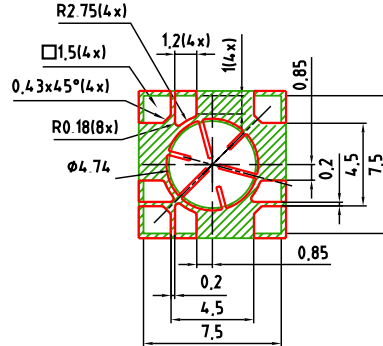
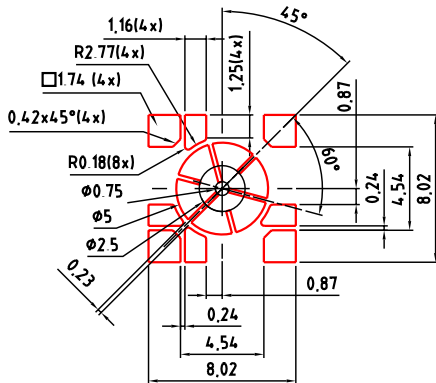
Pin Out		
Pad	Die	Function
1	A	Anode
2	A	Cathode
3	B	Anode
4	B	Cathode
5	C	Anode
6	C	Cathode
7	D	Anode
8	D	Cathode
9	n/a	Thermal



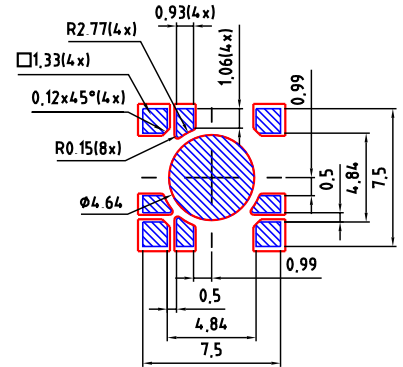
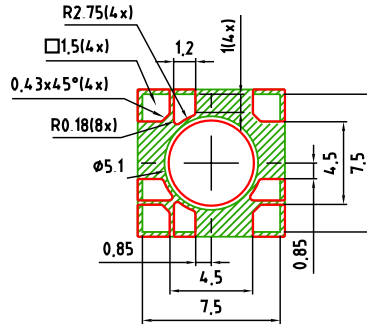
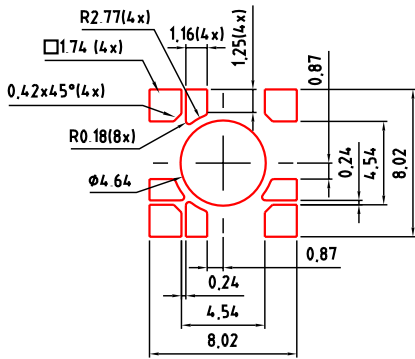
Pin	Description
1, 2	Die A - infrared (940 nm)
3, 4	Die B - white
5, 6	Die C - infrared (940 nm)
7, 8	Die D - white

Recommended Solder Pad ¹⁰⁾

Non-pedestal MCPCB Design



Pedestal MCPCB Design

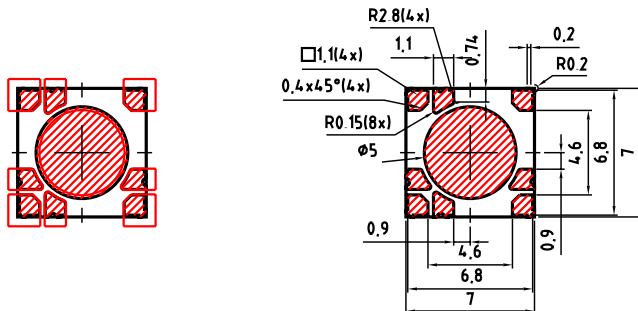


foot print

solder resist

solder stencil
recommended stencil
thickness 200µm

Component Location on Pad

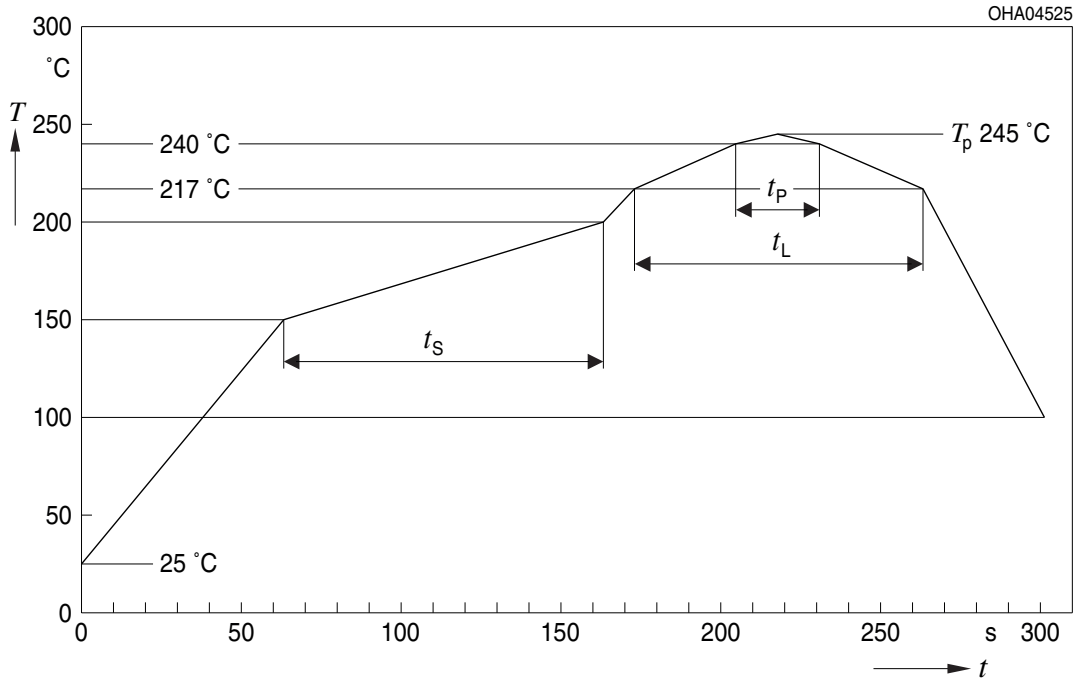


E062.3010.291-01

1. For superior solder joint connectivity results we recommend soldering under standard nitrogen atmosphere.
2. Package not suitable for ultra sonic cleaning.
3. Pedestal MCPCB allows the emitter thermal slug to be soldered directly to the metal core of the MCPCB. Such MCPCB eliminate the high thermal resistance dielectric layer that standard MCPCB technologies use in between the emitter thermal slug and the metal core of the MCPCB, thus lowering the overall system thermal resistance.
4. X-ray sample monitoring for solder voids underneath the emitter thermal slug is recommended. The total area covered by solder voids should be less than 20% of the total emitter thermal slug area. Excessive solder voids will increase the emitter to MCPCB thermal resistance and may lead to higher failure rates due to thermal over stress.

Reflow Soldering Profile

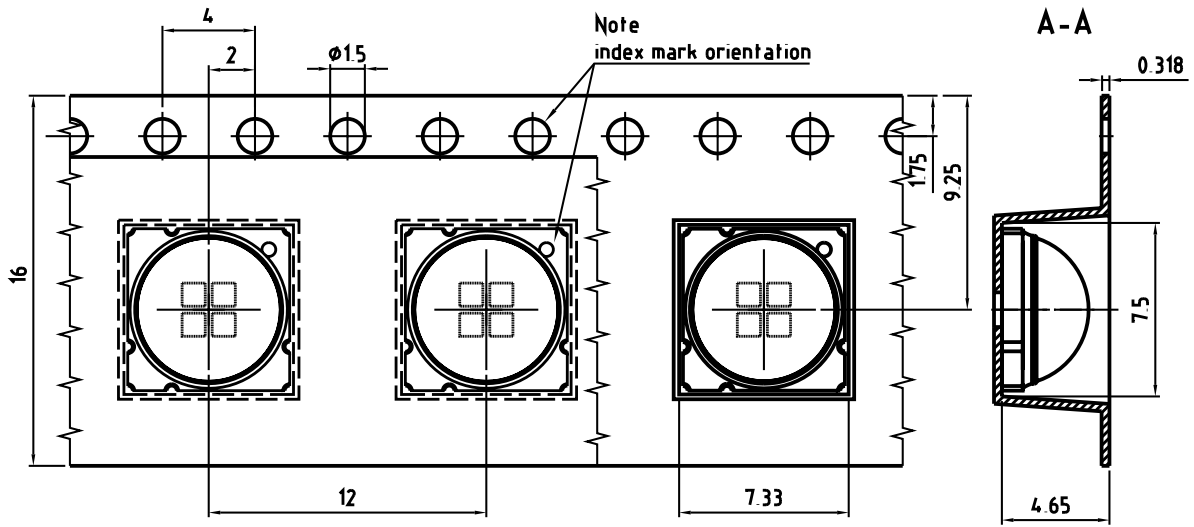
Product complies to MSL Level 1 acc. to JEDEC J-STD-020E



Profile Feature	Symbol	Pb-Free (SnAgCu) Assembly			Unit
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat ^{*)} 25 °C to 150 °C			2	3	K/s
Time t_s T_{Smin} to T_{Smax}	t_s	60	100	120	s
Ramp-up rate to peak ^{*)} T_{Smax} to T_p			2	3	K/s
Liquidus temperature	T_L		217		°C
Time above liquidus temperature	t_L		80	100	s
Peak temperature	T_p		245	250	°C
Time within 5 °C of the specified peak temperature $T_p - 5$ K	t_p	10	20	30	s
Ramp-down rate* T_p to 100 °C			3	4	K/s
Time 25 °C to T_p				480	s

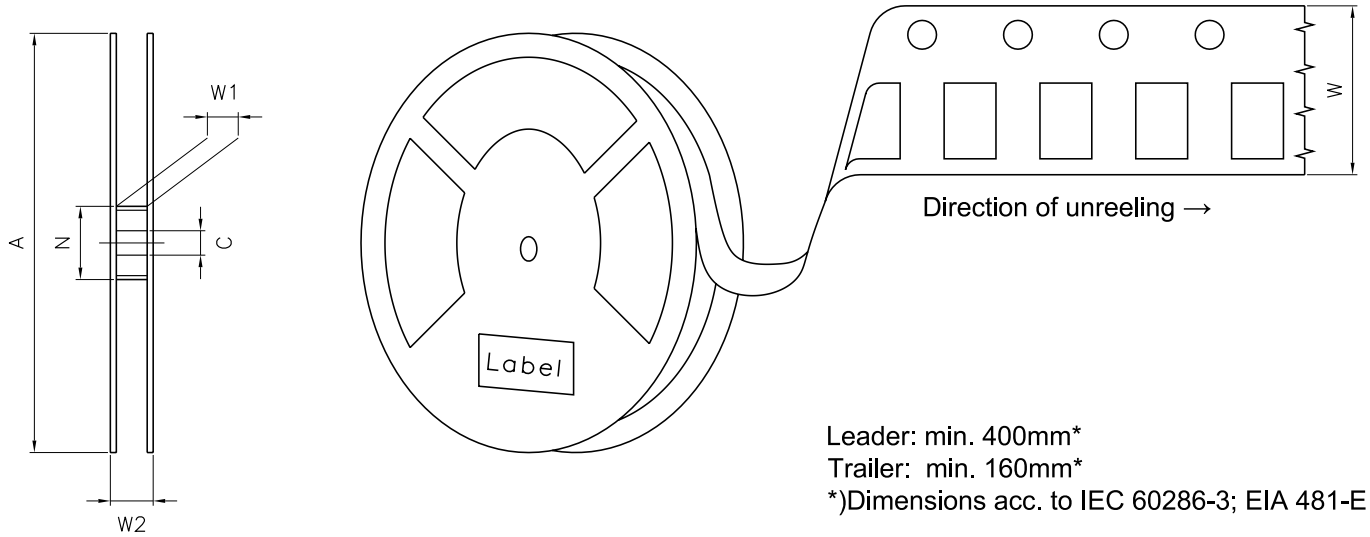
All temperatures refer to the center of the package, measured on the top of the component
 *) slope calculation DT/Dt : Dt max. 5 s; fulfillment for the whole T-range

Taping ¹⁰⁾



C67062-A0375-B4-01


Tape and Reel ¹¹⁾




Reel Dimensions

A	W	N _{min}	W ₁	W _{2max}	Pieces per PU
180 mm	16 + 0.3 / - 0.1 mm	60/100 mm	16.4 + 2 mm	22.4 mm	250


Barcode-Product-Label (BPL)

OSRAM
Opto Semiconductors
Our Brand 
LED ENGINE

LXX-XXXXX BIN1: XXX-X-X-XXX
XxxXxx BIN2: XXX-X-X-XXX
RoHS Compliant BIN3: XX-X-X-XXX
BIN4: XXX-XXX-X-XXX
BIN5: X-XX-X-XXX
BIN6: X-XX-X-XXX

(6P) Batch No: 1234567890 ML TEMP ST
(1T) Lot No: 1234567890 X XXX° X 

(X) Prod No: 12345678
(9D) D/C : 1234 Pack: RXX
(Q) Qty: 9999 B_X123_12345.1234
CoO: XX 001



Notes

The evaluation of eye safety occurs according to the standard IEC 62471:2006 (photo biological safety of lamps and lamp systems). Within the risk grouping system of this IEC standard, the device specified in this data sheet fall into the class **moderate risk (exposure time 0.25 s)**. Under real circumstances (for exposure time, conditions of the eye pupils, observation distance), it is assumed that no endangerment to the eye exists from these devices. As a matter of principle, however, it should be mentioned that intense light sources have a high secondary exposure potential due to their blinding effect. When looking at bright light sources (e.g. headlights), temporary reduction in visual acuity and afterimages can occur, leading to irritation, annoyance, visual impairment, and even accidents, depending on the situation.

Subcomponents of this device contain, in addition to other substances, metal filled materials including silver. Metal filled materials can be affected by environments that contain traces of aggressive substances. Therefore, we recommend that customers minimize device exposure to aggressive substances during storage, production, and use. Devices that showed visible discoloration when tested using the described tests above did show no performance deviations within failure limits during the stated test duration. Respective failure limits are described in the IEC60810.

Tapes and reels are shipped in airtight bags in order to reduce the onset of silver tarnish. We recommend bags only be opened when ready to use emitters. Partially used reels or trays should be stored in airtight bags or in storage purged with nitrogen.

Based on very short life cycle times in chip technology this component is subject to frequent adaption to the latest chip technology.

Changes to the content of this datasheet may occur without further notification. JEDEC 46C constitutes the guideline of the change management for the device specified in this document.

For further application related information please visit <https://ams-osram.com/support/application-notes>

Disclaimer

Attention please!

The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact our Sales Organization.

If printed or downloaded, please find the latest version on our website.

Packing

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office. By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

Product and functional safety devices/applications or medical devices/applications

Our components are not developed, constructed or tested for the application as safety relevant component or for the application in medical devices.

Our products are not qualified at module and system level for such application.

In case buyer – or customer supplied by buyer – considers using our components in product safety devices/ applications or medical devices/applications, buyer and/or customer has to inform our local sales partner immediately and we and buyer and /or customer will analyze and coordinate the customer-specific request between us and buyer and/or customer.

Glossary

- 1) **Brightness:** Brightness groups are tested at a current pulse duration of 10 ms and a tolerance of $\pm 10\%$.
- 2) **Operating Conditions:** Operating conditions according DC-derating (Max. Permissible Forward Current)
- 3) **Reverse Operation:** Not designed for reverse operation. Continuous reverse operation can cause migration and damage of the device.
- 4) **Chromaticity coordinate groups:** Chromaticity coordinate groups are tested at a current pulse duration of 10 ms and a tolerance of ± 0.01 .
- 5) **Typical Values:** Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- 6) **Peak Wavelength:** Wavelengths are tested at a current pulse duration of 10 ms and a tolerance of ± 2 nm.
- 7) **Forward Voltage:** Forward voltages are tested at a current pulse duration of 10 ms and a tolerance of ± 0.1 V.
- 8) **Characteristic curve:** In the range where the line of the graph is broken, you must expect higher differences between single devices within one packing unit.
- 9) **Thermal Resistance:** $R_{th\ max}$ is based on statistic values (6σ) used for Derating.
- 10) **Tolerance of Measure:** Unless otherwise noted in drawing, tolerances are specified with ± 0.1 and dimensions are specified in mm.
- 11) **Tape and Reel:** All dimensions and tolerances are specified acc. IEC 60286-3 and specified in mm.

Revision History

Version	Date	Change
1.1	2021-10-25	New Layout
1.2	2023-11-09	New Layout Applications Dimensional Drawing



EU RoHS and China RoHS compliant product

此产品符合欧盟 RoHS 指令的要求；
按照中国的相关法规和标准，
不含有毒有害物质或元素。

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Tobelbader Strasse 30, 8141 Premstaetten, Austria

Phone +43 3136 500-0

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