www.DataSheet4U.cppSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM1G43,SM1J43

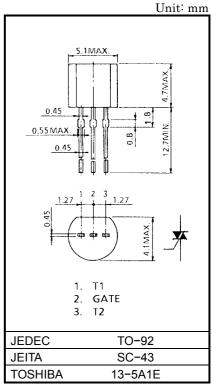
AC POWER CONTROL APPLICATIONS

Repetitive Peak Off-State Voltage : V_{DRM} = 400, 600V
 R.M.S On-State Current : I_T (RMS) = 1A

• Higt Commutating (dv / dt)

MAXIMUM RATINGS

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak	SM1G43	V _{DRM}	400	V	
Off-State Voltage	SM1J43	VDRM	600		
R.M.S On-State Currer (Full Sine Waveform To		I _{T (RMS)}	1.0	А	
Peak One Cycle Surge	On-State	_	8 (50Hz)	Α	
Current (Non-Repetitive)		ITSM	8.8 (60Hz)	A	
I ² t Limit Value		I ² t	0.32	A ² s	
Peak Gate Power Dissi	pation	P _{GM}	1	W	
Average Gate Power D	issipation	P _{G (AV)}	0.1	W	
Peak Gate Voltage		V_{GM}	6	V	
Peak Gate Current		I _{GM}	0.5	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature F	Range	T _{stg}	-40~125	°C	



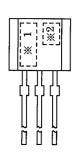
Weight: 0.2g



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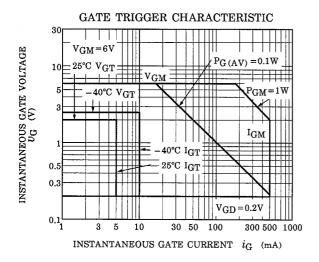
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I _{DRM}	V _{DRM} = Rated		_	_	10	μΑ
Gate Trigger Voltage	I	V _{GT}	$V_D = 12V$, $R_L = 20\Omega$	T2 (+), Gate (+)	_	_	2	V
	Ш			T2 (+) , Gate (-)	_	_	2	
	III			T2 (-) , Gate (-)	_	_	2	
	IV			T2 (-) , Gate (+)	_	2	_	
Gate Trigger Current	I	I _{GT}	V _D = 12V, R _L = 20Ω	T2 (+), Gate (+)	_	_	5	- mA
	Ш			T2 (+) , Gate (-)	_	_	5	
	III			T2 (-) , Gate (-)	_	_	5	
	IV			T2 (-) , Gate (+)	_	10	_	
Peak On-State Voltage		V_{TM}	I _{TM} = 1.5A		_	_	1.5	V
Gate Non-Trigger Voltage		V_{GD}	V _D = Rated, Tc = 125°C		0.2	_	_	V
Holding Current		lΗ	V _D = 12V, I _{TM} = 1A		_	_	10	mA
Thermal Resistance		R _{th (j-c)}	Junction to Case, AC		_	_	40	°C/W
Thermal Resistance		R _{th (j−a)}	Junction to Ambient, AC		_	_	180	°C/W

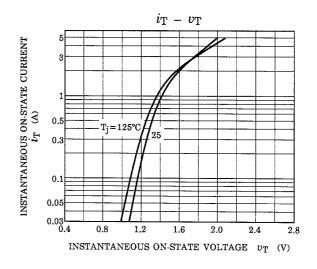
MARKING

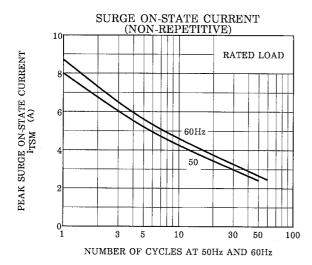


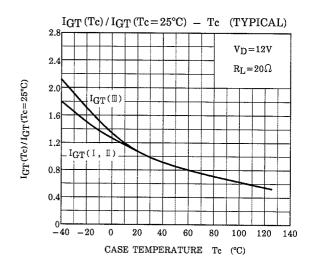
NUMBER		SYMBOL	MARK	
*1	TYPE	SM1G43	M1G43	
		SM1J43	M1J43	
*2	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Current Year)		Example 8A : January 1998 8B : February 1998 8L : December 1998	

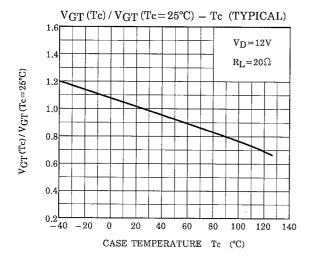
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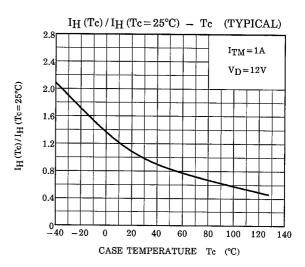




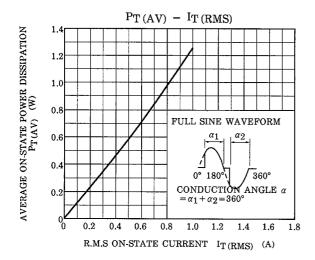


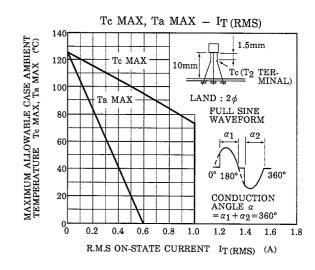


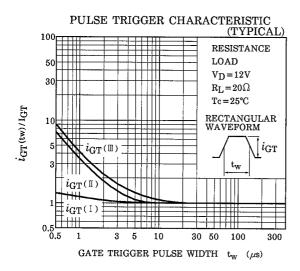


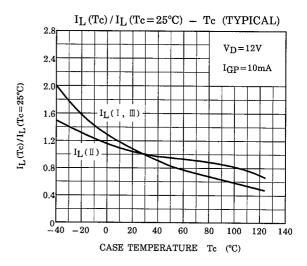


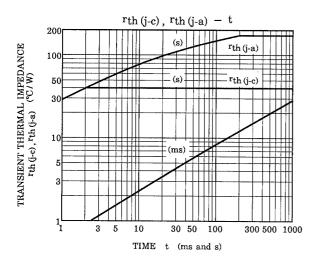
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