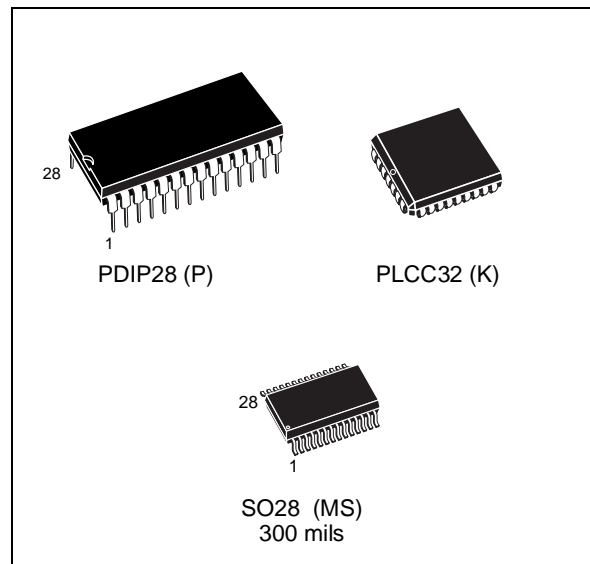


## 16K (2K x 8) PARALLEL EEPROM with SOFTWARE DATA PROTECTION

### DATA BRIEFING

- FAST ACCESS TIME: 90ns
- SINGLE 5V ± 10% SUPPLY VOLTAGE
- LOW POWER CONSUMPTION
- FAST WRITE CYCLE:
  - 64 Bytes Page Write Operation
  - Byte or Page Write Cycle: 3ms Max
- ENHANCED END OF WRITE DETECTION:
  - Ready/Busy Open Drain Output
  - Data Polling
  - Toggle Bit
- PAGE LOAD TIMER STATUS BIT
- HIGH RELIABILITY SINGLE POLYSILICON, CMOS TECHNOLOGY:
  - Endurance >100,000 Erase/Write Cycles
  - Data Retention >40 Years
- JEDEC APPROVED BYTEWIDE PIN OUT
- SOFTWARE DATA PROTECTION



### DESCRIPTION

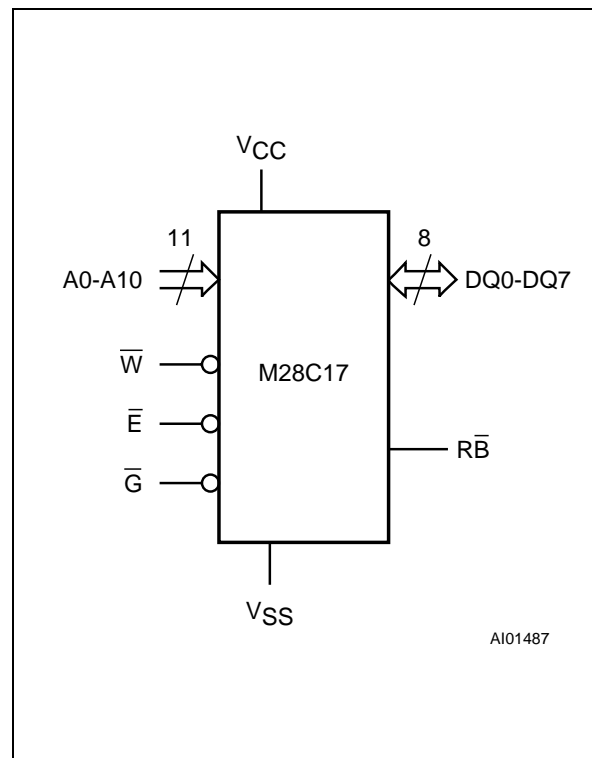
The M28C17 is a 2K x 8 low power Parallel EEPROM fabricated with SGS-THOMSON proprietary single polysilicon CMOS technology. The device offers fast access time with low power dissipation and requires a 5V power supply.

The M28C17 offers the same features than the M28C16, in addition to the Ready/Busy pin.

### Signal Names

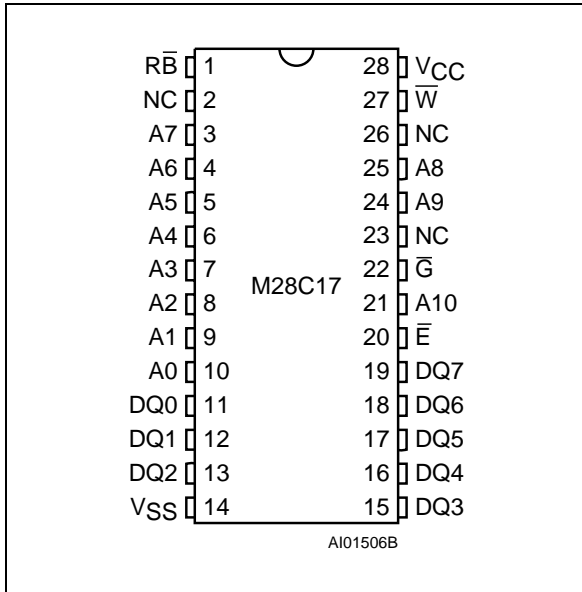
|                  |                     |
|------------------|---------------------|
| A0 - A10         | Address Input       |
| DQ0 - DQ7        | Data Input / Output |
| $\bar{W}$        | Write Enable        |
| $\bar{E}$        | Chip Enable         |
| $\bar{G}$        | Output Enable       |
| $\bar{R}\bar{B}$ | Ready / Busy        |
| V <sub>CC</sub>  | Supply Voltage      |
| V <sub>SS</sub>  | Ground              |

### Logic Diagram



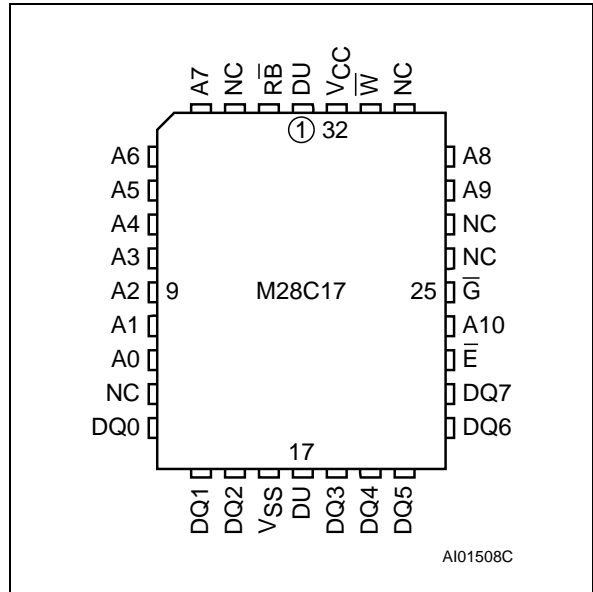
# M28C17

## DIP Pin Connections



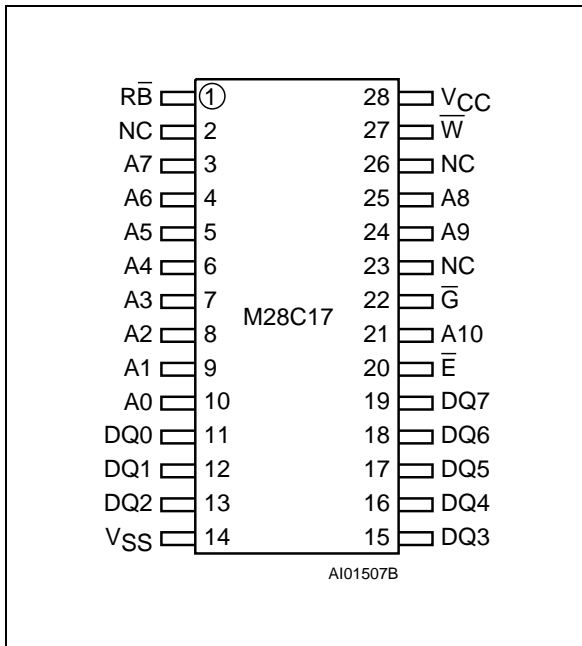
**Warning:** NC = Not Connected.

## LCC Pin Connections



**Warning:** NC = Not Connected, DU = Don't Use.

## SO Pin Connections

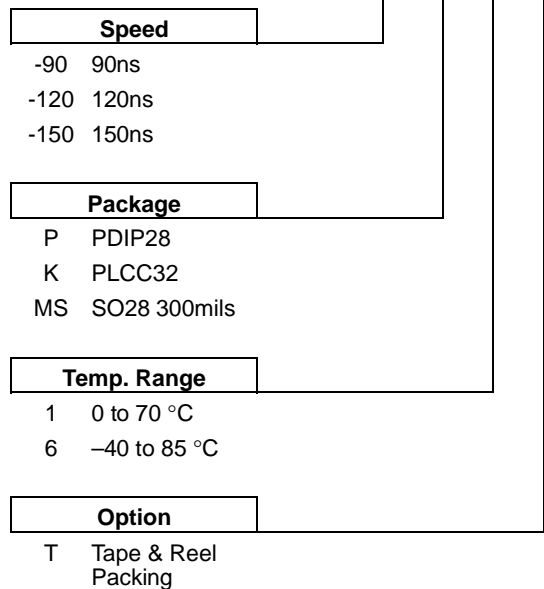


**Warning:** NC = Not Connected.

## Ordering Information Scheme

For a list of available options or for further information on any aspect of this device, please contact the SGS-THOMSON Sales Office nearest to you.

Example: M28C17 -90 K 1 T



**Note:** Devices are shipped from the factory with the memory content set at all "1's" (FFh).