

M51551

DUAL 2-MODE ELECTRONIC SWITCH

DESCRIPTION

The M51551P/M51551FP is an integrated circuit consisting of dual channels two-mode electronic switch. It can select the both channel input signal at the same time by means of forcing DC voltage at control terminal. And it consists of LED driver for displaying the selected input mode. M51551P is housed in 14-pin plastic molded DIL package. M51551FP is housed in 14-pin plastic molded FLAT package.

FEATURES

- Dual channel two-mode electronic switch
- Mode changeable by means of forcing DC voltage
- Built-in output circuit for mode display
- Low distortion transmission characteristics because of bipolar process 0.006%(typ.)
- Single power supply and dual power supply are available for power supply
- Positive logic control

APPLICATION

Signal switch, stereo radio cassette recorder, radio receiver

RECOMMENDED OPERATING CONDITIONS

($T_a=25^{\circ}\text{C}$)

Dual power supply

Operating supply voltage $\pm 6 \sim \pm 10\text{V}$

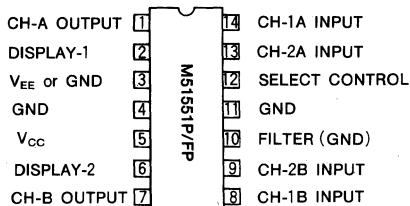
Rated supply voltage $\pm 8\text{V}$

Single power supply

Operating supply voltage $6 \sim 20\text{V}$

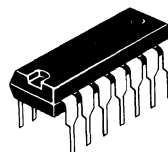
Rated supply voltage 15V

PIN CONFIGURATION (TOP VIEW)

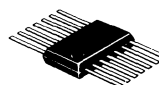


Outline 14P4 M51551P

Outline 14P2 M51551FP

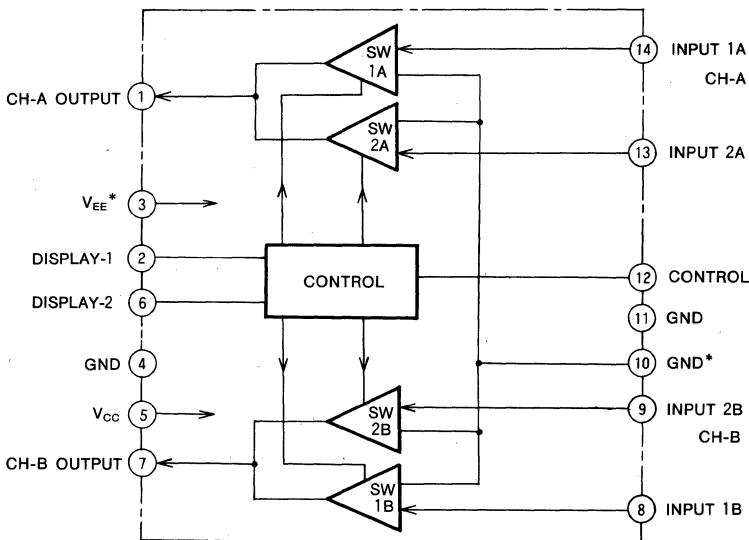


14-pin molded plastic DIL



14-pin molded plastic FLAT

BLOCK DIAGRAM



* : Terminal meaning

	Dual power supply	Single power supply
pin③	V_{EE}	GND
pin⑩	GND	Filter

DUAL 2-MODE ELECTRONIC SWITCH

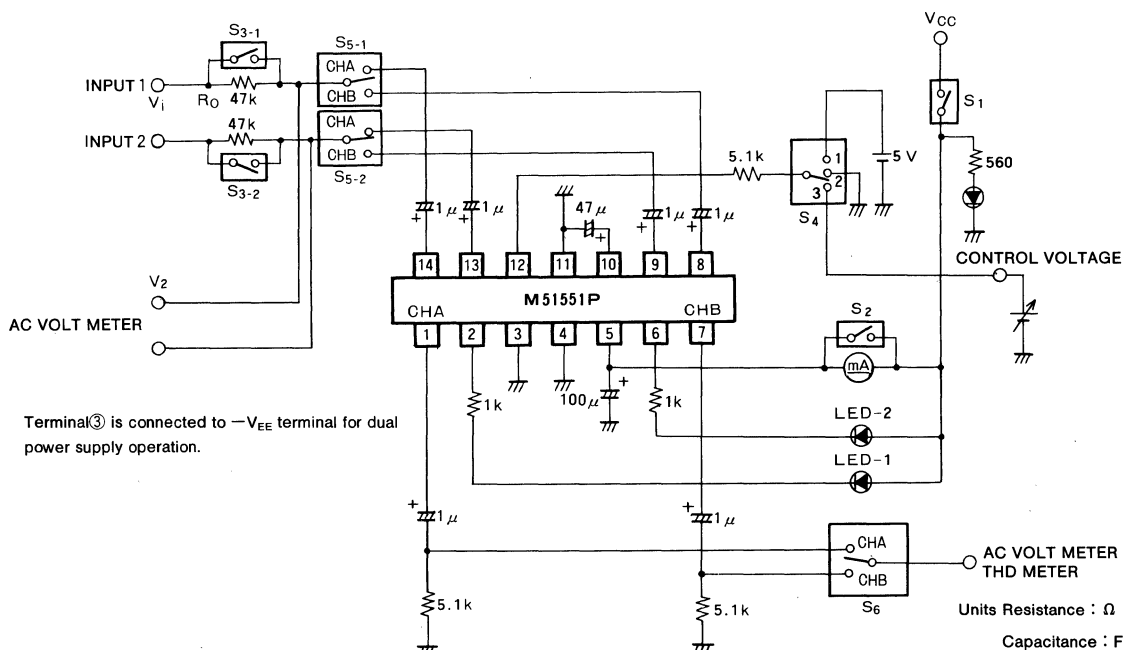
ABSOLUTE MAXIMUM RATINGS (T_a=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Limits	Unit
V _{CC}	Supply voltage	At zero signal (between pin⑤ to ③)	24	V
I _{CC}	Circuit current	Not including pin②, ⑥ current	30	mA
I _{CC} ②, ⑥	Display current	Pin②, ⑥	40	mA
P _d	Power dissipation		620	mW
K _θ	Thermal derating above 25°C	T _a ≥25°C	6.2	mW/°C
T _{opr}	Operating temperature		-20~+75	°C
T _{stg}	Storage temperature		-40~+125	°C

ELECTRICAL CHARACTERISTICS (T_a=25°C, V_{CC}=15V, V_i=1.5Vrms, f=1kHz, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I _{CC}	Circuit current	Not including pin②, ⑥ current		12	16	mA
V _{CNTL1}	Input 1select control voltage	Control voltage when input 1 is selected.	2.0			V
V _{CNTL-2}	Input 2select control voltage	Control voltage when input 2 is selected.			0.7	V
R _i	Input resistance	V _i =1.5Vrms, f=1kHz, R _o =47kΩ	30	47		kΩ
G _v	Voltage gain	V _i =1.5Vrms, f=1kHz	-1	0	1	dB
V _{O(max)}	Maximum output voltage.	Output level as output THD=1%	4.0	4.5		Vrms
THD	Total harmonic distortion	V _o =1.0Vrms		0.006	0.017	%
N _O	Noise output	Zero-signal, input pin short to GND		5.5	10	μVrms
C.T.	Cross talk	Leakage ratio of input 1 to input 2	52	58		dB
C.L.	Channel leakage	Leakage ratio of channel A to channel B	77	83		dB
C.B.	Channel balance	Voltage gain ratio of channel A to channel B	-0.5	0	0.5	dB

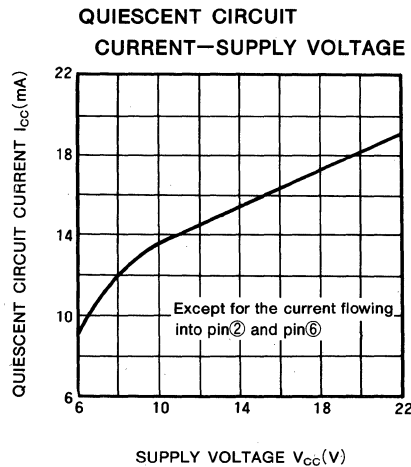
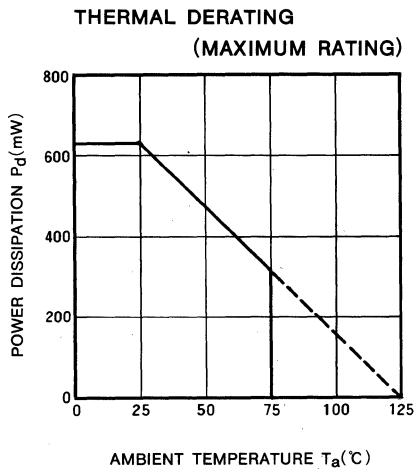
TEST CIRCUIT (for single power supply)



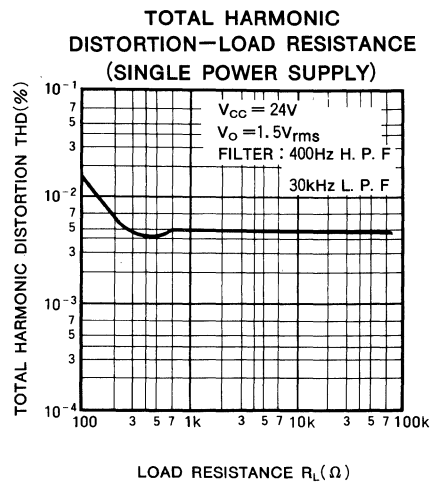
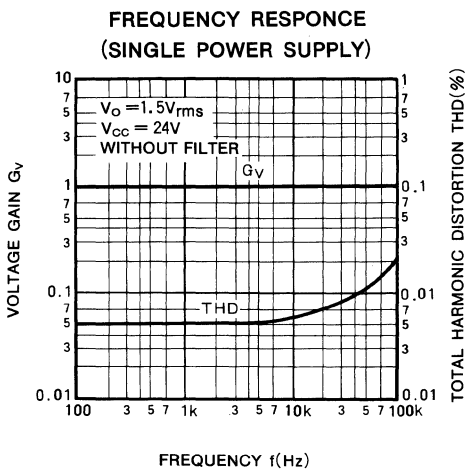
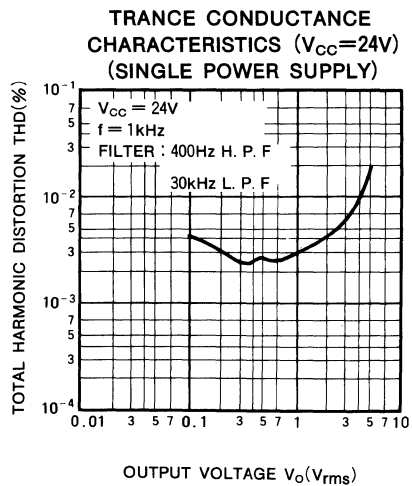
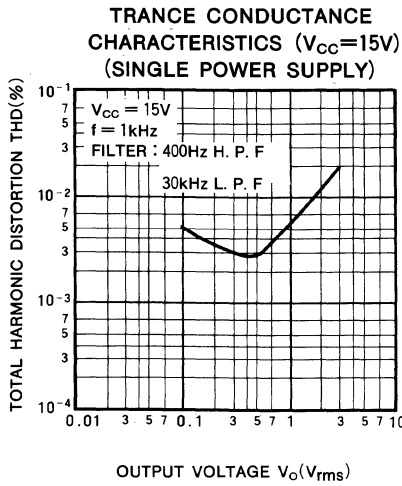
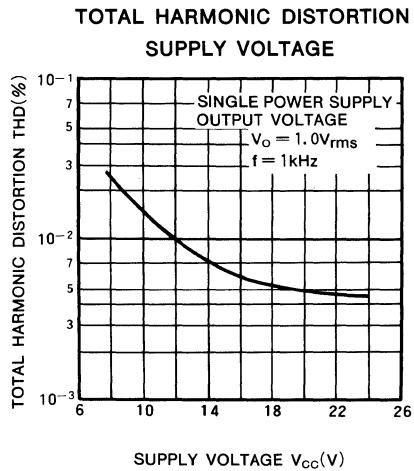
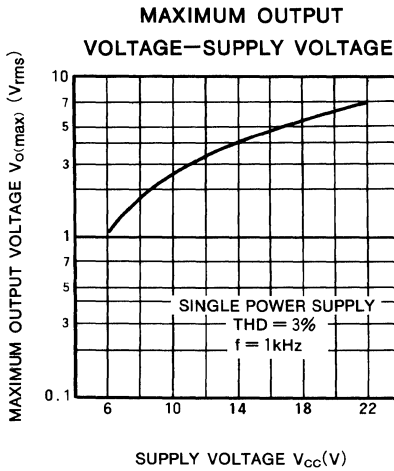
DUAL 2-MODE ELECTRONIC SWITCH

Symbol	Switch							Method
	S ₁	S ₂	S ₃	S ₄	S ₅₋₁	S ₅₋₂	S ₆	
I _{CC}	ON	OFF	ON	½				Zero signal, measure circuit current
V _{CNTL1}	ON	ON	ON	3				Measure Pin② DC supply voltage as input signal 1 is output
V _{CNTL2}	ON	ON	ON	3				Measure Pin② DC supply voltage as input signal 2 is output
R _i	ON	ON	OFF	½	CHA	CHA		R _i = $\frac{V_2 \times R_0}{V_1 - V_2}$, V ₁ = 1.5V _{rms} , R ₀ = 47kΩ Measure input level at V ₂ point
					CHB	CHB		
G _v	ON	ON	ON	½	CHA	CHA	CHA	G _v = 20 log $\frac{V_0}{V_1}$ (dB), V ₁ = 1.5V _{rms} V ₀ = output level
					CHB	CHB	CHB	
V _{O(max)}	ON	ON	ON	½	CHA	CHA	CHA	Measure output level as output THD is 1.0%
					CHB	CHB	CHB	
THD	ON	ON	ON	½	CHA	CHA	CHA	Measure output THD as output level is 1.0V _{rms}
					CHB	CHB	CHB	
N _o	ON	ON	ON	½			CHA	Input pin short to GND. Measure the output noise level, BPF 20Hz~20kHz
							CHB	
C.T.	ON	ON	ON	1↔2	CHA		CHA	C.T. = 20 log $\left(\frac{V_0(S_4 \rightarrow 1)}{V_0(S_4 \rightarrow 2)} \right)$ (dB)
					CHB		CHB	
C.L.	ON	ON	ON	1	CHA	CHA	CHA ↓ CHB	C.L. = 20 log $\left(\frac{V_0(\text{CHA})}{V_0(\text{CHB})} \right)$ (dB)
C.B.	ON	ON	ON	1	CHA	CHA	CHA	C.B. = 20 log $\left(\frac{V_0(\text{CHA})}{V_0(\text{CHB})} \right)$ (dB)
					CHB	CHB	CHB	

TYPICAL CHARACTERISTIC (T_a = 25°C, V_{CC} = 15V, unless otherwise noted)

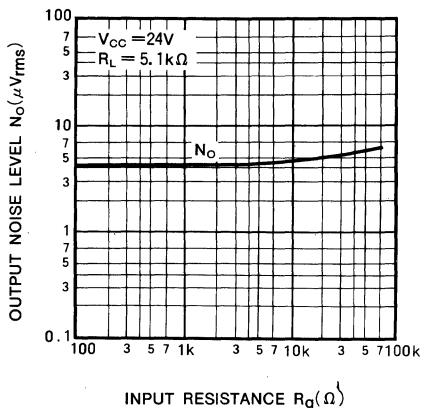


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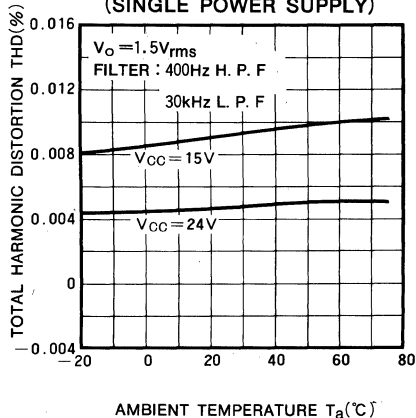


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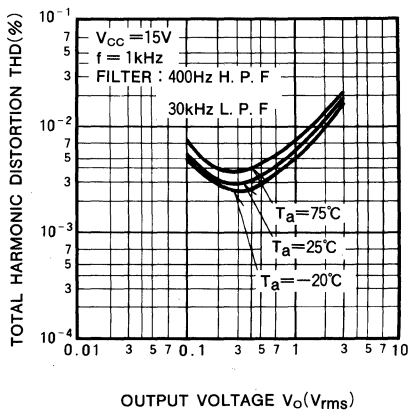
INPUT RESISTANCE—OUTPUT NOISE LEVEL (SINGLE POWER SUPPLY)



TOTAL HARMONIC DISTORTION—AMBIENT TEMPERATURE (SINGLE POWER SUPPLY)

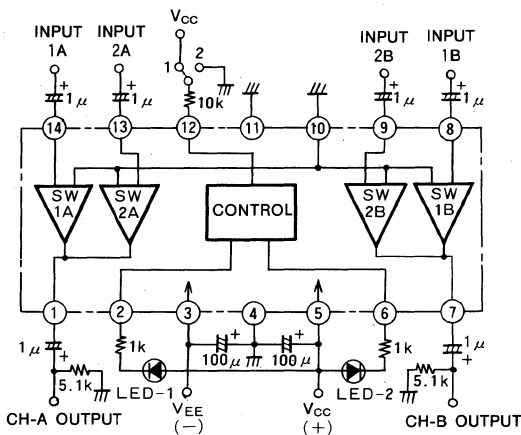


TRANSCONDUCTANCE—AMBIENT TEMPERATURE (SINGLE POWER SUPPLY)



APPLICATION CIRCUIT

DUAL POWER SUPPLY OPERATION



SINGLE POWER SUPPLY OPERATION

