

M51743P

6 MITSUBISHI ELEK (LINEAR)

80C 09367 D T-51-19

SEQUENCE CONTROLLER

DESCRIPTION

The M51743P is a semiconductor integrated circuit designed for use as sequence controller for oil combustion systems. It consists of five comparators and two voltage regulation circuits.

Three of the comparators can be used for sensor signal monitoring and two can be used for driving relays, etc.

Since one of the comparators for drive applications has a redundant "2" structure, the M51743P is especially suitable for use in oil combustion system, for which safety operation is required.

FEATURES

- Includes five comparators (three for signal monitoring and two for drive purposes)
- Includes two voltage regulation circuits
.....5.5V (typ), 6.2V (typ)
- Can directly drive relays, lamps, etc.
- Built-in memory function (comparator (2))
- Built-in hysteresis function (comparators (1), (4), (5))
- Redundancy "2" safety circuit design (comparator (5))
- Built-in reference power supply reduces the number of external parts
- Wide supply voltage range 8.5~20V

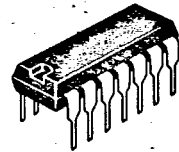
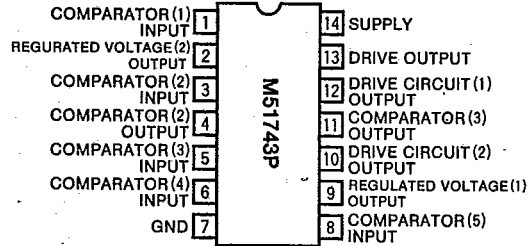
APPLICATION

Sequence controller (in oil and gas combustion systems), flame detection circuits.

RECOMMENDED OPERATING CONDITIONS

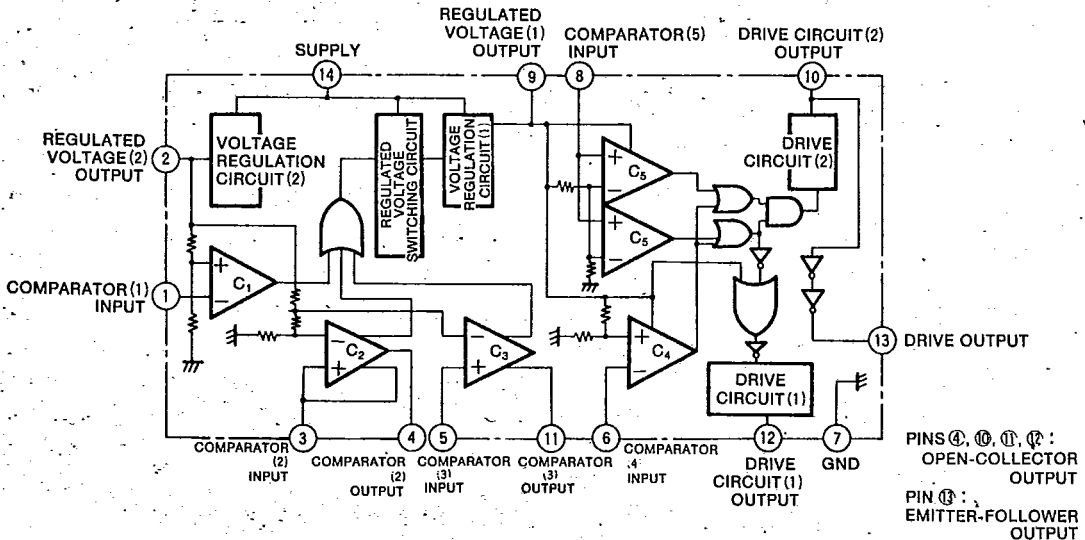
- Supply voltage range 8.5~20V
- Rated supply voltage 12V

PIN CONFIGURATION (TOP VIEW)



14-pin molded plastic DIP

BLOCK DIAGRAM



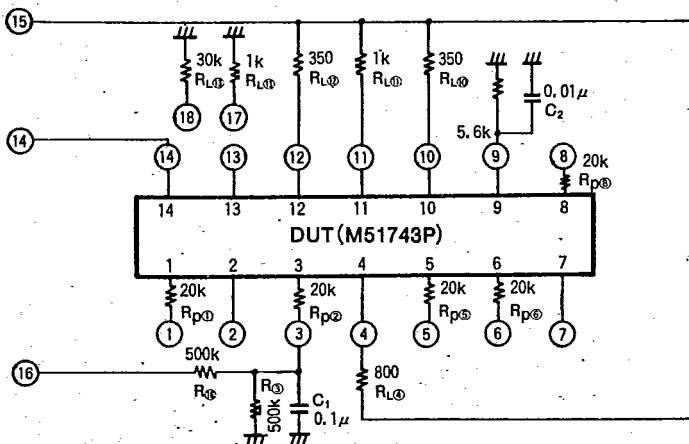
ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V_{CC}	Supply voltage		20	V
I_{CC}	Circuit current		22	mA
$I_{L(1)}$	Pin ① maximum current	*Instantaneous peak value	70	mA
$I_{L(2)}$	Pin ② maximum current		60	mA
$I_{L(3)}$	Pin ③ maximum current		60	mA
P_d	Power dissipation		900	mW
K_{θ}	Thermal derating	$T_a \geq 25^{\circ}\text{C}$	9	mW/ $^{\circ}\text{C}$
T_{opr}	Operating temperature range		-20~+65	$^{\circ}\text{C}$
T_{stg}	Storage temperature range		-40~+125	$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_a=25^{\circ}\text{C}$, $V_{CC}=15\text{V}$)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
$V_{O(1)}$	Voltage regulation circuit (1) output	Pin ⑩, pin ⑪ ON state	5.0	5.5	6.3	V
$V_{O(2)}$	Voltage regulation circuit (2) output		5.0	6.1	6.9	V
$I_{CC(I)}$	Circuit current (I)	$V_{(1)}=1.2\text{V}$, $V_{(2)}=3\text{V}$	6	14	20	mA
$I_{CC(II)}$	Circuit current (II)	$V_{(2)}=4.5\text{V}$	2	10	17	mA
$V_{ON(1)}$	Comparator (1) ON voltage		3.8	4.4	5.3	V
$V_{OFF(1)}$	Comparator (1) OFF voltage		3.0	3.5	4.6	V
$I_{IN(1)}$	Comparator (1) input current	$V_{(1)}=5.1\text{V}$		0.15	0.6	μA
$V_{ON(2)}$	Comparator (2) ON voltage		1.5	2.0	2.8	V
$V_{(3)}$	Pin ③ clmp voltage	$R_{(3)}=500\text{k}\Omega$	4.9	5.75	6.3	V
$V_{sat(4)}$	Pin ④ output saturation voltage	$R_{L(4)}=800\Omega$	0.9	1.12	1.5	V
$V_{ON(3)}$	Comparator (3) ON voltage		2.5	3.3	4.1	V
$I_{IN(3)}$	Comparator (3) input current	$V_{(3)}=4\text{V}$		0.15	0.5	μA
$V_{sat(1)}$	Pin ① output saturation voltage	$R_{L(1)}=1\text{k}\Omega$, $V_{(1)}=4.1\text{V}$	1.3	1.83	3.0	V
$V_{REF I(4)}$	Comparator (4) reference voltage (I)		3.3	3.84	4.5	V
$V_{REF II(4)}$	Comparator (4) reference voltage (II)		2.9	3.55	3.9	V
$I_{IA(4)}$	Comparator (4) input current	$V_{(4)}=2.5\text{V}$		50	250	nA
$V_{ON(5)}$	Comparator (5) ON voltage		2.0	2.3	2.7	V
$V_{OFF(5)}$	Comparator (5) OFF voltage		1.6	2.0	2.3	V
$I_{IN(5)}$	Comparator (5) input current	$V_{(5)}=2.5\text{V}$		0.1	0.35	μA
$V_{sat(10)}$	Pin ⑩ output saturation voltage (1)	$R_{L(10)}=350\Omega$, $V_{CC}=10\text{V}$		0.3	0.9	V
$V_{sat(11)}$	Pin ⑪ output saturation voltage (1)	$R_{L(11)}=350\Omega$, $V_{CC}=10\text{V}$		0.3	0.9	V
$V_{OH(1)}$	Pin ① output voltage	$R_{L(1)}=1\text{k}\Omega$	7.5			V

TEST CIRCUIT



Unit
Resistance : Ω
Capacitance : F

TEST METHODS

Symbol	Pin No.																		Measurement point
	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	
$V_{O(1)}$	②*1	①	GND		GND	1.2V	GND	GND	Ⓜ*2				⑰	15V	15V		⑬		⑨
$V_{O(2)}$	5.2	Ⓜ				1.2V							⑰	15V			⑬		②
$I_{OC(1)}$						1.2V		3V					⑰	Ⓜ 15V				⑬	⑭
$I_{OC(II)}$						4.5V							⑰	Ⓜ 15V				⑬	⑭
$V_{ON(1)}$	1.3V 3.8V					1.2V			Ⓜ				⑰	15V			⑬		⑨
$V_{OFF(1)}$	4.6V 3.0V								Ⓜ										⑨
$I_{IN(1)}$	Ⓜ 5.1V		GND																①
$V_{ON(2)}$	②	①				1.2V			Ⓜ										⑨
$V_{③}$			Ⓜ					GND											③
$V_{sat④}$				Ⓜ	GND	1.2V		GND											④
$V_{ON(3)}$			GND		4.1V 2.5V				Ⓜ										⑨
$I_{IN(3)}$					Ⓜ 4V														③
$V_{sat⑩}$					4V	1.2V					Ⓜ								⑩
$V_{REF I(4)}$					GND	4.1V 3.3V				Ⓜ									⑩
$V_{REF II(4)}$						3.9V 2.9V				Ⓜ				15V					⑩
$V_{sat⑫(1)}$						4.5V		2.6V				Ⓜ	⑰	10V					⑫
$V_{OH⑬}$								2.6V					⑰ Ⓜ	15V					⑬
$I_{IN(4)}$						Ⓜ 2.5V							⑰						④
$V_{ON(5)}$						4.5V		2.7V 2.0V				Ⓜ							⑫
$V_{OFF(5)}$								2.3V 1.6V				Ⓜ							⑫
$I_{IN(5)}$								2.5V						15V					⑤
$V_{sat⑭(1)}$	②	①	GND		GND	4.5V	GND	2.6V		Ⓜ			⑰	10V	15V		⑬		⑭

* 1 : ○ Indicates pin number
 * 2 : Ⓜ Indicates measurement pin

M51743P OPERATION TIME CHART

Input application conditions

V_{CC} pin ⑩

Comparator(1) Input pin ①

Comparator(2) Input pin ③

Comparator(3) Input pin ⑤

Comprator(5) input pin ⑧

Output waveform

Regulated voltage (1) Output pin ⑨

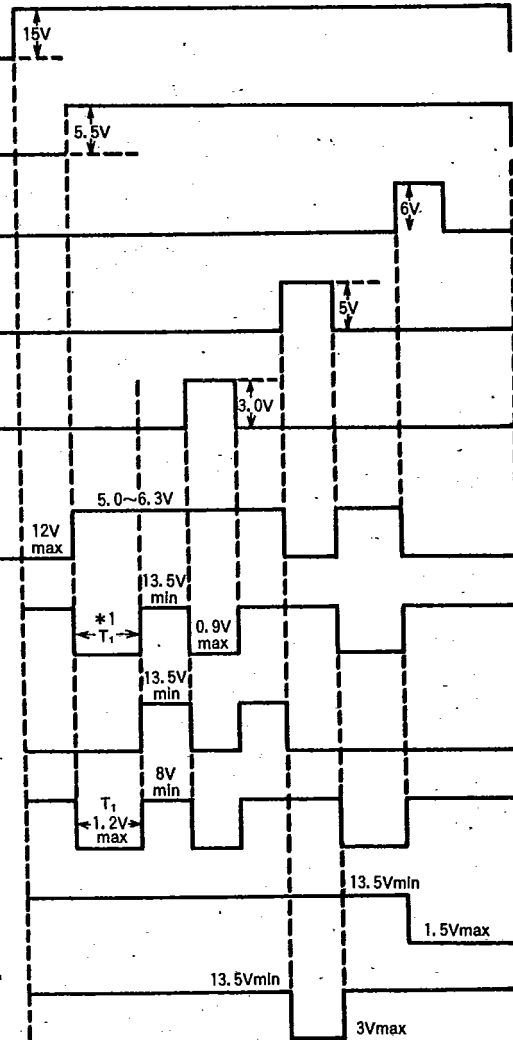
Drive output pin ⑩

Driver output pin ⑫

Pin(1) Inverted output pin ⑬

Comparator(2) output pin ④

Comparator(3) output pin ①



* 1 : T₁ is the timer output determined by the external connection of pin ⑥

M51743P INPUT/OUTPUT TRUTH TABLE
Operation comparators (1), (2), (3)

Input	Pin No.	State			
	Pin ①	0* ¹	1	1	1
Input	Pin ③	0	0	1	0
	Pin ⑤	0	0	0	1
	Pin ⑧	0	1	0	0
	Pin ⑩	1* ²	0* ⁴	1	1
Output	Pin ⑫	0* ²	0* ⁴	0	0
	Pin ⑬	1* ³	0* ⁴	1	1
	Pin ④	1* ²	1	0	1
	Pin ①	1* ²	1	1	0

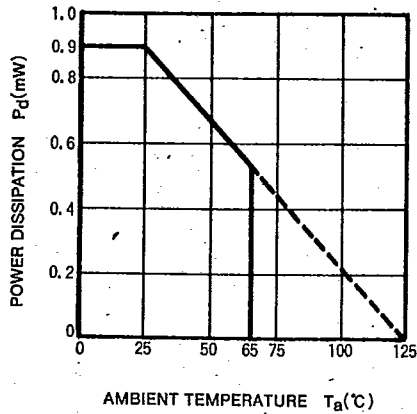
Operation comparators (4), (5)

Input	Pin No.	State							
	Pin ⑨	1	1	1	1	0	0	0	0
Input	Pin ⑥	0	1	0	1	0	1	—	—
	Pin ⑧	0	0	1	1	—	—	0	1
	Pin ⑩	0	1	0	0	1	1	1	1
Output	Pin ⑫	0	1	0	0	0	0	0	0
	Pin ①	0	1	0	0	1	1	1	1

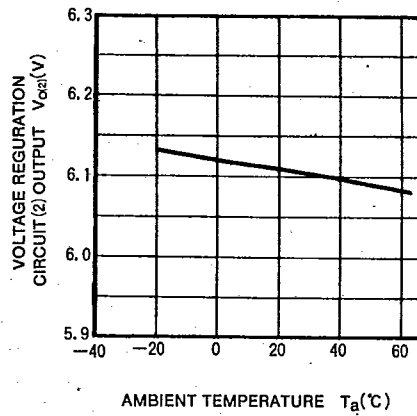
* 1 : "1" and "0" indicate the "H" and "L" voltage levels of the pins.
 * 2 : Pins ⑩, ⑫, ④, ① are ON at "0" and OFF at "1" (open-collector output).
 * 3 : Pin ⑬ is ON at "1" and OFF at "0" (emitter-follower output).
 * 4 : Will be 0 or 1 depending on the condition of comparators (4) and (5).

TYPICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$, $V_{CC}=9\text{V}$, unless otherwise noted)

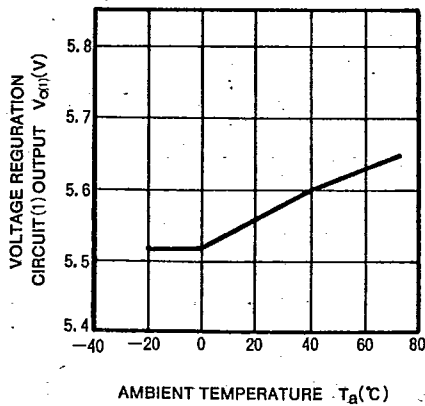
THERMAL DERATING
(MAXIMUM RATING)



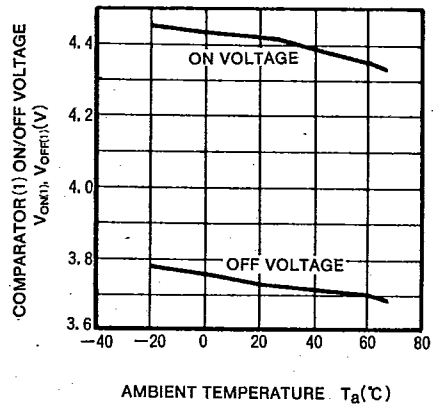
VOLTAGE REGULATION CIRCUIT (2)
VS. AMBIENT TEMPERATURE



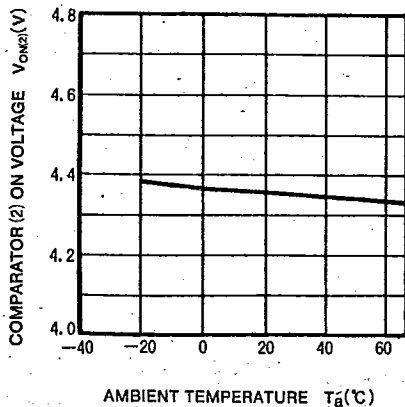
VOLTAGE REGULATION CIRCUIT (1)
AMBIENT TEMPERATURE



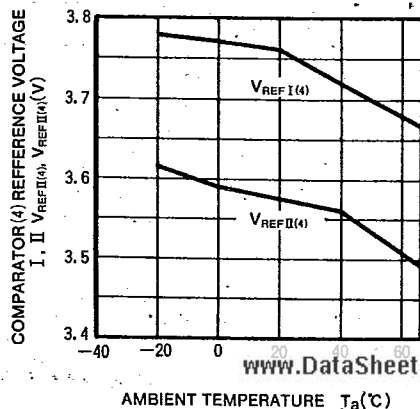
COMPARATOR ON/OFF VOLTAGE
VS. AMBIENT TEMPERATURE



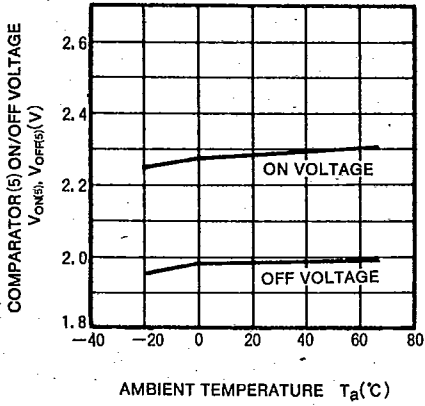
COMPARATOR (2) ON VOLTAGE
VS. AMBIENT TEMPERATURE



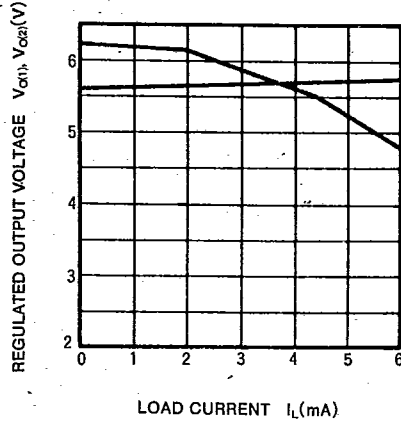
COMPARATOR (4) REFERENCE VOLTAGE
I, II VS. AMBIENT TEMPERATURE



COMPARATOR (5) ON/OFF VOLTAGE VS. AMBIENT TEMPERATURE



REGULATED OUTPUT VOLTAGE VS. LOAD CURRENT



APPLICATION EXAMPLE

Example of using comparator (4) as timer operation

