

MITSUBISHI ICs (TV)
M52016SP

PAL/NTSC VIDEO CHROMA DEFLECTION

DESCRIPTION

The M52016SP is a semiconductor integrated circuit for video, chroma, and deflection signal processing. Combined with IC component M51346AP for VIF/SIF, it realizes practical color television using only two IC components.

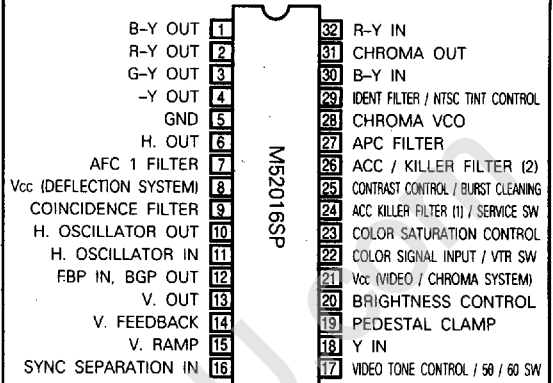
Circuit configuration includes built-in sync separation, horizontal AFC, horizontal oscillator, horizontal count-down, vertical count-down, contrast control, luminance control, picture quality control, ACC/killer detector, ident detector, APC detector, chroma oscillator, NTSC tint control, and chroma demodulator functions.

The M52016SP is packaged as a 32-pin, bipolar monolithic, formed plastic shrink DIP.

FEATURES

- Large-scale, single-chip construction enhances practicality and reliability of the television set itself while contributing to lower power consumption.
- Places of adjustment and number of external components are minimized.
- NTSC system switch enables construction of a PAL/NTSC system with a minimal amount of peripheral components. (Switches, demodulator axis, demodulation ratio, PAL matrix, and tint control.)
- Employs a sync detector system for ACC/killer detector, realizes superior weak electric field killer level.
- Double AFC in the horizontal circuit effectively reduces weak electric field horizontal "jitter," and "bending" on the screen is minimized thanks to luminance alteration. VCR switch improves skew distortion. Sync sensor circuit

PIN CONFIGURATION (TOP VIEW)



Outline 32P4B

not only expands pull-in range, but can be used as a sensor signal for sound muting, automatic channel selection, etc.

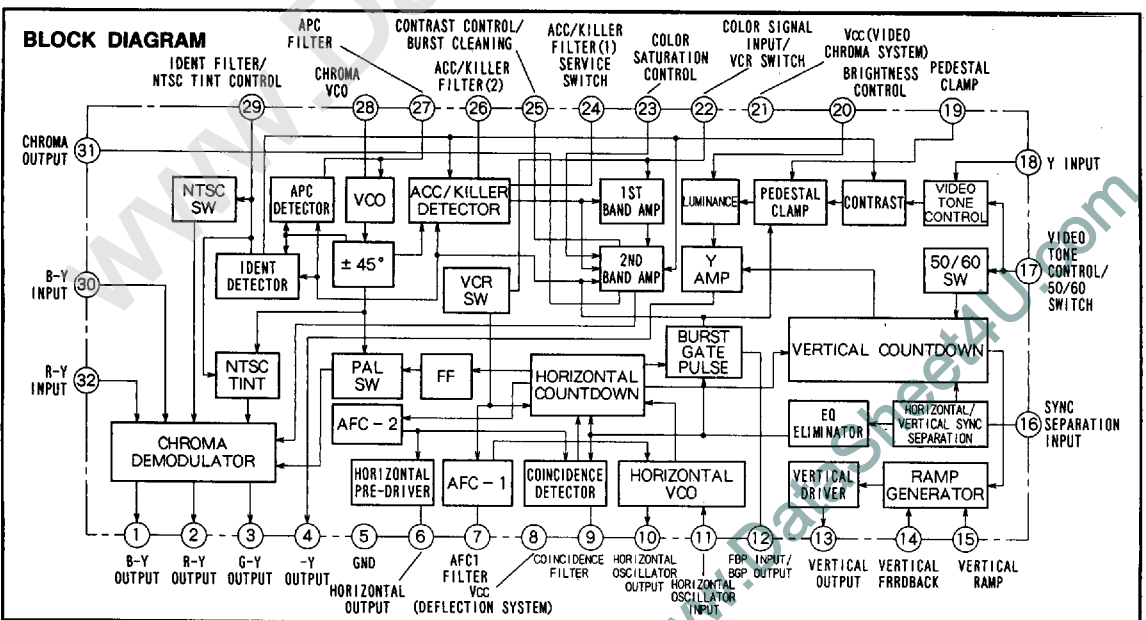
- Contains built-in service switch. (Contrast minimum killer on, vertical output off)

APPLICATION

PAL/NTSC System Color Television Receiver

RECOMMENDED OPERATING CONDITION

Supply voltage range.....10~13V
 Rated supply voltage.....11V



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PAL/NTSC VIDEO CHROMA DEFLECTION

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Ratings	Unit
V _{cc}	Supply voltage	13.5	V
P _d	Power Dissipation	1.25	W
Surge	Surge voltage resistance	± 200	V
V _{i4}	Pin ④ voltage	0.28V _{cc} + 6	V
I _{i5}	Pin ⑤ input current	+ 6	mA
I _{i2}	Pin ⑫ input current	- 1.0	mA
T _{opr}	Operating temperature	- 20~65	°C
T _{stg}	Storage temperature	- 40~125	°C

Note. The normal direction for current flow is the direction when output from the IC component. Use the absolute value as standard value.

ELECTRICAL CHARACTERISTICS (T_a = 25°C, unless otherwise noted)

VIDEO SECTION/CHROMA SECTION

Symbol	Parameter	Test	Input C	Test conditions*													Limits			Unit
				17A	19	20A	23A	25A	29A	S ₂₄₋₁	S ₂₄₋₂	S ₂₆	S ₂₉	S ₃₂	S ₃₀	12A	Note	Min.	Typ.	
I _{cc} - Y _c	Circuit current	A.1	-	-	-	-	-	-	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note1	32	45	58	mA
Y _{max}	Maximum output	4	SG1	-	-	-	-	V _{cc}	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note1	7.0	8.0		V _{P-P}
GY	Video amp gain	4	SG2	-	2V	-	-	V _{cc}	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note2	18	21	24	dB
GY _{mid-1}	Contrast control characteristics - 1	4	SG2	-	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note3	0.65	0.95	1.25	V _{P-P}
GY _{max}																	5.3	7.3	9.3	dB
GY _{min}	Contrast control characteristics-2	4	SG2	-	2V	-	-	0V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note4		-39	-32	dB
GY _{mid-2}	Contrast control characteristics-3	4	SG2	-	2V	-	-	5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note5	-8	-6	-4	dB
YT _{mid}	Video tone control characteristics-1	4	SG3	5.5V	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note6	0.9	1.35	2.0	V _{P-P}
YT _{min}	Video tone control characteristics-2	4	SG3	V _{cc}	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note7	-11	-8	-5	dB
YT _{max}	Video tone control characteristics-3	4	SG3	1.5V	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note8	2.0	4.6	7.0	dB
YP	Peaking value	4	C SG2 SG3	-	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note9	1.0	3.0	5.0	dB
Y _{BRTmid}	Brightness control characteristics-1	4	-	-	-	5.5V	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note10	4.55	5.15	5.75	V
Y _{BRTmin}	Brightness control characteristics-2	4	-	-	-	V _{cc}	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note11	2.7	3.3	3.9	V
Y _{BRTmax}	Brightness control characteristics-3	4	-	-	-	0V	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note12	6.4	7.0	7.6	V
Y _f	Frequency characteristics	4	C SG2 SG4	-	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 13	-3.0	1.0	5.0	dB
DG	Differential gain	4	C SG2	-	2.4V 1.8V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 14			10	%
H.BLKTH	Horizontal blanking threshold voltage	4 1A	D SG5	-	2V	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 15	9.0	9.5	10.0	V
Y _v .BLK	Vertical blanking voltage	4	-	-	-	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note16	8.2	8.8	9.4	V
Y _{DC} .REG	DC playback ratio control characteristics	4	C SGa	-	-	5.5V	-	5.5V	ON	OFF	OFF	OFF	1	ON ↓ OFF	V _{cc}	Note 17	0.9	1.5	2.1	V
ΔY _{DC} /T _a	Video DC level temperature characteristics	4	-	-	-	5.5V	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 18	2.0	4.0	6.0	mV/°C
C _{max}	Maximum output	31	SG5 0dB	-	-	-	-	V _{cc} V _{cc}	ON	OFF	OFF	OFF	1	OFF	V _{cc}		2.4	3.0	3.6	V _{P-P}
GC	Chroma maximum gain	31	SG5 Variable	-	-	-	-	V _{cc} V _{cc}	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 19	42	48	54	dB
Acc1	ACC characteristics - 1	31	SG5 0dB -20dB	-	-	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 20	-3.0	-1.0	1.0	dB
Acc2	ACC characteristics - 2	31	SG5 +6dB	-	-	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 21	-2.6	0.4	2.4	dB
KIL	Killer operation input	SG5 23	SG5	-	-	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 22	-31	-25	-20	dB
D.KIL	Killer color residual	31	SG5 0dB	-	-	-	-	5.5V	ON	OFF	OFF	OFF	1	OFF	V _{cc}	Note 23			200	mV _{P-P}

* : "-" indicates OPEN.

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PAL/NTSC VIDEO CHROMA DEFLECTION

VIDEO/CHROMA SECTION (cont.)

Symbol	Parameter	Test point	Input C	Test conditions*																Limits			Unit
				17A	19	20A	23A	25A	29A	S24-1	S24-2	S26	S29	S32	S20	12A	Note	Min.	Typ.	Max.			
CC _{mid-1}	Color control characteristics - 1	31	SG5	-	-	-	5.5V	V _{CC}	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 24	0.7	1.2	1.7	V _{P-P}		
CC _{max}			0dB															4.0	8.0	12.0	dB		
CC _{min}	Color control characteristics - 2	31	SG5	-	-	-	0V	V _{CC}	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 25		-40	-30	dB		
CC _{mid-2}	Color control characteristics - 3	31	SG5	-	-	-	5V	V _{CC}	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 26	-5.0	-3.3	-1.3	dB		
APC1	APC pull-in range - 1	23	D SG6	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 27	580	900		Hz			
APC2	APC pull-in range - 2	23	D SG6	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 28	850	1500		Hz			
D _{dc}	Demodulated output DC voltage	1A 2A 3A	-	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 29	5.9	6.4	6.9	V			
D _{offset}	Demodulated output DC offset	-	-	-	-	-	-	-	-	-	-	-	-	-	-	Note 30			0.3	V			
D _{max}	Demodulated output maximum amplitude	1A	E SG7 0.5 V _{P-P}	-	-	-	-	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}		3.5	4.5	5.5	V _{P-P}			
D _{B-Y}	B-Y modulated sensitivity	1A	E SG7 0.2 V _{P-P}	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}		2.4	2.9	3.4	V _{P-P}			
R/B	Demodulated ratio - 1	2A	E SG7 0.2 V _{P-P}	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}	Note 31	0.56	0.63	0.70	-			
G/B	Demodulated ratio - 2	3A	E SG7 0.2 V _{P-P}	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}	Note 32	0.31	0.37	0.43	-			
Δ D/H	Demodulated output 1H level difference	2A	E SG7 0.2 V _{P-P}	-	-	-	0V	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}	Note 33			35	mV _{P-P}		
C _{leak}	Demodulated output carrier leak	1A 2A 3A	-	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 34			0.2	V _{P-P}			
V ₂₃	Pin @ voltage (killer ON)	23	-	-	-	-	5.5V	-	ON	OFF	ON	OFF	1	OFF	V _{CC}			0.12	0.5	V			
V _{NTSC}	NTSC operation control voltage	1A 29A	E SG7 0.2 V _{P-P}	-	-	-	5.5V	Variable	OFF	OFF	OFF	ON	2	OFF	V _{CC}	Note 35	4.53	4.7	5.05	V			
NTSC B	Demodulated output (NTSC)	1A	D SG9 0dB	-	-	-	5.5V	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}		2.4	3.4	4.7	V _{P-P}		
NTSC R/B	Demodulation ratio - 1 (NTSC)	2A	D SG9 0dB	-	-	-	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}	Note 36	0.9	1.0	1.1	-			
NTSC G/B	Demodulation ratio - 2 (NTSC)	3A	D SG9 0dB	-	-	-	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}	Note 37	0.23	0.28	0.33	-			
V _{P/N}	PAL/NTSC demodulated output DC voltage	1A	-	-	-	-	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}	Note 38			300	mV			
Δ D/KIL	Killer ON/OFF demodulated output DC voltage difference	1A	-	-	-	-	5.5V	-	OFF	OFF	ON	OFF	1	OFF	V _{CC}	Note 39			50	mV			
SS	Service switch operation	4 13 23	C SG2	-	-	-	5.5V	-	ON	ON	OFF	OFF	1	OFF	V _{CC}	Note 40				-			
D _{bw}	Demodulated output bandwidth	1 2 3	E SG8	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}	Note 41	0.7	0.9		MHz			
CD	Chroma input dynamic range	31	D SG5	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 42	0.8	1.2		V _{P-P}			
∠R-Y-P	PAL demodulated phase angle	1A	E SG8 4.53 MHz	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}	Note 43		90		deg.			
∠G-Y-P		2A 3A	-	-	-	-	5.5V	-	OFF	OFF	OFF	OFF	2	OFF	V _{CC}			241		deg.			
∠R-Y-N	NTSC demodulated phase angle	1A	D	-	-	-	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}	Note 44		100		deg.			
∠G-Y-N		2A 3A	SG9 0dB	-	-	-	5.5V	2V	ON	OFF	OFF	ON	1	OFF	V _{CC}			232		deg.			
T _{min}	NTSC TINT	1A	D	-	-	-	5.5V	Variable	ON	OFF	OFF	ON	1	OFF	V _{CC}	Note 45	29	49	69	deg.			
T _{max}		2A	SG6 0dB	-	-	-	5.5V	Variable	ON	OFF	OFF	ON	1	OFF	V _{CC}		20	40	60	deg.			
D _{dc} /T _a	Demodulated output DC voltage temperature characteristics	1A 2A 3A	-	-	-	-	5.5V	-	ON	OFF	OFF	OFF	1	OFF	V _{CC}	Note 46	-2	0	+2	mV/°C			

*: "-" indicates OPEN.

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PAL/NTSC VIDEO CHROMA DEFLECTION

DEFLECTION SECTION - 1

Symbol	Parameter	Test point	Input A	Test conditions*														Limits			Unit	
				9	12A	14	15A	Vcc	22A	S6	S14	S16	S17	S22	S24-2	S7	Note	Min.	Typ.	Max.		
Icc - D	Circuit current	A2	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON		21	30	39	mA	
V8min	Horizontal oscillator starting voltage	6	-	-	Vcc	6V	GND	11V	Variable	-	ON	OFF	3	OFF	OFF	OFF	ON	Note 47		5.2	6.2	V
fH	Horizontal freerun frequency	6	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	OFF	OFF	OFF	ON	Note 48	15.45	15.625	15.8	kHz	
fPHIL	Horizontal pull-in range-1	6	SGb	6V	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	Note 49		-970	-660		Hz
fPHIH																		+550	+900		Hz	
fPH2L	Horizontal pull-in range-2	6	SGb	6V	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	ON	OFF	ON	Note 50		-1100	-700		Hz
fPH2H																		+650	+1000		Hz	
VCR SW	VCR SW threshold voltage	6 22	SGb	-	Vcc	6V	GND	11V	Variable	ON	OFF	1	OFF	OFF	OFF	ON	Note 51	4.6	5.1	5.6	V	
V9H	Coincidence detection maximum voltage	9	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON		9.5	10.3		V	
V9L	Coincidence detection minimum voltage	9	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	OFF	OFF	OFF	ON			0	0.5		V
τH	Horizontal output pulse amplitude	6	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	Note 52	22	24	26	μ sec	
VHmin	Horizontal output voltage	6	SGa	-	Vcc	6V	GND	11V	-	OFF	OFF	1	OFF	OFF	OFF	ON	Note 53	3.0	3.85	4.7		V
VHmax																			0.04	0.16		V
TGP	Burst gate pulse position	12	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	Note 54	3.5	4.3	5.3	μ sec	
fv50	Vertical freerun frequency 50(Hz)	13	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	OFF	OFF	OFF	ON		43	44.4	46	Hz	
fv60	Vertical freerun frequency 60(Hz)	13	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	ON	OFF	OFF	ON		50.5	52.8	55	Hz	
fpv50	Vertical pull-in range 50(Hz)	13	SGc	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	Note 55	54	55.7	58	Hz	
fpv60	Vertical pull-in range 60(Hz)	13	SGc	-	Vcc	6V	GND	11V	-	ON	OFF	1	ON	OFF	OFF	ON	Note 56	65	66.3	69	Hz	
τv50	Vertical output pulse amplitude 50 (Hz)	13	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	OFF	OFF	OFF	ON	Note 57	474	544	614	μ sec	
τv60	Vertical output pulse amplitude 60 (Hz)	13	-	-	Vcc	6V	GND	11V	-	ON	OFF	3	ON	OFF	OFF	ON	Note 58	474	544	614	μ sec	
Vvmax	Vertical output maximum voltage	13	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	Note 59	3.2	4.2		Vo-P	

*: "-" indicates OPEN.

DEFLECTION SECTION - 2

Symbol	Parameter	Test point	Input A	Test conditions*														Limits			Unit	
				9	12A	14	15A	Vcc	22A	S6	S14	S16	S17	S22	S24-2	S7	S15	Note	Min.	Typ.		Max.
Vvmin	Vertical output minimum voltage	13	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	OFF	Note 60			0.3	V
Pramp	Ramp peak voltage	15	SGa	-	Vcc	6V	-	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	ON	Note 61	6.4	6.8	7.2	V
Vramp	Ramp amplitude	15	SGa	-	Vcc	6V	-	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	ON	Note 62	1.5	1.8	2.1	Vp-P
GvV	Vertical open loop gain	13	B SGd -30dB	-	Vcc	14A 5V	GND	11V	-	ON	ON	3	OFF	OFF	ON	ON	OFF	Note 63	16	20	24	dB
Iss	Sync separation input sensitivity current	12	-	-	Vcc	6V	GND	11V	-	ON	OFF	2	OFF	OFF	OFF	ON	OFF	Note 64	0.07	0.1	0.15	mA
TBGP1	Burst gate pulse timing - 1	12	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	OFF	Note 65	0.25	0.4	0.6	μ sec
TBGP2	Burst gate pulse timing - 2	12	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	OFF	Note 66	3.4	4.0	4.6	μ sec
VFBP	Flyback pulse clamp voltage	12	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	OFF	Note 67	4.5	5.0	5.5	V
VGBP	Burst gate pulse voltage	12	SGa	-	Vcc	6V	GND	11V	-	ON	OFF	1	OFF	OFF	OFF	ON	OFF	Note 68	9.5	10.0	10.5	V

*: "-" indicates OPEN.

DEFLECTION SECTION - 3

Symbol	Parameter	Test point	Input	Test conditions*												Limits			Unit	
				9	12A	14	15A	Vcc	22A	S6	S14	S16	S17	S22	S24-2	Note	Min.	Typ.		Max.
AFC2 _{min}	AFC	6	-	-	-	6V	GND	11V	-	OFF	OFF	3	OFF	OFF	OFF	Note 69		1.0	4	μ sec
AFC2 _{max}	AFC interlocking range	12B	-	-	-	6V	GND	11V	-	OFF	OFF	3	OFF	OFF	OFF	Note 69	12	20		μ sec
AFC2G	AFC - 2 loop gain	6 12B 12	-	-	-	6V	GND	11V	-	OFF	OFF	3	OFF	OFF	OFF	Note 70	31	41	51	dB

* : "-" indicates OPEN.

VIDEO SECTION / CHROMA SECTION

Note1. Maximum Output "Y_{max}"

- a) Test amplitude when not V blanking. Wave form should be distorted at this time.

Note2. Video Amplifier Gain "GY"

- a) Test amplitude when not V blanking and make V_{cm} the testing value.

b) $GY = 20 \log \frac{V_{cm} (mV_{P-P})}{200 (mV_{P-P})} (dB)$

Note3. Contrast Control Characteristics-1 "GY_{mid-1}, GY_{max}"

- a) Test amplitude when not V blanking and make V_{co} the testing value.

$GY_{mid-1} = V_{co}$

b) $GY_{max} = 20 \log \frac{V_{cm}}{V_{co}} (dB)$

Note4. Contrast Control Characteristics-2 "GY_{min}"

- a) Test amplitude when not V blanking and make V_{c1} the testing value.

b) $GY_{min} = 20 \log \frac{V_{c1}}{V_{co}} (dB)$

Note5. Contrast Control Characteristics-3 "GY_{mid-2}"

- a) Test amplitude when not V blanking and make V_{c2} the testing value.

b) $GY_{mid-2} = 20 \log \frac{V_{c2}}{V_{co}} (dB)$

Note6. Video Tone Characteristics-1 "YT_{mid}"

- a) Test amplitude when not V blanking and make V_{to} the testing value.

b) $YT_{mid} = V_{to} (V_{P-P})$

Note7. Video Tone Characteristics-2 "YT_{min}"

- a) Test amplitude when not V blanking and make V_{t1} the testing value.

b) $YT_{min} = 20 \log \frac{V_{t1}}{V_{t0}} (dB)$

Note 8. Video Tone Characteristics-3 "YT_{max}"

- a) Test amplitude when not V blanking and make V_{t2} the testing value.

b) $YT_{max} = 20 \log \frac{V_{t2}}{V_{t0}} (dB)$

Note9. Peaking Value "YP"

- a) Test amplitude when not V blanking.
- b) Make V_{p1} the testing value when SG2 is input.
- c) Make V_{p2} the testing value when SG3 is input.

d) $YP = 20 \log \frac{V_{p2}}{V_{p1}} (dB)$

Note10. Brightness Control Characteristics-1 "Y_{BRTmid}"

- a) Test DC voltage when not V blanking.

Note11. Brightness Control Characteristics-2 "Y_{BRTmin}"

- a) Same as Note10.

Note12. Brightness Control Characteristics-3 "Y_{BRTmax}"

- a) Same as Note10.

Note13. Frequency Characteristics "Y_f"

- a) Test amplitude when not V blanking.
- b) Make V_{f1} the testing value when SG2 is input.
- c) Make V_{f2} the testing value when SG3 is input.

d) $Y_f = 20 \log \frac{V_{f2}}{V_{f1}} (dB)$

Note14. Differential Gain "DG"

- a) Test amplitude when not V blanking.
- b) Make V_{G1} the testing value when 2.4V is input into pin ⑨.
- c) Make V_{G2} the testing value when 1.8V is input into pin ⑨.

d) $DG = \frac{|V_{G1} - V_{G2}|}{V_{G2}} \times 100 (\%)$

Note15. Horizontal Blanking Threshold Voltage "H_{BLK TH}"

- a) Apply voltage to pin ④ and increase from 8V.
- b) Test the voltage of pin ④ when signal ceases to be output by 1A.

Note16. Vertical Blanking Voltage "Y_{v.BLK}"

- a) Test voltage during V blanking.

Note17. DC Playback Ratio Control Characteristics "Y_{DGREG}"

- a) Set S_{Ga} of point C to 1.5V_{P-P}.
- b) Test H.sync when S₂₀ is on and DC when not blanking. Make REG1 the testing value.
- c) Test in the same manner when S₂₀ is off and REG2 the testing value.
- d) $Y_{DC REG} = REG1 - REG2$

Note18. Video DC Level Temperature Characteristics "ΔY_{oc}/Ta"

- a) Test DC voltage rate of change when ambient temperature is gradually raised from -20°C to +65°C.

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Note19. Chroma Maximum Gain "GC"

a) Test output amplitude (P-P) and make V_{GC} the testing value. (Input level is killer level +1dB)

$$b) GC = 20 \log \frac{V_{GC} \text{ (mV}_{P-P})}{\text{Input Amplitude (mV}_{P-P})} \text{ (dB)}$$

Note20. ACC Characteristics-1 "ACC1"

a) Test output amplitude (P-P).

b) Make V_{A0} the testing value when SG5 0dB is input.

c) Make V_{A1} the testing value when SG5 -22dB is input.

$$d) ACC1 = 20 \log \frac{V_{A1}}{V_{A0}} \text{ (dB)}$$

Note21. ACC Characteristics-2 "ACC2"

a) In the same manner as in Note20, make V_{A2} the testing value when SG5 + 6dB is input.

$$b) ACC2 = 20 \log \frac{V_{A2}}{V_{A0}} \text{ (dB)}$$

Note22. Killer Operation Input "KIL"

a) Gradually attenuate the level of SG5.

b) While monitoring DC voltage of pin ②, input level of SG5 when voltage becomes less than 1V.

Note23. Killer Color Residual "D.KIL"

a) Test output amplitude within 1H interval.

Note24. Color Control Characteristics-1 "CC_{mid-1}, CC_{max}"

a) Test output amplitude (P-P) and make V_{C10} the testing value.

$$CC_{mid-1} = V_{C10} \text{ (V}_{P-P})$$

$$b) CC_{max} = 20 \log \frac{V_{C12}}{V_{C10}} \text{ (dB)}$$

Note 25. Color Control Characteristics-2 "CC_{min}"

a) Test output amplitude (P-P) and make V_{C11} the testing value.

$$b) CC_{min} = 20 \log \frac{V_{C11}}{V_{C10}} \text{ (dB)}$$

Note26. Color Control Characteristics-3 "CC_{mid-2}"

a) Test output amplitude (P-P) and make V_{C12} the testing value.

$$b) CC_{mid-2} = 20 \log \frac{V_{C12}}{V_{C10}} \text{ (dB)}$$

Note27. APC Pull-In Range-1 "APC1"

a) Set so that the frequency of SG6 is less than 4.433MHz and pin ② is Lo.

b) Gradually increase the frequency of SG6.

c) Test the frequency when the voltage of pin ② changes from Lo to Hi and make F_{AU} the testing value.

$$d) APC1 = 4433619 \text{ (Hz)} - F_{AU} \text{ (Hz)}$$

Note28. APC Pull-In Range-2 "APC2"

a) Set so that the frequency of SG6 is more than 4.434MHz and pin ② is Lo.

b) Gradually decrease the frequency of SG6.

c) Test the frequency when the voltage of pin ② changes from Lo to Hi and make F_{Ad} the testing value.

$$d) APC2 = F_{Ad} \text{ (Hz)} - 4433619 \text{ (Hz)}$$

Note29. Demodulated Output DC Voltage "D_{dc}"

a) Test DC voltage at 1A, 2A and 3A.

Note30. Demodulated Output DC Offset "D_{offset}"

a) Calculate each voltage difference of 1A2A, 2A3A and 3A1A from the testing value of Note29.

Note31. Demodulation Ratio-1 "R/B"

a) Test output amplitude and make D_{R-Y} the testing value.

$$b) R/B = \frac{D_{R-Y}}{D_{B-Y} \text{ (Testing Value at C15)}}$$

Note32. Demodulation Ratio-2 "G/B"

a) Test output amplitude and make D_{G-Y} the testing value.

$$b) G/B = \frac{D_{G-Y}}{D_{B-Y} \text{ (Testing Value at C15)}}$$

Note33. Demodulated Output 1 H Level Difference "ΔD/H"

a) Test both AC, DC for each 1H level difference.

Note34. Demodulated Output Carrier Leak "Cleak"

a) Test output carrier element for 1A, 2A and 3A.

Note35. NTSC Operation Control Voltage "V_{ntsc}"

a) Gradually decrease voltage of 29A from the area of 8V.

b) Test the 29A voltage when signal ceases to be output by 1A.

Note36. Demodulation Ratio (NTSC)-1 "NTSC R/B"

a) Test output amplitude and make $NTSC_R$ the testing value.

$$b) NTSC R/B = \frac{NTSC_R}{NTSC_B \text{ (Testing Value at C22)}}$$

Note37. Demodulation Ratio (NTSC)-2 "NTSC G/B"

a) Test output amplitude and make $NTSC_G$ the testing value.

$$b) NTSC G/B = \frac{NTSC_G}{NTSC_B \text{ (Testing Value at C22)}}$$

Note38. PAL/NTSC Demodulated Output DC Voltage Difference "VP/N"

a) Test the difference in DC voltage when S_{29} is on and when it is off.

Note39. Killer ON/OFF Modulated Output DC Voltage Difference "ΔD/KIL"

a) Make $DD1$ the voltage difference when S_{26} is on, and make $DD2$ the voltage difference when S_{26} is off.

$$b) \Delta D/KIL = |DD1 - DD2|$$

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Note40. Service Switch Operation "SS"

- No output signal from ④
- No vertical sync pulse from ⑬
- Voltage of ② drops below 1V.
- Check a, b and c.

Note41. Demodulated Output Bandwidth "Daw"

- Set frequency of SG8 to 4.5MHz, and test output amplitude of ①, ② and ③.
- Gradually increase the frequency of SG8.
- Test output frequency of ①, ② and ③ when output amplitude is 3dB less than when 4.5MHz is input.

Note42. Chroma Input Dynamic Range "CD"

- Increase the level of SG5 and test the input amplitude when output becomes distorted.

Note43. PAL Demodulated Phase Angle "ZR-Y-P, ZG-Y-P"

- Make ZR-Y-P the phase difference of 1A and 2A.
- Make ZG-Y-P the phase difference of 1A and 3A.

Note44. NTSC Demodulated Phase Angle "ZR-Y-N, ZG-Y-N"

- Make ZR-Y-N the phase difference of 1A and 2A.
- Make ZG-Y-N the phase difference of 1A and 3A.

Note45. NTSC Tint "T_{min}, T_{max}"

- Set oscilloscope to X-Y. Connect 1A to X and 2A to Y.
- Open 29A and set SG6 frequency to 4.433619MHz.
- At this time the oscilloscope waveform is shown as 180°.
- Make T_{min} the remainder of subtracting 180° from the angle when 29A was set to 4V.
- Make T_{max} the remainder of subtracting 180° from the angle when 29A was set to 1V.

Note46. Demodulated Output DC Voltage Temperature Characteristics "Doc/Ta"

- Test output voltage rate of change when ambient temperature is gradually raised from -20°C to +65°C.

Note47. Horizontal Oscillator Starting Voltage "V_{Bmin}"

- Increase V_{cc} from 0V.
- Test V_{cc} voltage where the output waveform cycle of ⑥ is approx. 64μs.

Note48. Horizontal Freerun Frequency "f_H"**Note49. Horizontal Pull-In Range-1 "f_{PH1L}, f_{PH1H}"**

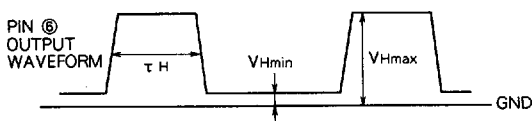
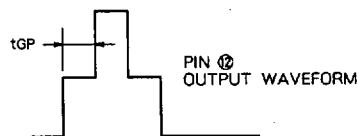
- Decrease the frequency of input signal so that the SGB input signal and pin ⑥ output waveform are not synchronized.
- Gradually increase the frequency of SGB.
- Test the SGB frequency when SGB and pin ⑥ output waveform become synchronized and make f_{L1} the testing value.
- f_{PH1L} = f_{L1} - f_H (Testing Value at Note48)
- Test the upper side pull-in in the same manner and make f_{H1} the SGB frequency when the two become synchronized.
- f_{PH1H} = f_{H1} - f_H (Testing Value at Note 48)

Note50. Horizontal Pull-In Range-2 "f_{PH2L}, f_{PH2H}"

- Test in the same manner as Note49 and make f_{L2} the lower side pull-in frequency, and make f_{H2} the upper side pull-in frequency.
- f_{PH2L} = f_{L2} - f_H (Testing Value at Note48)
- f_{PH2H} = f_{H2} - f_H (Testing Value at Note48)

Note51. VCR Switch Threshold Voltage "VCR SW"

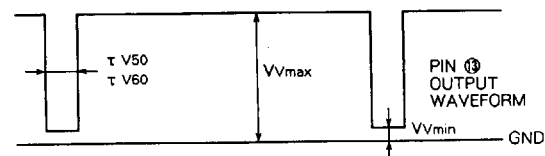
- Set SGB frequency of Note49 immediately preceding pull-in.
- Increase 22A voltage from 6V and test voltage of pin ② at time of pull-in.

Note52. Horizontal Output Pulse Amplitude "τ_H"**Note53. Horizontal Output Voltage "V_{Hmin}, V_{Hmax}"****Note54. Burst Gate Pulse Position "t_{GP}"****Note55. Vertical Pull-In Range 50 (Hz) "f_{pv50}"**

- Increase the frequency of input signal so that the SGC input signal and pin ⑬ output waveform are not synchronized.
- Decrease the frequency of SGC and test the SGC frequency when SGC and the output waveform of pin ⑬ become synchronized.

Note56. Vertical Pull-In Range 60 (Hz) "f_{pv60}"

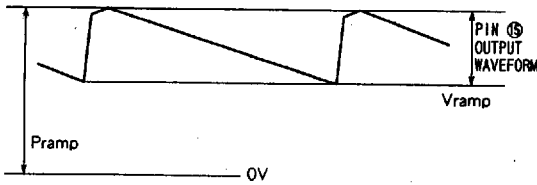
- Same as Note55.

Note57. Vertical Output Pulse Amplitude 50 (Hz) "τ_{v50}"**Note58. Vertical Output Pulse Amplitude 60 (Hz) "τ_{v60}"****Note59. Vertical Output Maximum Voltage "V_{vmax}"****Note60. Vertical Output Minimum Voltage "V_{vmin}"****Note61. Ramp Peak Voltage "P_{ramp}"**

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Note62. Ramp Amplitude "V_{ramp}"



Note63. Vertical Open Loop Gain "G_v"

- a) Check ⑩ and adjust the voltage at 14A lest the waveform should be distorted (approx. 3.5V).
- b) Test the output amplitude of pin ⑩ and make V_{vo} the testing value.
- c) $G_v = 20 \log \frac{V_{vo} \text{ (mV}_{p-p})}{\text{Input Amplitude (} \approx 15.8\text{mV}_{p-p})}$ (dB)

Note64. Sync Separation Input Sensitivity Current "I_{ss}"

- a) Increase I_s from 0mA.
- b) Test I_s when burst gate pulse ceases to be output by ⑫.

Note65. Burst Gate Pulse Timing-1 "T_{BGP1}"

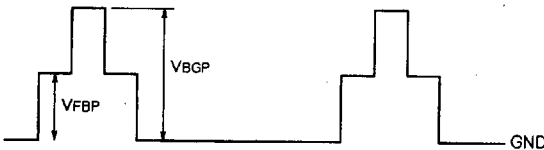
- a) Test the time from SGa rise to burst gate pulse rise.

Note66. Burst Gate Pulse Timing-2 "T_{BGP2}"

- a) Test burst gate pulse amplitude.

Note67. Flyback Pulse Clamp Voltage "V_{FBP}"

Note68. Burst Gate Pulse Voltage "V_{BGP}"



Note69. AFC2 Interlock Range "AFC2_{min}, AFC2_{max}"

- a) Trigger external pulse generator by pin ⑥ output.
- b) Generate flyback pulse with external pulse generator, and input to 12B.
- c) While gradually decreasing the pulse generator's pulse delay, when the position of pin ⑫ waveform largely differs from the output waveform of pin ⑥, make AFC2_{min} the time between ⑥ rise and flyback pulse rise.
- d) Increasing the pulse generator's pulse delay, test in the same manner to find the value of AFC2_{max}.

Note70. AFC2 Loop Gain "AFC2G"

- a) Test the burst gate pulse position (t_{GP}) at AFC2_{min} of Note69 + 1μs, and make t_{GPmin} the testing value.
- b) Test the burst gate pulse position at AFC2_{max} - 1μs and make t_{GPmax} the testing value.
- c) $AFC2G = 20 \log \frac{AFC2_{max} - AFC2_{min} \text{ (} \mu\text{sec)}}{t_{GPmin} - t_{GPmax} \text{ (} \mu\text{sec)}}$

Precautions Concerning Electrical Characteristics

- 1) Adjust the one-shot multivibrator's potentiometer so that the timing of the horizontal input flyback pulse of pin ⑫ and pulse amplitude are as shown in the figure below.
- 2) Standard Conditions of Deflection Section for Testing Video/Chroma Sections

Input A	9	14A	15A	V _{cc}	S ₆	S ₁₄	S ₁₆	S ₁₇	S ₂₂	S ₇
SGa	-	8V	-		ON	OFF	1	OFF	OFF	ON

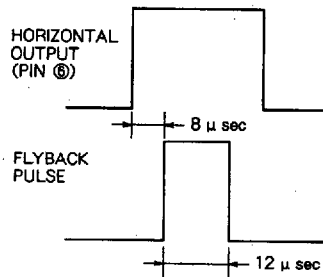
"-" indicates OPEN.

- 3) Standard Conditions of Video/Chroma Sections for Testing Deflection Section

Input C,D,E	17A	19	20A	23A	25A	29A	S ₂₄₋₁	S ₂₆	S ₂₉	S ₃₂	S ₂₀
	-	-	5.5V	-	5.5V	-	ON	OFF	OFF	1	OFF

"-" indicates OPEN.

- 4) Unless designated otherwise, V_{cc}=11V, T_a=25°C.



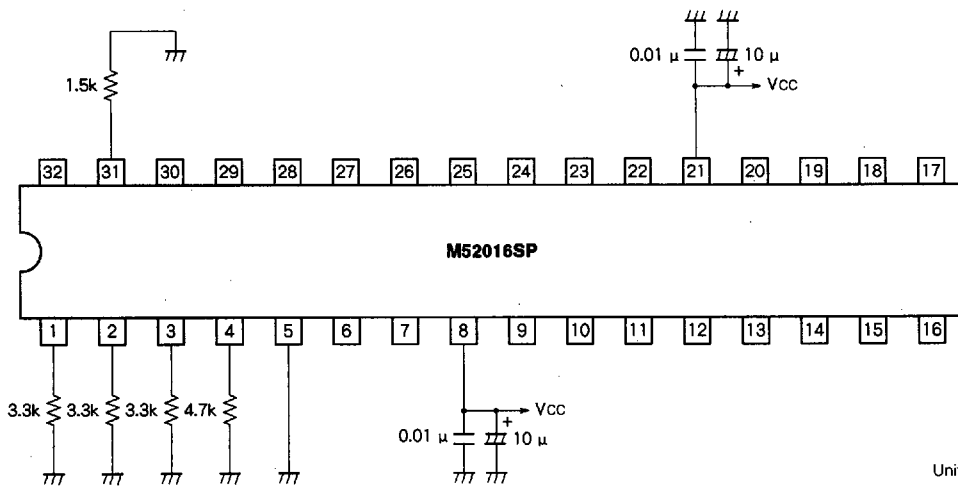
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PAL/NTSC VIDEO CHROMA DEFLECTION

VIDEO/CHROMA INPUT IMPEDANCE

Symbol	Parameter	Test point	Test conditions	Limits			Unit
				Min.	Typ.	Max.	
R _i (18)	Video input resistance	18	V _{cc} = 12V, test with LCR meter, 4.43MHz 0.1V		5		k Ω
C _i (18)	Video input capacitance	18			5.5		pF
R _i (17)	Picture quality input resistance	17			9.7		k Ω
C _i (17)	Picture quality input capacitance	17			6		pF
R _i (22)	Chroma input resistance	22			2		k Ω
C _i (22)	Chroma input capacitance	22			20		pF
R _i (30)	B - Y input resistance	30			3		k Ω
C _i (30)	B - Y input capacitance	30			7		pF
R _i (32)	R - Y input resistance	32			3		k Ω
C _i (32)	R - Y input capacitance	32			7		pF

INPUT IMPEDANCE TESTING CIRCUIT

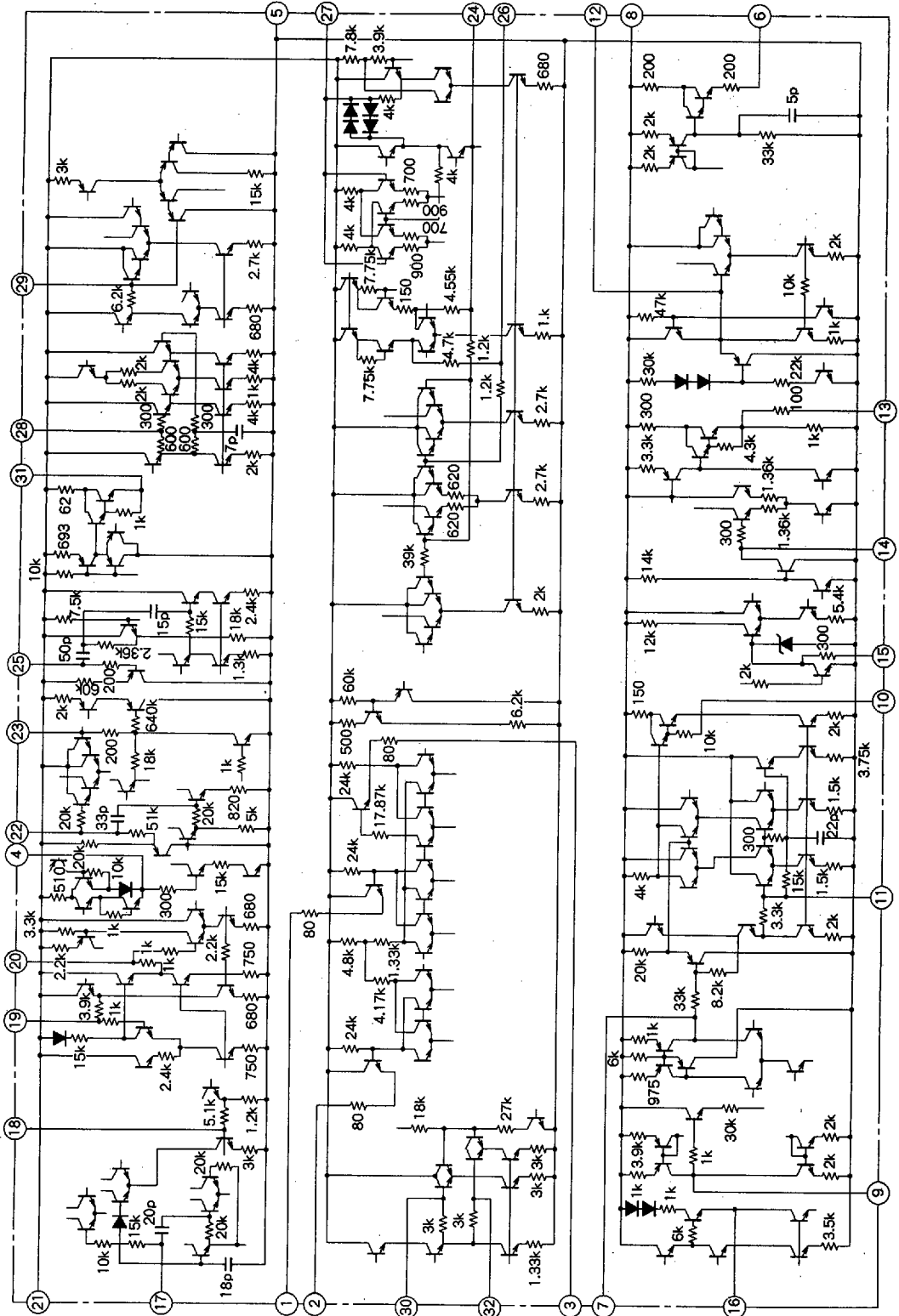


Units Resistance: Ω
Capacitance: F

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PAL/NTSC VIDEO CHROMA DEFLECTION

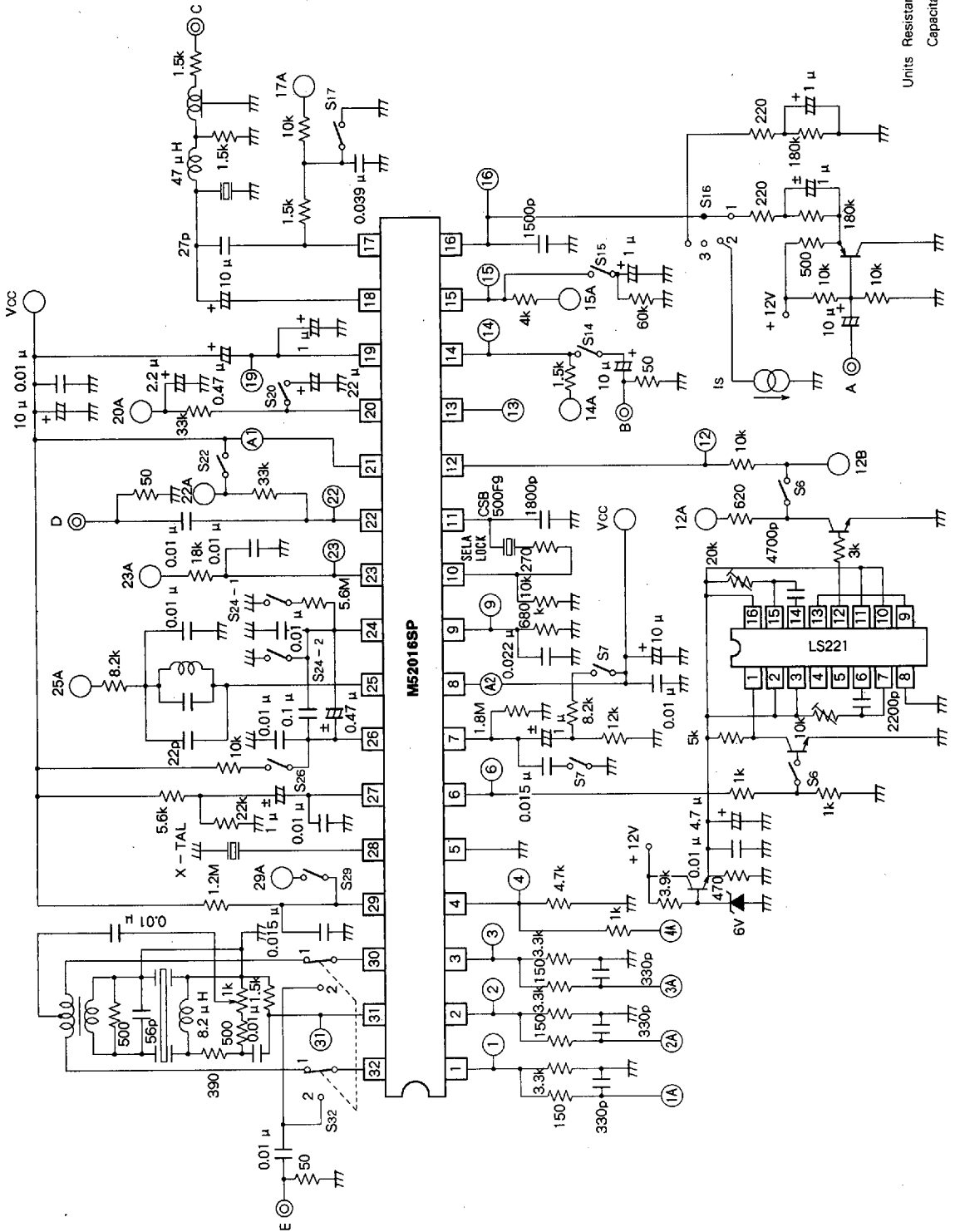
PERIPHERAL CIRCUIT OF PINS



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PAL/NTSC VIDEO CHROMA DEFLECTION

TEST CIRCUIT



Units Resistance: Ω
Capacitance: F

PAL/NTSC VIDEO CHROMA DEFLECTION

INPUT SIGNAL

TYPICAL CHARACTERISTICS

SG No.	Signals
SG1	100kHz CW 3VP-P
SG2	100kHz CW 200mVP-P
SG3	2MHz CW 200mVP-P
SG4	5MHz CW 200mVP-P
SG5	<p>PAL Simple chroma signal 0dB</p> <p>fsb = fsc = 4.433619MHz (Same phase)</p> <p>The phase correlation between the about signals is outlined in the figure on the right. The phase correlation with burst of ec (n) and ec (n+1) does not always have to be as shown in the figure on the right, and in particular must be adjustable according to conditions when testing phase correlation.</p>
SG6	With PAL simple chroma signals for SG5, the phase of burst and chroma signals should be the same and the frequency should be adjustable.
SG7	4.42MHz CW 0.2~0.5VP-P
SG8	4~6MHz CW
SG9	SG5 ƒ, fsb (burst) = 4.433619MHz, fsc (chroma) = 4.543MHz
SGa	<p>For sync separation input, make input for sync separation PAL-type APL 100% 1.5VP-P normal complex video signal shown in the figure on the right.</p>
SGb	<p>2VP-P Duty 90%</p>
SGc	<p>2VP-P Duty 95%</p>
SGd	2kHz CW 500mVP-P = 0dB

