

# M5216

## DUAL LARGE-CURRENT OPERATIONAL AMPLIFIERS (DUAL POWER SUPPLY TYPE)

### DESCRIPTION

The M5216 is a semiconductor integrated circuit designed as a high-output and high-speed operational amplifier for use in high-performance headphone amplifiers and mixer amplifiers found in cassette decks.

The device comes in an 8-pin SIL or DIL package and it contains two circuits for yielding a high internally phase-compensated gain, a high current capacity and a high slew rate. It can be widely used as a general-purpose dual amplifier in electronic equipment. In addition, it can be used in a single power supply format and employed in conditions where the supply voltage is low. These are features which make this device ideal for headphone amplifiers in portable products.

### FEATURES

- Large current capacity.....  $I_{LP} = \pm 100\text{mA}$
- High power output.....  $P_O = 40\text{mW}(\text{typ.})$   
 $P_O = 27\text{mW}(\text{typ.})$
- High slew rate, high  $f_T$   $\cdot$   $SR = 3.5\text{V}/\mu\text{s}$ ,  $f_T = 10\text{MHz}(\text{typ.})$
- Low noise ( $R_S = 1\text{k}\Omega$ ) FLAT.....  $V_{NI} = 1.7\mu\text{Vrms}(\text{typ.})$
- Low supply voltage drive possible.....  $V_{CC} = 4\text{V}(\pm 2\text{V})$
- High allowable power...  $P_d = 800\text{mW}(\text{SIL})$ ,  $625\text{mW}(\text{DIL})$

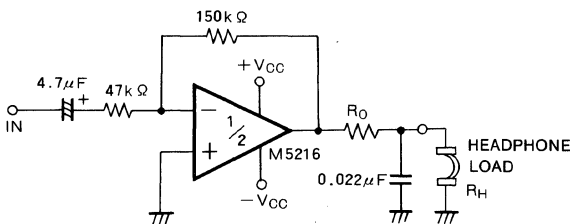
### APPLICATION

High-performance headphone amplifiers in VTRs, tape decks and stereo cassette tape recorders with built-in radios; mixer amplifiers, public address equipment, electronic musical instruments; also as a high-current, high-speed, general-purpose operating amplifier in other electronic products and equipment.

### RECOMMENDED OPERATING CONDITIONS

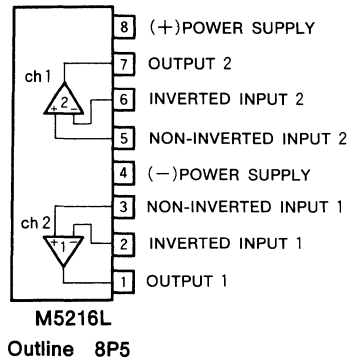
- Supply voltage range.....  $\pm 2\text{V} \sim \pm 16\text{V}$  (dual power supply)  
 $+4\text{V} \sim +32\text{V}$  (single power supply)
- Rated supply voltage.....  $\pm 15\text{V}$

### APPLICATION EXAMPLE IN HEADPHONE AMPLIFIER

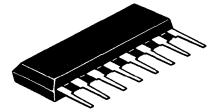
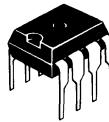
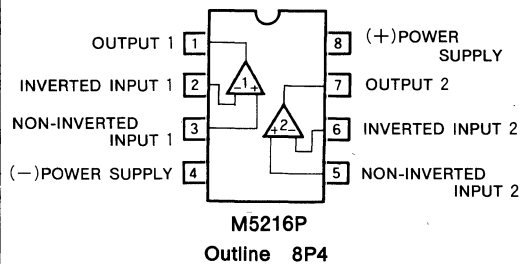


Note : For a single power supply system, the level of the (+) input pin should be shifted to  $V_{CC}/2$  potential and the output should be AC-coupled through a capacitor.

### PIN CONFIGURATION (TOP VIEW)

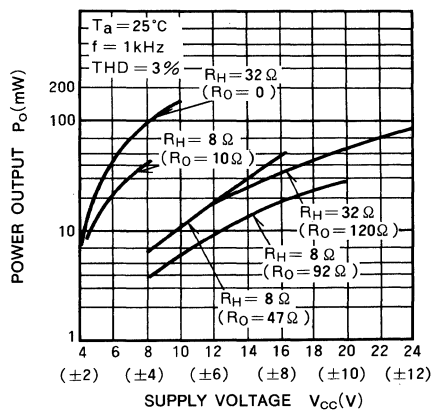


### PIN CONFIGURATION (TOP VIEW)



8-pin molded plastic DIL 8-pin molded plastic SIL

### HEADPHONE AMPLIFIER CIRCUIT $P_O$ VS $V_{CC}$



DUAL LARGE-CURRENT OPERATIONAL AMPLIFIERS  
(DUAL POWER SUPPLY TYPE)

ABSOLUTE MAXIMUM RATINGS (T<sub>a</sub>=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Limits	Unit
V <sub>CC</sub>	Supply voltage		±18	V
I <sub>LP</sub>	Load current		±100	mA
V <sub>ID</sub>	Differential input voltage		±30	V
V <sub>IC</sub>	Common input voltage		±15	V
P <sub>d</sub>	Power dissipation		800(M5216L)	mW
			625(M5216P)	
K <sub>θ</sub>	Thermal derating	T <sub>a</sub> ≥25°C	8(M5216L)	mW/°C
			6.25(M5216P)	
T <sub>opr</sub>	Ambient temperature		-20~+75	°C
T <sub>stg</sub>	Storage temperature		-55~+125	°C

ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25°C, V<sub>CC</sub>=±15V)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
I <sub>CC</sub>	Circuit current	V <sub>IN</sub> =0	—	7.5	12.0	mA
V <sub>IO</sub>	Input offset voltage	R <sub>S</sub> ≤10kΩ	—	0.5	6.0	mV
I <sub>IO</sub>	Input offset current		—	5	200	nA
I <sub>B</sub>	Input bias current		—	—	500	nA
R <sub>iN</sub>	Input resistance		0.3	5	—	MΩ
G <sub>VO</sub>	Open loop voltage gain	R <sub>L</sub> ≥2kΩ, V <sub>O</sub> =±10V	86	110	—	dB
V <sub>OM</sub>	Maximum output voltage	R <sub>L</sub> ≥2kΩ	±12	±13.5	—	V
		R <sub>L</sub> ≥150Ω	±10.5	±11	—	V
V <sub>CM</sub>	Common input voltage width		±12	±14	—	V
CMRR	Common mode rejection ratio	R <sub>S</sub> ≤10kΩ	70	90	—	dB
SVRR	Supply voltage rejection ratio	R <sub>S</sub> ≤10kΩ	—	30	150	μV/V
P <sub>d</sub>	Power dissipation		—	225	360	mW
SR	Slew rate	G <sub>v</sub> =0dB, R <sub>L</sub> =2kΩ	—	3.0	—	V/μs
f <sub>T</sub>	Gain bandwidth product		—	10	—	MHz
V <sub>IN</sub>	Input referred noise voltage	R <sub>S</sub> =1kΩ, BW: 10Hz~30Hz	—	1.8	—	μVrms

TYPICAL CHARACTERISTICS

