

M54540AL

BI-DIRECTIONAL MOTOR DRIVER

DESCRIPTION

The M54540AL, BI-DIRECTIONAL MOTOR DRIVER, consists of a full bridge power driver designed for a low power D-C motor control.

FEATURES

- Small single-in-line package
- Integral diodes for transient suppression
- PMOS compatible input

APPLICATION

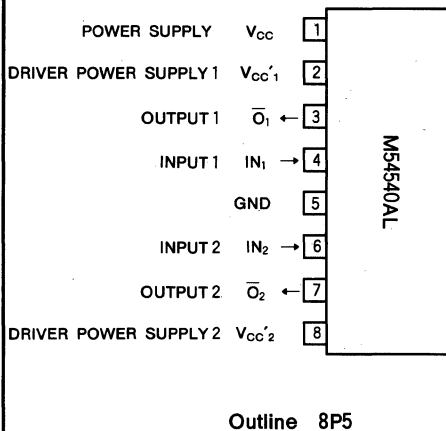
Audio cassette tape recorder

FUNCTION

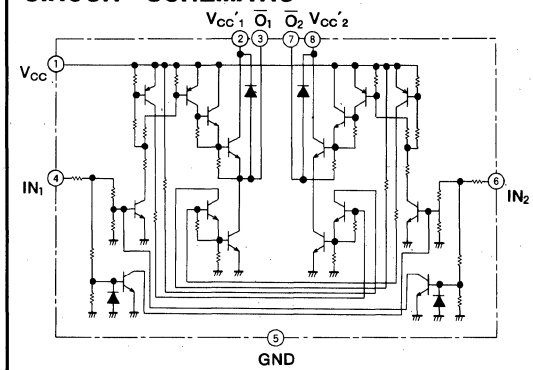
The M54540AL, full bridge motor driver, has the logic circuitry and non-darlington power drivers for bidirection control of D-C motors operating at currents of up to 600mA.

The power supplies for the logic circuitry and the drivers are separated so that the applied voltage to the motor can be controlled by the V_{CC}' of the driver supply voltage.

PIN CONFIGURATION (TOP VIEW)



CIRCUIT SCHEMATIC



LOGIC TRUTH TABLE

Input		Output		Note
IN ₁	IN ₂	O ₁	O ₂	
L	L	"OFF" state	"OFF" state	Open
H	L	H	L	⊙
L	H	L	H	⊙
H	H	"OFF" state	"OFF" state	Open

ABSOLUTE MAXIMUM RATINGS (T_a=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC}	Supply voltage		-0.3~+12	V
V _{CC'}	Driver supply voltage		-0.3~V _{CC}	V
V _I	Input voltage		-0.3~V _{CC}	V
V _O	Output voltage		-0.3~V _{CC} +2.5	V
I _{O(max)}	Peak output current	t _{OP} =10ms, Repetitive cycle 0.2Hz max	±600	mA
I _O	Continuous output current		±120	mA
P _d	Power dissipation	T _a =60°C	850	mW
T _{opr}	Operating ambient temperature range		-10~+60	°C
T _{stg}	Storage temperature range		-55~+125	°C

BI-DIRECTIONAL MOTOR DRIVER

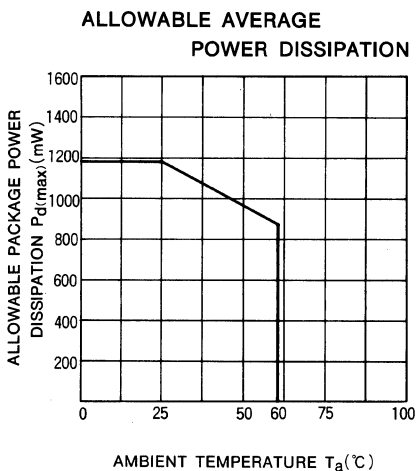
RECOMMENDED OPERATING CONDITIONS ($T_a=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Limits			Unit
			Min	Typ	Max	
V_{CC}	Supply voltage		6	10	11	V
I_O	Continuous output current				± 100	mA
V_{IH}	"H" Input voltage		3	5	V_{CC}	V
V_{IL}	"L" Input voltage			0	0.4	V
T_{Off}	Input switching interval	It is prohibited to switch the inputs at the same time.	10	300		ms

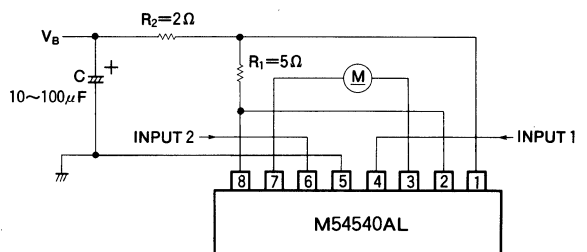
ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test conditions		Limits			Unit
				Min	Typ	Max	
$I_{O(Leak)}$	Output leakage current	$V_{CC}=V_{CC'}=20\text{V}$ $V_{I1}=V_{I2}=3\text{V}$	$V_O=12\text{V}$ $V_O=0\text{V}$			100 -100	μA
V_{OH}	"H" Output saturation voltage	$V_{CC}=V_{CC'}=10\text{V}$ $I_{OH}=-100\text{mA}$	$V_{I1}=3\text{V}, V_{I2}=0\text{V}$ $V_{I1}=0\text{V}, V_{I2}=3\text{V}$	8			V
V_{OL}	"L" Output saturation voltage	$V_{CC}=V_{CC'}=10\text{V}$ $I_{OL}=100\text{mA}$	$V_{I1}=3\text{V}, V_{I2}=0\text{V}$ $V_{I1}=0\text{V}, V_{I2}=3\text{V}$			0.6	V
I_{IH}	"H" Input current	$V_{CC}=V_{CC'}=10\text{V}$	$V_{I1}=3\text{V}$ $V_{I2}=3\text{V}$			500	μA
I_{CC}	Supply current	$V_{CC}=V_{CC'}=12\text{V}$	$V_{I1}=3\text{V}, V_{I2}=0\text{V}$		28	40	mA
			$V_{I1}=0\text{V}, V_{I2}=3\text{V}$				
			$V_{I1}=0\text{V}, V_{I2}=0\text{V}$				
			$V_{I1}=3\text{V}, V_{I2}=3\text{V}$				

TYPICAL CHARACTERISTICS



TYPICAL APPLICATION



Note

1. It is prohibited to switch the both inputs simultaneously. The inputs should be driven separately to avoid high crossover current.
2. The pins 1, 2 and 8 are separated and shall be connected externally.