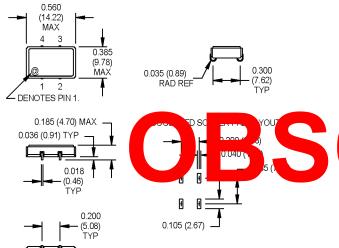
M5S Series 3.3 Volt PECL Oscillators





M5S Series PECL Clock Oscillators with Optional Complementary Outputs. PLL Version



All dimensions in inches (mm).

Ordering Information								
Product Series Temperature Range 1: 0°C to +70°C 6: -20°C to +70°C				x	•	J 	00.0000 MHz	
3: ±100 ppm 5: ±35 ppm 8: ±20 ppm	2: ±500 ppm 4: ±50 ppm 6: ±25 ppm							
Output Type See Table Below Symmetry/Logic Con A: 40/60 Package/Lead Config J: J Lead Frequency (custome	B: 45/55 (Up jurations —	to 80	0.000	MHz)				

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Т		2	Ţ			Ţ	8	
1	Α	А	s	А	А	С	С	
2	А	А	А	А	А	С	N	A = Available N = Not Available
6	А	А	A	А	А	С	С	S = Standard
7	А	А	Α	Α	А	С	N	C = Consult Factor

Pin Connections

PIN	FUNCTION(S) (Model Dependent)
1	N/C or Output #2
2	Case Ground
3	Output #1
4	+Vcc

PECL Output Type

PIN FUNCTIONS:	х	Z	
Single Output Pin 3	۲		
Dual Output Pin 1 & 3		۰	
Pin 2 Case Ground	2 Case Ground •		
Pin 4 +Vcc	*	8	

Electrical Specifications

Standard Operating Conditions • 0°C to +70°C; Vcc = $3.3 \pm 5\%$ VDC							
PARAMETERS	MIN.	TYP.	MAX.	UNITS			
Frequency Range	19.440		100.000	MHz			
Output Load ¹							
Symmetry ²	40/60		60/40	%			
Logic "0" Level			Vcc-1.63	V			
Logic "1" Level	Vcc-0.98			V			
Rise/Fall Time ³			2.5	nS			
Supply Current		50	90	mA			

¹ Internally terminated outputs. See load circuit diagram #4 on page 137.

² Symmetry is measured at Vcc-1.3 V level.
³ Rise/Fall times are measured between Vcc-0.98 and Vcc-1.63.

Utilizes PLL clock generation. Not recommended for low jitter applications. See page 136, Figure "2" for suggested solder profile.

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