M6007 Series 9x14 mm FR-4, 3.3 Volt, HCMOS/TTL, TCVCXO



Condition/Notes



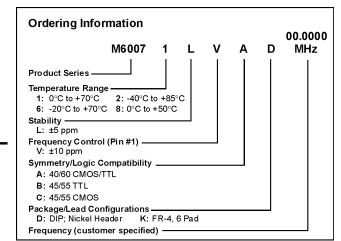
123

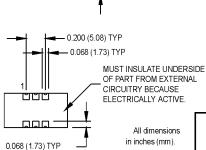
DENOTES PIN 1.

- Stratum 3 compliant stability and aging
- Ideal for WLL/DWDM/ ATM, and SONET/SDH applications

PARAMETER

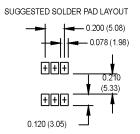






(9.10 ±0.25)

0.295 (7.50) MAX



	Frequency Range	F	60		170	MHz	
	Operating Temperature	TA	(See Ordering Information)				
	Storage Temperature	Ts	-55		+105	°C	
	Frequency Stability	ÄF/F	(See Ordering Information)				See Note 1
	Aging						
	1st Year				1.5	ppm	
	Thereafter (per year)				0.5	ppm	
	Pullability/APR		(See Ordering Information				
lus	Control Voltage	Vc	0.5	1.5	2.5		
[엹	Tuning Range				10	ppm/V	
Įį.	Modulation Bandwidth	fm	10			kHz	
Specifications	Input Impedance	Zin	50k			Ohms	
	Input Voltage	Vdd	3.15	3.3	3.45	V	
	Input Current	ldd			25	mA	
흕	Output Type						CMOS/TTL
Electrical	Load		2 TTL or 15 pF max.				
m [5	Symmetry (Duty Cycle)		(See Ordering Information)				
	Logic "1" Level	Voh	2.5			V	
	Logic "0" Level	Vol			0.5	V	
	Rise/Fall Time	Tr/Tf			10	ns	
	Tristate Function		Input Logic "1": output active				Opposite tristate
			Input Log	ic "0": ou	ıtput disab	les	logic available upon request.
	Start up Time		10			ms	
	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier
	@155.52 MHz	-60	-90	-110	-120	-120	dBc/Hz

Typ.

Max.

Units

Pin Connections

FUNCTION	SMT
N/C	1
Tristate	2
Ground/Case	3
Output	4
N/C	5
+Vdd	6

1. Stability is inclusive of five year aging at 25°C.

MtronPTI reserves the right to make changes to the product(s) and service(s) described herein without notice. No liability is assumed as a result of their use or application.

Symbol

Min.