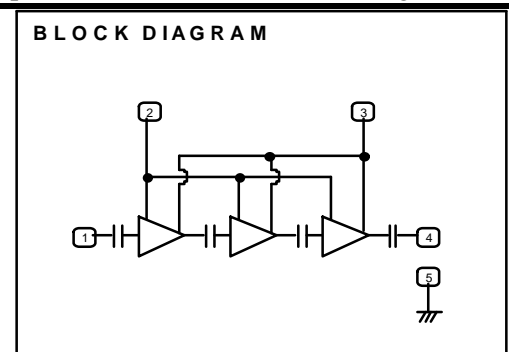
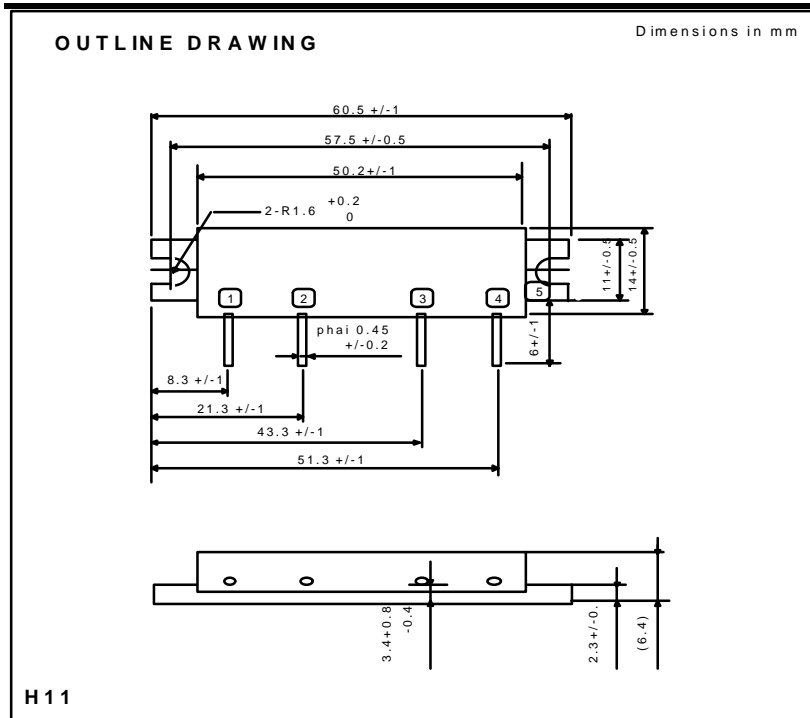


M68701M

Silicon MOS FET Power Amplifier, 860-915MHz 6W FM /Digital Mobile

ATTENTION
OBSERVE PRECAUTIONS
FOR HANDLING
ELETROSTATIC
SENSITIVE
DEVICES



PIN:
 ① P_{in} : RF INPUT
 ② V_GG : GATE BIAS SUPPLY
 ③ V_DD : DRAIN BIAS SUPPLY
 ④ P_O : RF OUTPUT
 ⑤ GND : FIN

MAXIMUM RATINGS (T_c=25deg C UNLESS OTHERWISE NOTED)

SYMBOL	PARAMETER	CONDITIONS	RATINGS	UNIT
VDD	SUPPLY VOLTAGE	V _G G<5V,Z _G =Z _L =50 ohms	17	V
VGG	GATE BIAS VOLTAGE		5.5	V
P _{in}	INPUT POWER	f=860-915MHz,Z _G =Z _L =50 ohms	10	mW
P _o	OUTPUT POWER	f=860-915MHz,Z _G =Z _L =50 ohms	10	W
T _c (OP)	OPERATION CASE TEMPERATURE	f=860-915MHz,Z _G =Z _L =50 ohms	-30 to +100	deg. C
T _{stg}	STORAGE TEMPERATURE		-40 to +110	deg. C

Note: Above parameters are guaranteed independently.

ELECTRICAL CHARACTERISTICS (T_c=25deg. C ,Z_G=Z_L=50 ohms UNLESS OTHERWISE NOTED)

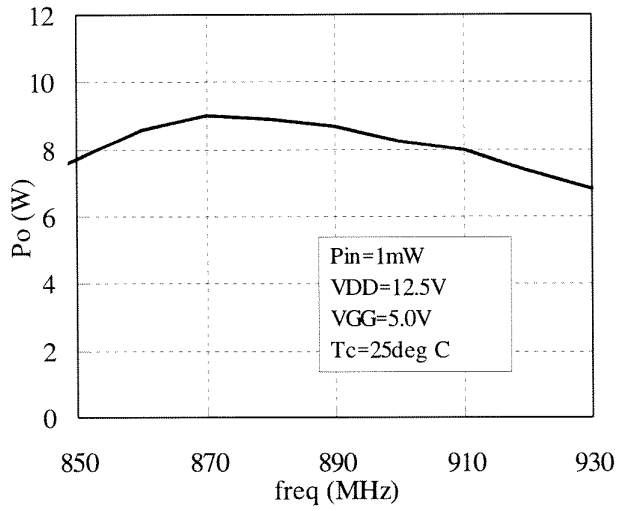
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS		UNIT
			MIN	MAX	
f	FREQUENCY RANGE		860	915	MHz
P _o	OUTPUT POWER	V _D D=12.5V,V _G G=5V,P _{in} =1mW	6		W
Efficiency	TOTAL EFFICIENCY	V _D D=12.5V, P _{out} =6W (V _G G adjust)	35		%
2f _o	2nd HARMONIC	P _{in} =1mW		-30	dBc
V _{SWR in}	INPUT VSWR			4	-
-	LOAD VSWR TOLERANCE	V _D D=15.2V,P _{in} =1mW,P _o =6W(V _G G adjust) Z _G =50 ohms, LOAD VSWR=20:1	No degradation or destroy		-

ABOVE PARAMETERS, RATINGS, LIMITS AND CONDITIONS ARE SUBJECT TO CHANGE .

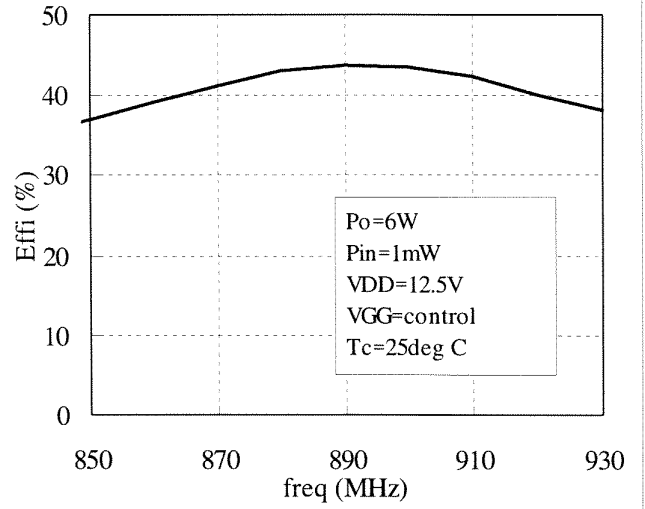
Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

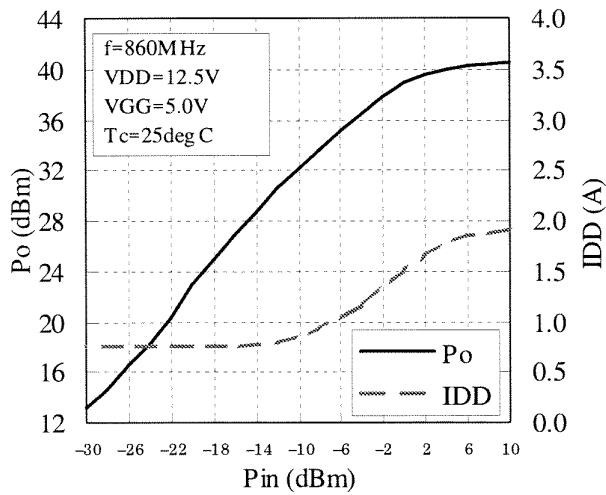
Po vs. freq



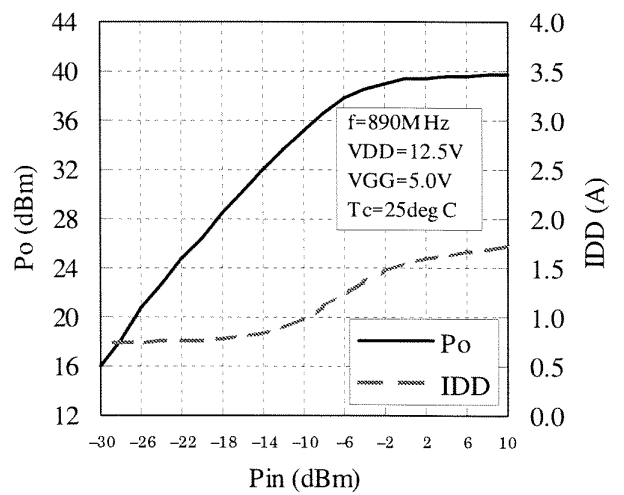
Efficiency vs. freq



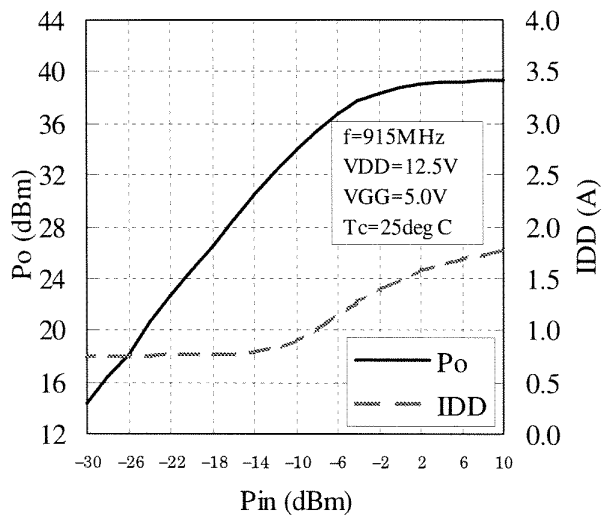
Po, IDD vs. Pin (860MHz)



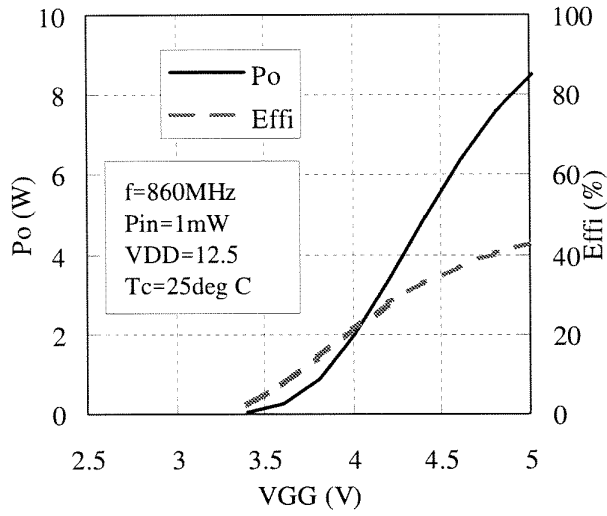
Po, IDD vs. Pin (890MHz)



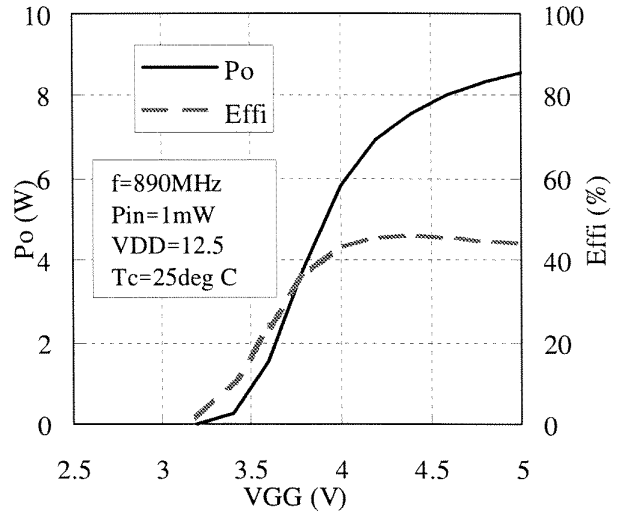
Po, IDD vs. Pin (915MHz)



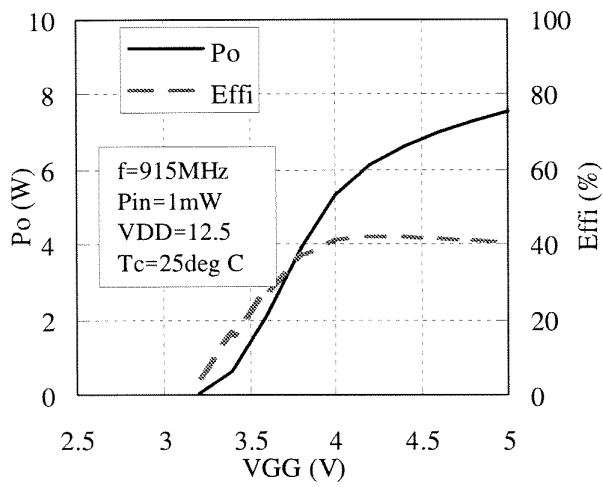
Po, Effi vs. VGG (860MHz)



Po, Effi vs. VGG (890MHz)



Po, Effi vs. VGG (915MHz)



Po vs. VDD

