



PIR CONTROLLER  
FOR RELAY OR TRIAC APPLICATION

## DESCRIPTION

The M7622 integrated circuit combines all required functions for a single chip Passive Infra-Red (PIR) light controller. It is designed for load switching with a TRIAC in 2 wire and 3 wire systems.

External potentiometers or resistors are used to set the operating parameters for sensitivity, on-time, brightness, fade, daylight sensor and environment temperature correction. The corresponding voltage levels are converted to digital values with a 4 bit Resolution. All signal processing is performed digitally.

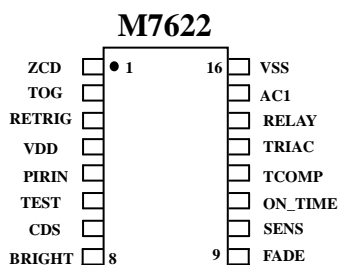
## FEATURES

- Digital signal processing
- On chip rectifier and supply regulator
- Temperature compensation input
- Adjustable soft on/off switching (fading)
- Light mode : Increase lamp brightness from set brightness to full brightness on movement.
- Light mode : Increase lamp brightness from off to set brightness on movement.
- Dimmer function
- Inductive load switching
- Drive either RELAY or TRIAC
- Suitable for 115V/60Hz and 230V/50Hz applications
- SOP-16 package

## APPLICATIONS

- PIR light controller, Motion Detector, Alarm system, Auto-door bell

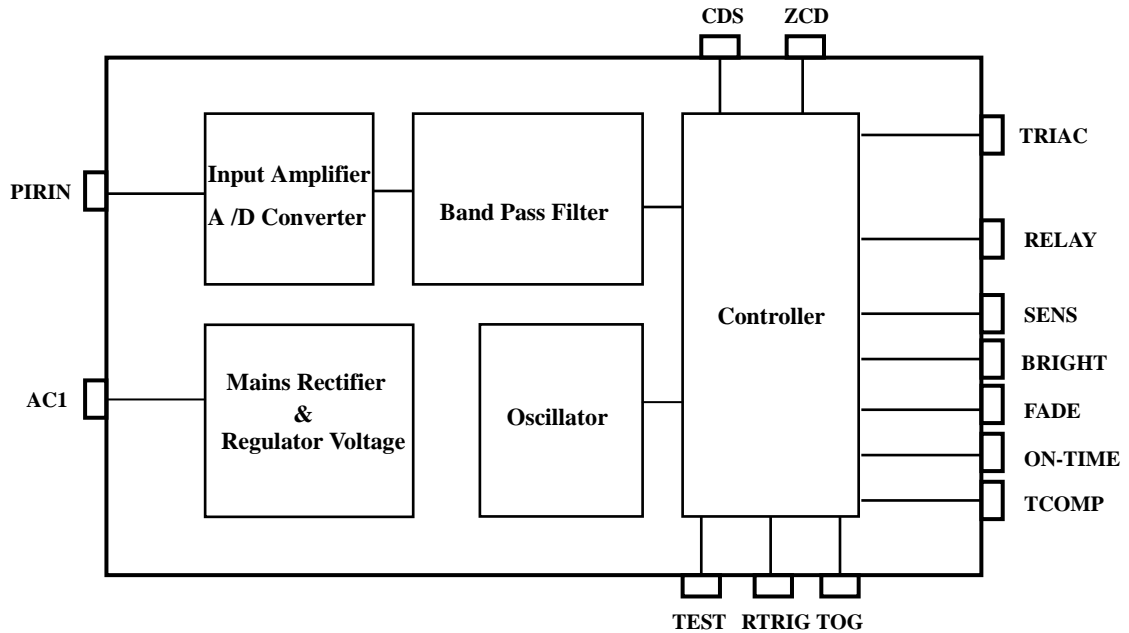
## PIN ASSIGMENT





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BLOCK DIAGRAM





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**PIN DESCRIPTION**

Pin No.	Name	I/O	Description
1	ZCD	I	Input pin for AC zero crossing detecting
2	TOG	I	Toggle mode OTSB and STFB mode select. If enabled a mains On – Off - On will switch the load permanently to set brightness if DARK. VDD *3/16 or greater : OTSB mode, mains toggling enabled. VDD *5/32 : STFB mode, mains toggling enabled. VDD *3/32 : STFB mode, mains toggling disabled VDD *1/16 or less : OTSB mode, mains toggling enabled
3	RETRIG	I	Retrigger mode select VDD or floating : The timer for the on-time is restarted whenever movement is detected. VSS : The light will stay on for the on-time. Movement detection is ignored during this period.
4	VDD	—	Digital VDD
5	PIRIN	I	PIR sensor input
6	TEST	I	Reserved , Test mode , connect to VSS
7	CDS	I	Dark mode input, connected to CDS/Photodiode VDD : Enable switching of the light VSS : Disables switching of the light Do not leave this input floating
8	BRIGHT	I	Brightness adjustment; The brightness levels are divided into 15 equal steps. One step corresponds to 90*(1- 1/16) degrees phase angle.
9	FADE	I	Fade time adjustment ◦ On and off fading takes equally long. Refer to <a href="#">Table 1</a>
10	SENS	I	Sensitivity threshold adjustment ◦ With 8 steps different threshold values are possible. Refer to <a href="#">Table 1</a>
11	ON-TIME	I	Light on-time adjustment. Refer to <a href="#">Table 1</a>
12	TCOMP	I	Temperature compensation input, A temperature dependent resistor network may be connected to this pin to generate voltages between VDD*16/128 and VDD*31/128. The voltage on this pin must decrease as the temperature increases. At 37°C, the voltage should be between VDD*19/128 and VDD*20/128. Internally. Refer to <a href="#">Table2</a> . TCOMP input may be connected to VSS if the application does not require a temperature compensation function.
13	TRIAC	O	TRIAC gate signal , push-pull
14	RELAY	O	RELAY output, push-pull The RELAY output is an active high output and changes state at the start of the fading cycle
15	AC1	I	Mains power input
16	VSS	—	Ground



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## FUNCTION DESCRIPTION

### Power-up Mode

After the device powers on it first enters a warm-up period. The light is switched on for the selected on-time duration. The CDS input is ignored on power-up, to allow the user to verify the installation during daylight conditions.

### Trigger Condition

The threshold voltage (SENS) is multiplied with the TCOMP factor (refer to [Table 1](#)), to obtain a temperature dependent threshold voltage (refer to [Table 2](#)). When the PIR signal exceeds this calculated threshold voltage, a trigger condition occurs.

### Conditions for Switching the Light ON (OTSB mode)

If a trigger condition occurs and the CDS input is high, the light will be switched on. The light's brightness will increase to the selected BRIGHTNESS within the selected FADE TIME.

The RELAY output is activated at the start of the fading-on cycle.

The relay will remain on and the light at the set BRIGHTNESS for the duration set by the ON-TIME input. The CDS input is ignored during this time.

If the TOGGLE MODE is enabled, the light can also be switched on by toggling the mains ON-OFF-ON within 3 seconds. In this mode, the PIR input is ignored; the CDS input will switch the light ON or OFF. This mode is disabled by disconnecting the mains for a few seconds.

### Conditions for Switching the Light OFF (OTSB mode)

The light fades to zero after the selected ON-TIME has elapsed, within the selected FADE TIME.

The RELAY output is switched off at the start of the fading-off cycle.

### Conditions for Switching the Light ON (STFB mode)

If the DARK input is high, the light will be on at the set BRIGHTNESS. If a trigger condition occurs, the light will be switched to full brightness. The light's brightness will increase to maximum within the selected FADE TIME.

The RELAY output is activated at the start of the fading-on cycle.

The relay output will be active and the light remains at full brightness for the duration set by the ON-TIME input.

If the TOGGLE MODE is enabled, the light can also be switched on to the set brightness by toggling the mains ON-OFF-ON within 3 seconds. In this mode, the PIR input is ignored; the CDS input will switch the light ON or OFF. This mode is disabled by disconnecting the mains for a few seconds.


The RELAY output is switched off at the start of the fading-off cycle.

### Condition for Switching the Light to set brightness (STFB mode)

The light will fade within the selected FADE TIME to the set BRIGHTNESS after the selected ON-TIME has elapsed.



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Pin voltage	On time	Fade time (50Hz)	Fade time (60Hz)	SENS distance
$(13/64) \times VDD \leq V_{in} < (15/64) \times VDD$	78 min	4.5 sec	3.7 sec	Near distance  Long distance
$(11/64) \times VDD \leq V_{in} < (13/64) \times VDD$	39 min	3.8 sec	3.2 sec	
$(9/64) \times VDD \leq V_{in} < (11/64) \times VDD$	20 min	3.2 sec	2.7 sec	
$(7/64) \times VDD \leq V_{in} < (9/64) \times VDD$	10 min	2.6 sec	2.1 sec	
$(5/64) \times VDD \leq V_{in} < (7/64) \times VDD$	5 min	1.9 sec	1.6 sec	
$(3/64) \times VDD \leq V_{in} < (5/64) \times VDD$	2.5 min	1.3 sec	1.1 sec	
$(1/64) \times VDD \leq V_{in} < (3/64) \times VDD$	37 sec	0.6 sec	0.5 sec	
$0 \leq V_{in} < (1/64) \times VDD$	9.2 sec	0 sec	0 sec	

**Table 1** : Parameter and operational settings related to pin voltage

Pin voltage	TCOMP factor	Pin voltage	TCOMP factor
$< (16/128) \times VDD$	7/8	$(24/128) \times VDD$	8/8
$(17/128) \times VDD$	6/8	$(25/128) \times VDD$	9/8
$(18/128) \times VDD$	5/8	$(26/128) \times VDD$	10/8
$(19/128) \times VDD$	4/8	$(27/128) \times VDD$	11/8
$(20/128) \times VDD$	4/8	$(28/128) \times VDD$	12/8
$(21/128) \times VDD$	5/8	$(29/128) \times VDD$	13/8
$(22/128) \times VDD$	6/8	$(30/128) \times VDD$	14/8
$(23/128) \times VDD$	7/8	$> (31/128) \times VDD$	15/8

**Table 2** : Temperature compensation factor

**Note:** At 37°C, the voltage should be between  $VDD \times 19/128$  and  $VDD \times 20/128$ .



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**M7622**

**ABSOLUTE MAXIMUM RATING**

(TA=25°C)

Parameter	Sym.	Min.	Max.	Unit	Remarks
Supply Voltage	VDD	-0.3	7	V	
Current On Any Pin		-100	100	mA	
Operating Temperature		-25	70	°C	
Storage temperature	Tst	-45	125	°C	

**ELECTRICAL CHARACTERISTICS**

(TA=25°C, VDD=4V)

Characteristics	Sym.	Min.	Typ.	Max.	Unit	Conditions
<b>Rectifier / Regulator</b>						
AC1, input current, t < 200us		—	500	—	mA	Repetitive @60Hz
Regulated supply voltage	VDD	3.6	—	4.5	V	
Supply current	IDD	—	—	0.3	mA	VDD=4.5V Outputs unloaded
<b>Oscillator and Filter</b>						
LPF cutoff frequency		—	7	—	Hz	
HPF cutoff frequency		—	0.5	—	Hz	
Clock frequency	F <sub>CLK</sub>	—	64	—	KHz	
<b>Analog Inputs</b>						
Input leakage current (ON-TIME, SENS, FADE, BRIGHT, TCOMP, TOG)		-1	—	1	μA	
PIRIN resistance to VSS		—	60	—	KΩ	
PIRIN input AC voltage		—	—	5	mV	Peak-to-Peak
PIRIN input DC voltage		0	—	VDD	V	
<b>Digital Inputs, Schmitt Triggers ( TEST, RETRIG, CDS )</b>						
Input low voltage	V <sub>IL</sub>	80	—	—	%VDD	
Input high voltage	V <sub>IH</sub>	—	—	20	%VDD	
Pull down current on TEST		—	140	—	μA	Input to VDD
Pull up current on RETRIG		—	140	—	μA	Input to VSS
Leakage current on CDS		—	—	±1	μA	Input to VSS or VDD
Pull up / Pull down scanning current on SWIN		—	—	6	mA	Input to VSS or VDD
<b>Digital Outputs ( TRIAC, RELAY )</b>						
Output low voltage	V <sub>OL</sub>	—	—	10	%VDD	I <sub>SOURCE</sub> = 5mA
Output high voltage	V <sub>OH</sub>	90	—	—	%VDD	I <sub>SINK</sub> = 2mA

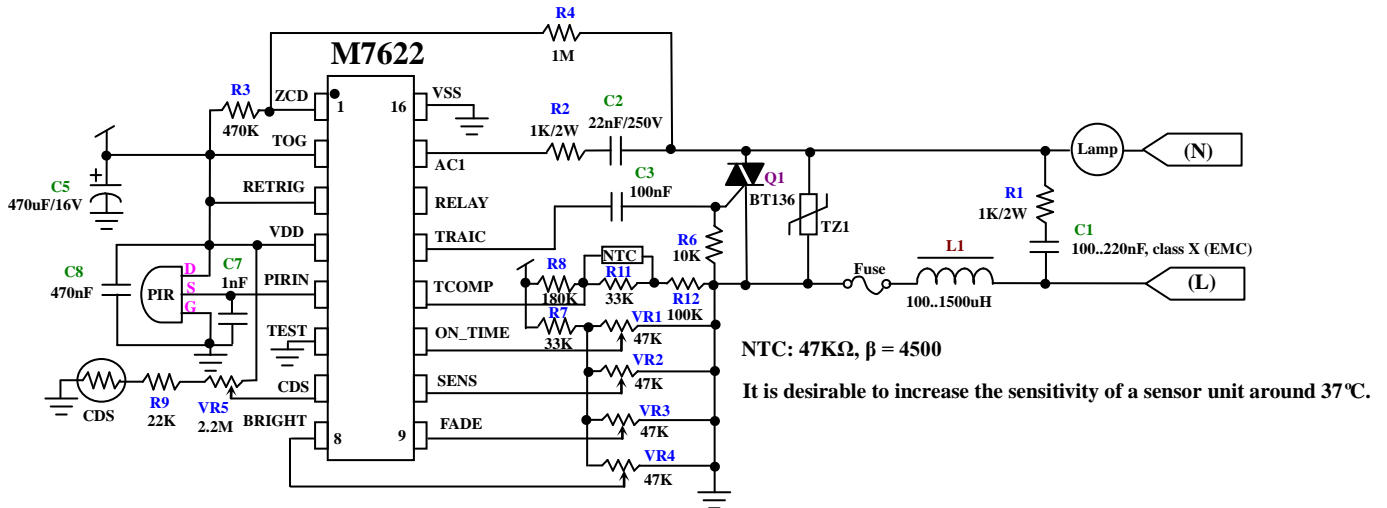


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M7622

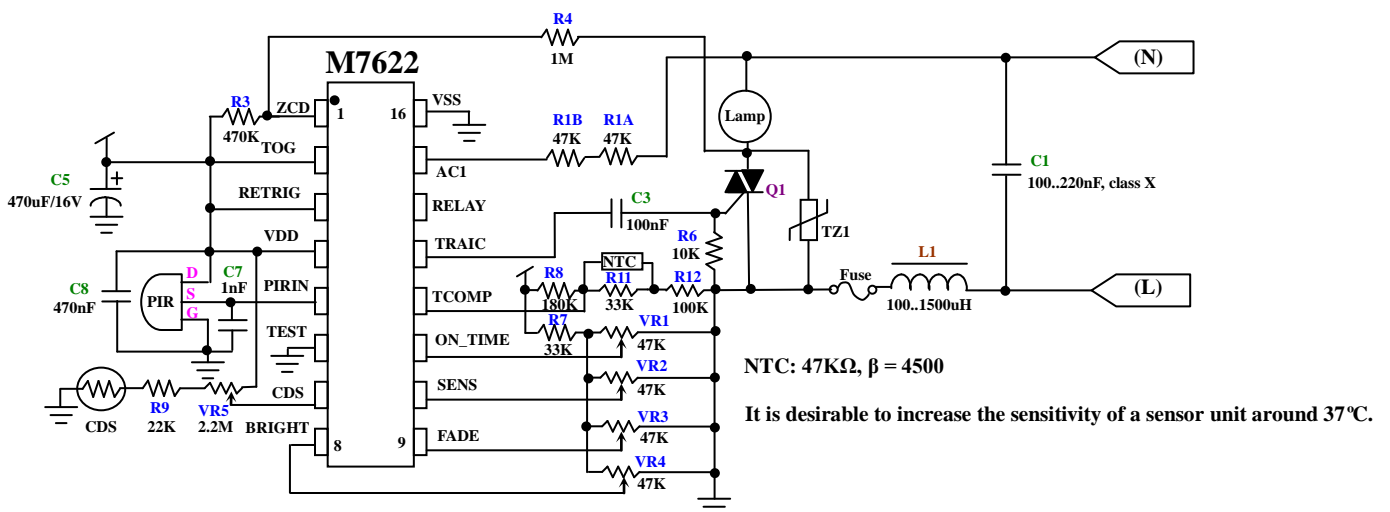
APPLICATION CIRCUIT

TRIAC Application Circuit (2 Wire)



RETRIG = VDD : Retrigger  
= VSS : Non-Retrigger

TRIAC Application Circuit (3 Wire)



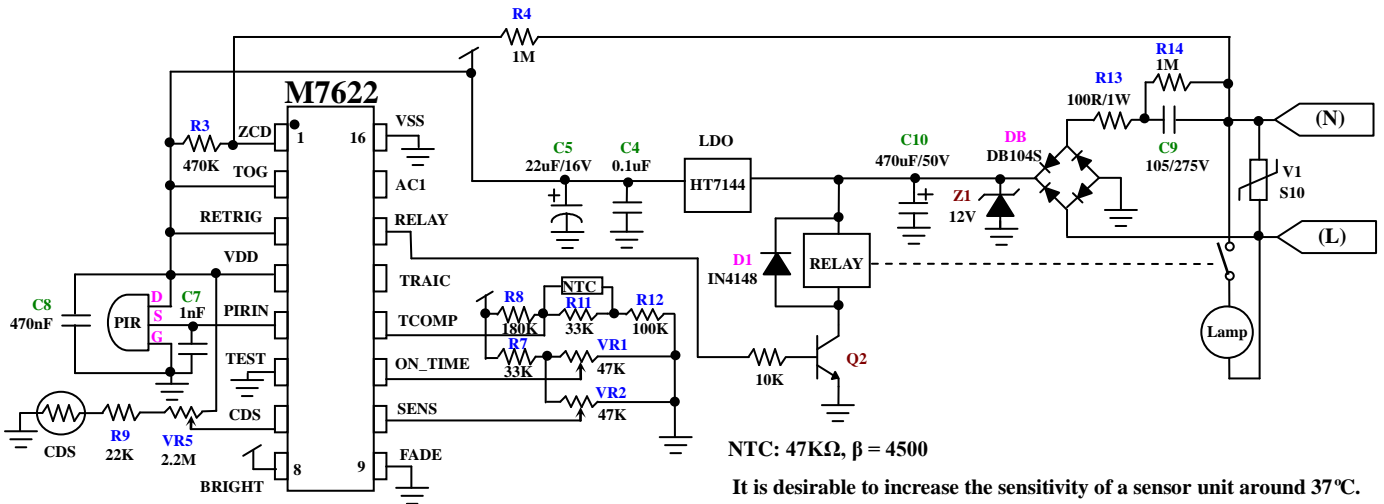
RETRIG = VDD : Retrigger  
= VSS : Non-Retrigger



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M7622

RELAY Application Circuit



RETRIG = VDD : Retrigger  
= VSS : Non-Retrigger

\* All specs and applications shown above subject to change without prior notice.

PACKAGE OUTLINE

