# **MA6X126** (MA126)

## Silicon epitaxial planar type

For switching circuit

#### ■ Features

- Four isolated elements contained in one package, allowing highdensity mounting
- High breakdown voltage:  $V_R = 80 \text{ V}$

### ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Reverse voltage	$V_R$	80	V	
Maximum peak reverse voltage	$V_{RM}$	80	V	
Forward current *1	$I_{F}$	100	mA	
Peak forward current *1	$I_{FM}$	225	mA	
Non-repetitive peak forward surge current *1, 2	$I_{FSM}$	500	mA	
Junction temperature	$T_{j}$	150	°C	
Storage temperature	$T_{stg}$	-55 to +150	°C	

Note) \*1: Value for single diode

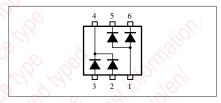
\*2: t = 1 s

### ■ Electrical Characteristics $T_a = 25$ °C $\pm 3$ °C

2.90 <sup>+0.20</sup> 1.9-0.1 (0.95), (0.95) 4 5 6 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8	Unit: mm
0.30+0.10	
0.30±0.10 0.50±0.10 0.50±0.10 0.50±0.10	
10°	<del>.</del> √⊘.
25. 65. 65. 65. 65. 65. 65. 65. 65. 65. 6	1: Anode 3, 4 2: Anode 1
	3: Anode 2
0 to 0.1	4: Cathode 1, 2 5: Cathode 3
(1/6), 0	6: Cathode 4
EIAJ: SC-74	Mini6-G1 Package

Marking Symbol: M2S

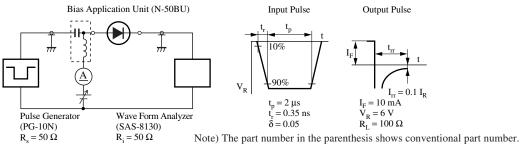
#### Internal Connection

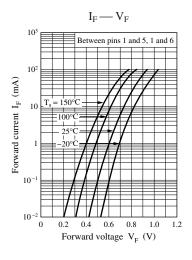


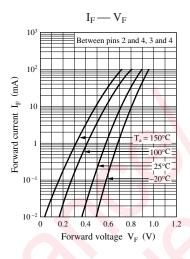
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	V <sub>F</sub>	$I_F = 100 \text{ mA}$	U)	·	1.2	V
Reverse voltage	$V_R$	$I_R = 100 \mu A$	80	80.		V
Reverse current	$I_R$	V <sub>R</sub> = 75 V	-01/	<i>J.</i>	100	nA
Terminal capacitance	C <sub>t1</sub> *1	$V_R = 0 \text{ V, } f = 1 \text{ MHz}$			15	pF
	C <sub>t2</sub> *2	6, 2,100	)		2	
Reverse recovery time *3	t <sub>rr1</sub> *1	$I_F = 10 \text{ mA}, V_R = 6 \text{ V}$			10	ns
	t <sub>rr2</sub> *2	$I_{rr} = 0.1 I_R$ , $R_L = 100 \Omega$			3	

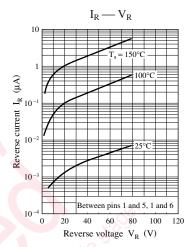
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

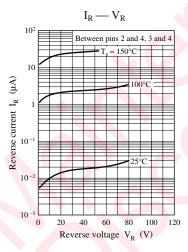
- 2. Absolute frequency of input and output is 100 MHz.
- 3. \*1: Between pins 1 and 5, Between pins 1 and 6
  - \*2: Between pins 4 and 2, Between pins 4 and 3
  - \*3: t<sub>rr</sub> measurement circuit

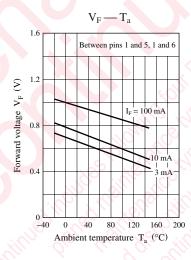


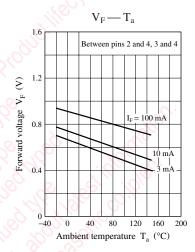


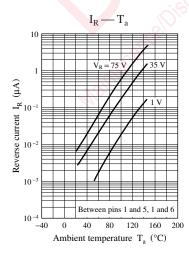


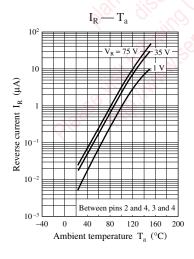


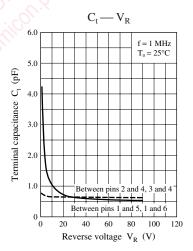




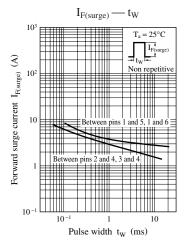








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