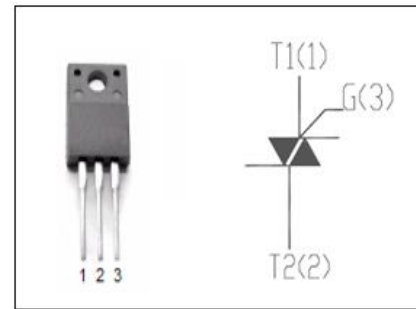


**isc Triacs**
**MAC223A10FP**
**FEATURES**

- With TO-220F Full Pack
- Off-State Voltages to 800 Volts
- Gate Triggering Guaranteed in Four Modes
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


**ABSOLUTE MAXIMUM RATINGS(Ta=25°C)**

SYMBOL	PARAMETER	MIN	UNIT
$V_{DRM}$	Repetitive peak off-state voltage	800	V
$V_{RRM}$	Repetitive peak reverse voltage	800	V
$I_{T(RMS)}$	RMS on-state current (full sine wave) $T_j=80^\circ\text{C}$	25	A
$I_{TSM}$	Non-repetitive peak on-state current $t_p=8.3\text{ms}$	250	A
$T_j$	Operating junction temperature	-40~125	$^\circ\text{C}$
$T_{stg}$	Storage temperature	-40~150	$^\circ\text{C}$
$R_{th(j-c)}$	Thermal resistance, junction to case	1.2	$^\circ\text{C/W}$

**ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT	
$I_{RRM}$	Repetitive peak reverse current	$V_R=V_{RRM}$ , $V_R=V_{RRM}$ , $T_j=125^\circ\text{C}$	0.01 2	mA	
$I_{DRM}$	Repetitive peak off-state current	$V_D=V_{DRM}$ , $V_D=V_{DRM}$ , $T_j=125^\circ\text{C}$	0.01 2	mA	
$I_{GT}$	Gate trigger current	$V_D=12\text{V}$ ; $R_L=30\ \Omega$	I	50	mA
			II	50	
			III	50	
			IV	75	
$I_H$	Holding current	$I_{GT}=0.1\text{A}$ , Gate Open	50	mA	
$V_{GT}$	Gate trigger voltage all quadrant	$V_D=12\text{V}$ ; $R_L=30\ \Omega$	2	V	
$V_{TM}$	On-state voltage	$I_T=35\text{A}$ ; $t_p=2\text{ms}$	1.85	V	

**NOTICE:**

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