

isc Triacs
MAC4DHMT4
DESCRIPTION

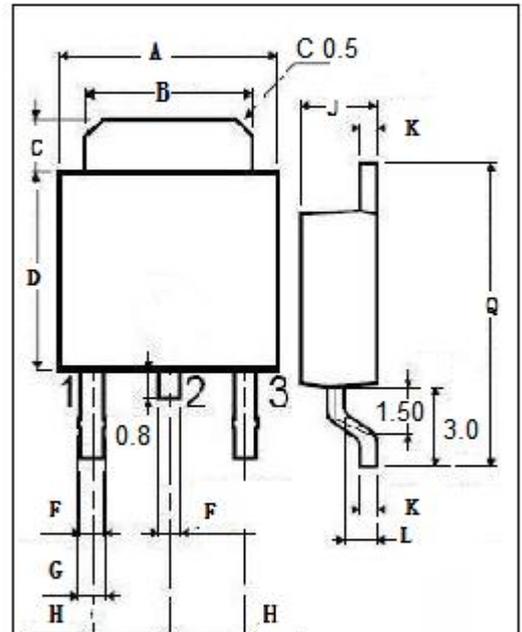
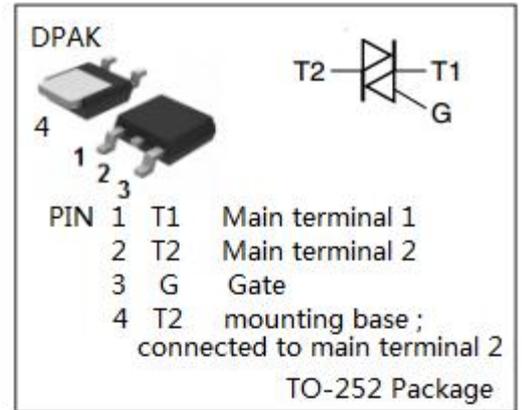
- Small size surface mount DPAK package
- Passivated die for reliability and uniformity
- Low level triggering and holding characteristics
- Triggering in all four quadrants
- Blocking voltage to 600V
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for high volume, low cost, industrial and consumer applications such as motor control; process control; temperature, light and speed control

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	600	V
I _{T(RMS)}	RMS on-state current (full sine wave; T _c =93°C)	4	A
I _{TSM}	Non-repetitive peak on-state current (T _j =110°C)	40	A
I ² t	I ² t for fusing t _p =8.3ms	6.6	A ² S
I _{GM}	Peak gate current	0.2	A
V _{GM}	Peak gate voltage	5	V
P _{GM}	Peak gate power dissipation	0.5	W
P _{G(AV)}	Average gate power dissipation	0.1	W
T _j	Operating junction temperature	-40~110	°C
T _{stg}	Storage temperature	-40~150	°C



DIM	mm	
	MIN	MAX
A	6.40	6.60
B	5.20	5.40
C	1.15	1.35
D	5.70	6.10
F	0.65	
G	0.75	
H	2.10	2.50
J	2.10	2.40
K	0.40	0.60
L	0.90	1.10
Q	9.90	10.1

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER		CONDITIONS	MIN	MAX	UNIT
I _{DRM}	Repetitive peak off-state current		V _D =V _{DRM} , V _D =V _{DRM} , T _j =110°C		0.01 2.0	mA
I _{GT}	Gate trigger current	I	V _D =12V; R _L = 100 Ω		5	mA
		II			5	
		III			5	
		IV			10	
V _{TM}	On-state voltage		I _T = 6A		1.6	V
I _H	Holding current		V _D = 12V		15	mA
I _L	Holding current	I	V _D = 12V, I _G =5mA		10	mA
		II	V _D = 12V, I _G =5mA		10	mA
		III	V _D = 12V, I _G =5mA		10	mA
		IV	V _D = 12V, I _G =10mA		10	mA
V _{GT}	Gate trigger voltage		V _D =12V; R _L = 100 Ω		1.3	V

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