

Isc Triacs

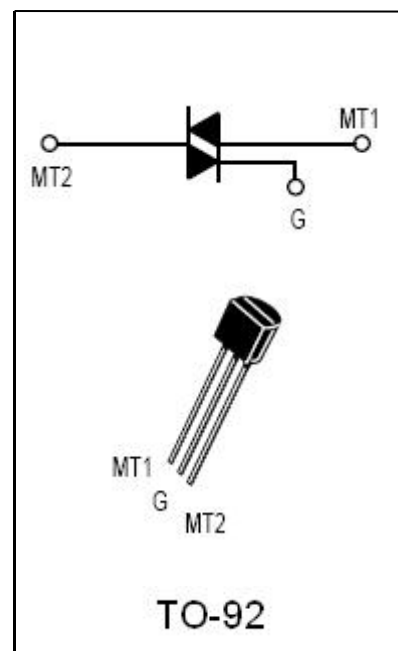
MAC97A8

FEATURES

- With TO-92 package
- Designed for use in solid state relays, MPU interface, TTL logic and any other light industrial or consumer application.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DRM}	Repetitive peak off-state voltage	600	V
V_{RRM}	Repetitive peak off-state voltage	600	V
$I_{\text{T(RMS)}}$	On-state RMS current	0.6	A
I_{TSM}	Peak non-repetitive surge current	8	A
P_{GM}	Peak gate power $t \leq 2 \mu\text{s}$	5.0	W
$P_{\text{G(AV)}}$	Average gate power $T_c=80^{\circ}\text{C}$, $t \leq 8.3\text{ms}$	0.1	W
T_j	Operating junction temperature	-40~+110	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-40~+ 150	$^{\circ}\text{C}$



ELECTRICAL CHARACTERISTICS ($T_c=25^{\circ}\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R = V_{\text{RRM}}$ $V_R = V_{\text{RRM}}$; $T_j = 110^{\circ}\text{C}$		10 200	μA
I_{DRM}	Repetitive peak off-state current	$V_D = V_{\text{DRM}}$ $V_D = V_{\text{DRM}}$; $T_j = 110^{\circ}\text{C}$		10 200	μA
I_{GT}	Gate trigger current	$V_D = 12\text{V}$; $R_L = 100 \Omega$	I	5	mA
			II	5	
			III	5	
			IV	7	
V_{TM}	On-state voltage	$I_T = 0.85\text{A}$		1.65	V
I_{H}	Holding current	$I_T = 0.2\text{A}$, Gate Open		5	mA
V_{GT}	Gate trigger voltage all quadrant	$V_D = 12\text{V}$; $R_L = 100 \Omega$		1.5	V

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