

### Features

- Low Series Resistance, 4  $\Omega$
- Low Capacitance, 45 fF
- High Cutoff Frequency
- Silicon Nitride Passivation
- Polyimide Scratch Protection
- Solderable Bump Die Attach

### Description

M/A-COM's MADS-001317-1320AG is a Gallium Arsenide Flip-Chip Schottky diode with solder bumps. These devices are fabricated on OMCVD epitaxial wafers using a process designed for high device uniformity and extremely low parasitics. This device can be used up to 80 GHz. This diode is fully passivated with silicon nitride and has an additional layer of a polymer for scratch protection. The protective coatings prevent damage to the junction during handling and circuit attachment.



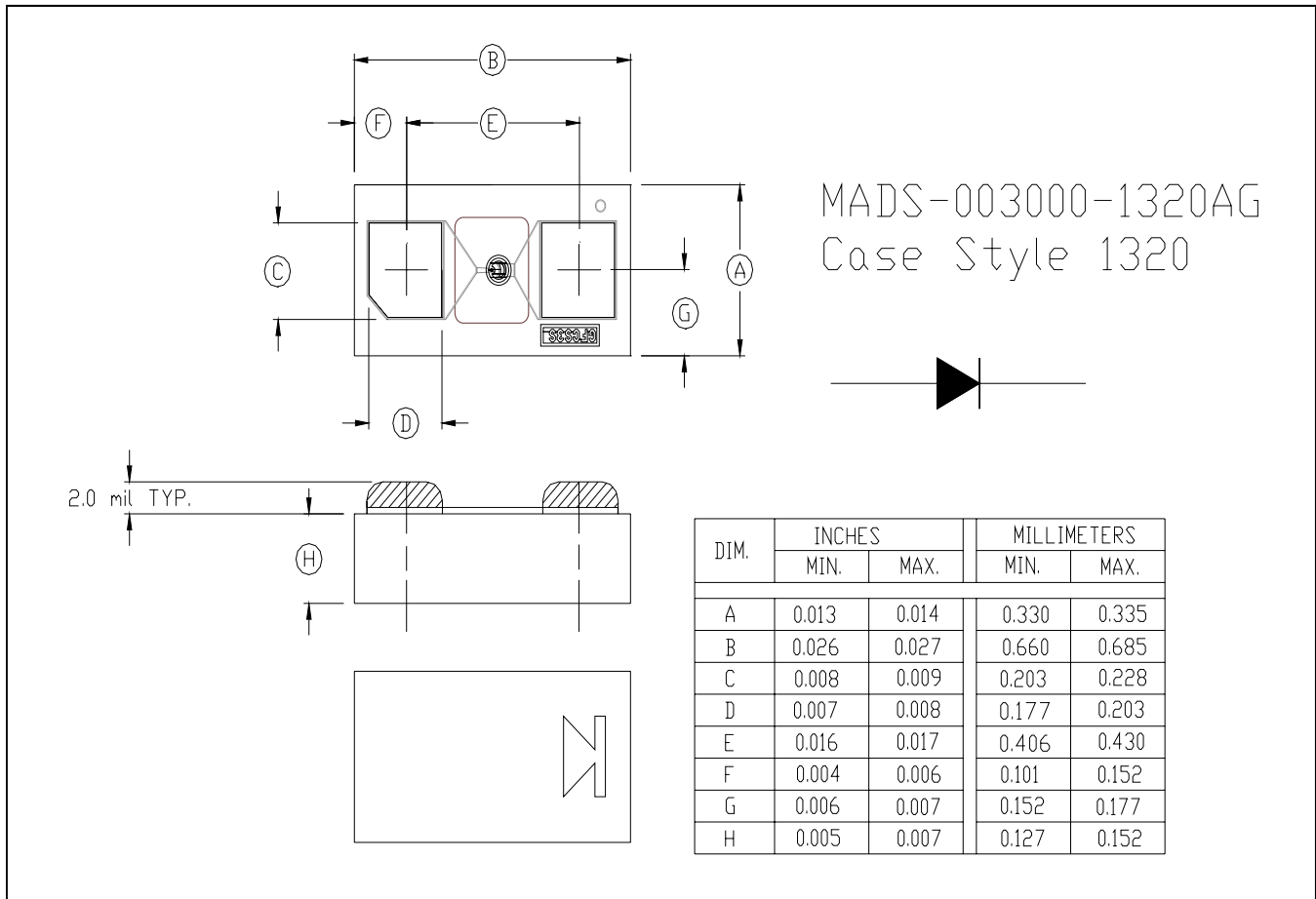
### Applications

The high cutoff frequency of this device allows use through millimeter wave frequencies. Typical Applications include single and double balanced mixers in PCN transceivers, radios, police radar detectors and automotive radar detectors.

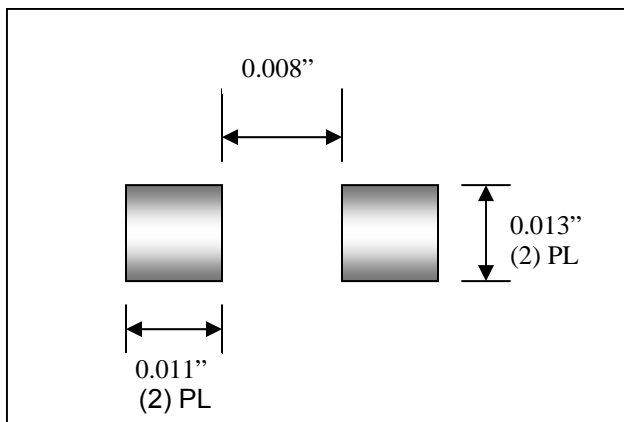
### Electrical Specifications $T_A = 25^\circ\text{C}$

Parameters and Test Conditions	Symbol	Units	Min.	Typ.	Max.
Junction Capacitance at 0V at 1 MHz	Cj	pF	—	0.020	—
Total Capacitance at 0V at 1 MHz <sup>1</sup>	Ct	pF	.030	0.045	0.060
Forward Voltage at 1mA	Vf	Volts	.60	.70	.80
Dynamic Resistance at 9.5 - 10.5 mA	Rd	Ohms	—	4	7
Reverse Breakdown Voltage at 10 $\mu\text{A}$	Vb	Volts	4.5	7	—

1. Total Capacitance is equivalent to the sum of junction capacitance (Cj) and parasitic capacitance (Cp).



### Circuit Mounting Dimensions (Inches)



## Device Installation Procedures

The following guidelines should be observed to avoid damaging GaAs Flip-Chips.

### Cleanliness

These devices should be handled in a clean environment. Do not attempt to clean die after installation.

### Static Sensitivity

Gallium Arsenide Schottky diodes are ESD sensitive and can be damaged by static electricity. Since Schottky diodes are rated as Class 0, proper ESD techniques should be used when handling these devices.

### General Handling

These devices have a polymer layer which provides scratch protection for the junction area and the anode air bridge. Die can be handled with plastic tweezers or picked and placed automatically with a #27 tip vacuum pencil.

Assembly Requirements using Tin Lead Solder

This Flip Chip Diode employs a 6  $\mu\text{m}$  thick, Sn 63/Pb 37 Solderable interface as part of the 50  $\mu\text{m}$  high solder bump. These chips are designed to be soldered onto hard or soft substrates with the junction side down. They should be mounted onto silkscreened circuits using 60/40 Sn/Pb solder paste. A typical profile for a Sn 63/Pb 37soldering process is provided in [Application Note, M538 Surface Mounting Instructions](#) on the M/A-COM website [www.macom.com](http://www.macom.com)

### Typical Spice Parameters

Is (A)	Rs ( $\Omega$ )	N	Ct0 (pF)	M	Ik (A)	Vj (V)	FC	BV (V)	IBV (A)
1.7 E-14	4.6	1.08	.047	.38	.016	.86	.99	7	1.0 E-5

### Absolute Maximum Ratings @ 25°C <sup>2</sup>

Parameter	Maximum Ratings
Operating Temperature	-65°C to +125°C
Storage Temperature	-65°C to +150°C
Incident LO Power	+20 dBm
Incident RF Power	+20 dBm
Mounting Temperature	+300°C for 10 seconds.

2. Exceeding these limits may cause permanent damage.

### Ordering Information

Part Number	Packaging
MADS-001317-1320AG	Gel Pack