

Rev. V3

#### **Features**

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5V Bias
- Lead-Free 5 mm 32-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

### **Description**

The MAOC-009268 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The MAOC-009268 primary applications are Point-to-Point Radio, Point-to-Multipoint Radio, Communications Systems, and Low Phase Noise applications.

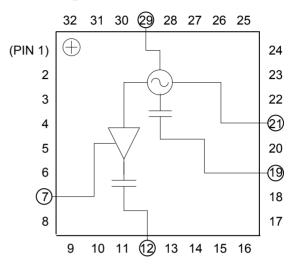
The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260°C reflow temperature. The package also features low lead inductance and an excellent thermal path.

## Ordering Information<sup>1</sup>

Part Number	Package
MAOC-009268-TR0500	500 piece reel
MAOC-009268-TR1000	1000 piece reel
MAOC-009268-SMB003	Sample Board

<sup>1.</sup> Reference Application Note M513 for reel size information.

### **Block Diagram**



## Pin Designations<sup>2</sup>

	Pin Function Pin Function			
FIII	Function	FIII	Function	
1	N/C	17	N/C	
2	N/C	18	N/C	
3	N/C	19	RF	
4	N/C	20	N/C	
5	N/C	21	V <sub>CC</sub>	
6	N/C	22	N/C	
7	V <sub>BUFFER</sub>	23	N/C	
8	N/C	24	N/C	
9	N/C	25	N/C	
10	N/C	26	N/C	
11	N/C	27	N/C	
12	RF/2	28	N/C	
13	N/C	29	$V_{TUNE}$	
14	N/C	30	N/C	
15	N/C	31	N/C	
16	N/C	32	N/C	

The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Rev. V3

## Electrical Specifications: $T_A = +25^{\circ}C$ , $V_{CC} = V_{BUFFER} = 5.0 \text{ V}^3$ , $Z_0 = 50 \Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Output Power	RF Port, 12.7 - 14.2 GHz RF/2 Port, 6.35 - 7.1 GHz	dBm	6 -3	9 1	
SSB Phase Noise $V_{CC}=V_{BUFFER}=V_{TUNE}=5V$	RF Port, 10KHZ Offset RF Port, 100KHZ Offset	dBc/Hz	_	-79 -108	_
Harmonics/Subharmonics V <sub>CC</sub> =V <sub>BUFFER</sub> =V <sub>TUNE</sub> =5V	RF Port, ${}^{1}I_{2}$ F <sub>o</sub> RF Port, 2 F <sub>o</sub>	dBc	_	-16 -38	_
Pulling (Sensitivity to Match) V <sub>CC</sub> =V <sub>BUFFER</sub> =V <sub>TUNE</sub> =5V	RF Port, VSWR = 1.95:1 to 2.25:1	MHz pk-pk	_	10	
Pushing (Sensitivity to Supply Voltage)	RF Port, $V_{TUNE} = 5 V$ RF/2 Port, $V_{TUNE} = 5 V$	MHz/V	_	10 5	_
Frequency Drift Rate (Sensitivity to Temperature)	RF Port, 12.7 - 14.2 GHz RF/2 Port, 6.35 - 7.1 GHz	MHz/°C	_	1.2 0.7	
Output Return Loss	RF Port, 12.7 - 14.2 GHz RF/2 Port, 6.35 - 7.1 GHz	dB	_	2.5 6	
Tuning Sensitivity @ RF Port	V <sub>TUNE</sub> = 5 V	GHz/V	_	0.21	_
Supply Current	I <sub>TOTAL</sub> (I <sub>CC</sub> + I <sub>BUFFER</sub> ) I <sub>CC</sub> I <sub>BUFFER</sub>	mA	_	165 145 20	205 175 30
Tune Voltage	$V_{TUNE}$	V	1		13
Tuning Current Leakage	V <sub>TUNE</sub> = 13 V	μA	_	5	10

<sup>3.</sup> VCO can operate over the 4.75 V to 5.25 V supply voltage range.

## **Absolute Maximum Ratings** 4,5,6

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Parameter	Absolute Maximum	
Supply Voltage (V <sub>CC</sub> & V <sub>BUFFER</sub> )	+5.5 Vdc	
V <sub>TUNE</sub>	0 to +15 Vdc	
Storage Temperature	-55°C to +150°C	
Operating Temperature	-40°C to +85°C	
Case Temperature (T <sub>C</sub> ) (measured @ exposed pad)	+100°C	
Junction Temperature <sup>7</sup>	+135°C	

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- MACOM does not recommend sustained operation near these survivability limits.
- 6. Operating at nominal conditions with  $T_J \le +135^{\circ}C$  will ensure MTBF > 2.5 x  $10^6$  hours.
- 7. Junction Temperature ( $T_J$ ) =  $T_C$  +  $\Theta$ jc \* (V \* I) Typical thermal resistance ( $\Theta$ jc) = 35° C/W. a) For  $T_C$  = 25°C,  $T_J$  = 54°C @ 5 V, 165 mA

b) For  $T_C = 85^{\circ}C$ ,  $T_J = 115^{\circ}C @ 5 V$ , 170 mA

### **Handling Procedures**

Please observe the following precautions to avoid damage:

### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



**ESD Rating: Class 1A** 

2

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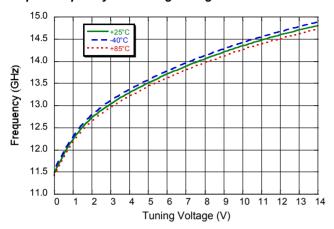
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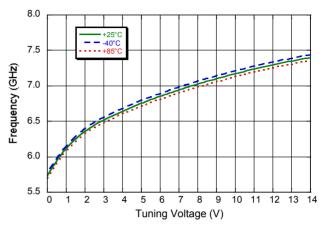
Rev. V3

## Typical Performance Curves: $V_{CC} = V_{BUFFER} = 5V$ , $T_A = +25^{\circ}C$ (unless otherwise indicated)

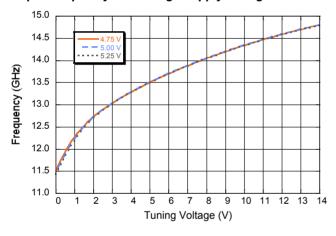
#### Output Frequency vs. Tuning Voltage - RF Port



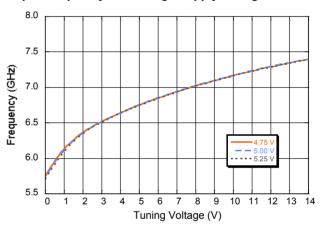
### Output Frequency vs. Tuning Voltage - RF/2 Port



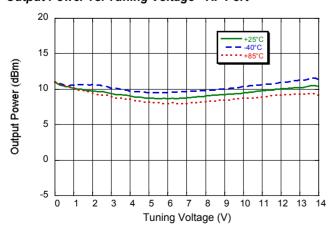
#### Output Frequency vs. Tuning / Supply Voltage - RF Port



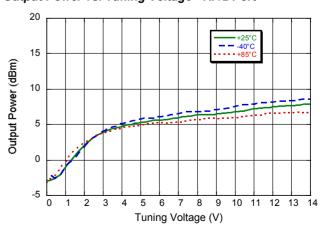
#### Output Frequency vs. Tuning / Supply Voltage - RF/2 Port



#### Output Power vs. Tuning Voltage - RF Port



### Output Power vs. Tuning Voltage - RF/2 Port



3

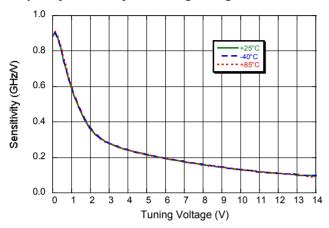
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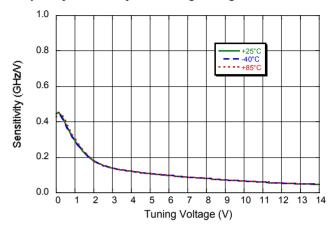
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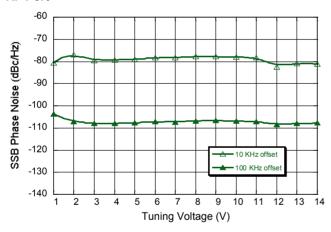
### Frequency Sensitivity vs. Tuning Voltage - RF Port



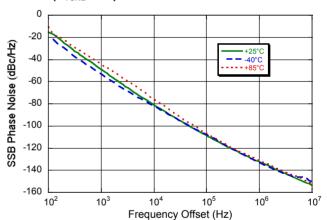
### Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



# Single Side Band Phase Noise vs. Tuning Voltage RF Port



Single Side Band Phase Noise vs. Frequency Offset RF Port  $(V_{TUNE} = 5V)$ 

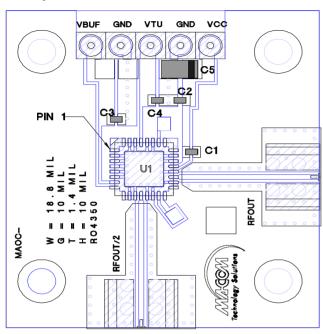


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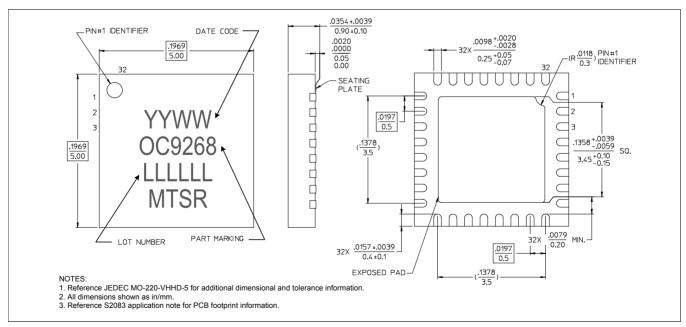
### **Sample Board**



### **Parts List**

Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 μF	0402
C5	10 μF Tantalum	1206

### Lead-Free 5 mm 32-Lead PQFN<sup>†</sup>



<sup>&</sup>lt;sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 3 requirements. Plating is 100% matte tin over copper.

5

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