

Rev. V1

Features

- Insertion Loss: 0.35 dB @ 1 GHz
- Lead-Free 1 mm 6-Lead PDFN Package
- Halogen-Free "Green" Mold Compound
- RoHS* Compliant and 260°C Reflow Compatible

Description

The MASW-009588 is a GaAs pHEMT MMIC single pole two throw (SP2T) switch in a miniature 1x1mm 6-lead PDFN package. The MASW-009588 is ideally suited for applications where low control voltage, low insertion loss, moderate isolation, and small size are required.

Typical applications are for filter and antenna switching in handset systems that connect separate receive functions to a common antenna, as well as other related handset and general purpose applications. This part can be used in all systems operating up to 4 GHz requiring high power at low control voltage.

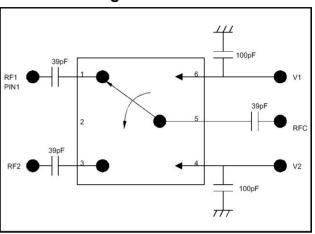
The MASW-009588 is fabricated using a 0.5 micron gate length GaAs pHEMT process. The process features full passivation for performance and reliability.

Ordering Information 1,2

| Part Number | Package |
|--------------------|-----------------|
| MASW-009588-000000 | Bulk |
| MASW-009588-TR3000 | 3000 piece reel |
| MASW-009588-001SMB | Sample Board |

- 1. Reference Application Note M513 for reel size information.
- 2. All sample boards include 5 loose parts.

Functional Diagram



Pin Configuration

| Pin No. | Function | Description | |
|---------|----------|-------------|--|
| 1 | RF1 | RF Port 1 | |
| 2 | GND | Ground | |
| 3 | RF2 | RF Port 2 | |
| 4 | V2 | Control 2 | |
| 5 | RFC | RF Common | |
| 6 | V1 | Control 1 | |

Absolute Maximum Ratings 3,4

| Parameter | Absolute Maximum | |
|--|------------------|--|
| Max Input Power (0.5 - 4 GHz, 2.6V Control) | +33 dBm | |
| Operating Voltage | +5 volts | |
| Operating Temperature | -40°C to +85°C | |
| Storage Temperature | -65°C to +150°C | |

- 3. Exceeding any one or combination of these limits may cause permanent damage to this device.
- 4. M/A-COM Technology does not recommend sustained operation near these survivability limits.

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^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.



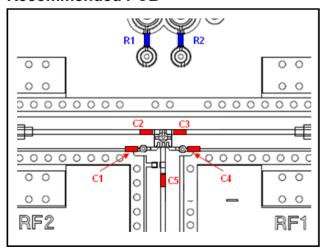
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Electrical Specifications: $T_A = 25^{\circ}C$, $V_C = 2.6V$, $Z_0 = 50 \Omega^5$

| Parameter | Test Conditions | Units | Min. | Тур. | Max. |
|----------------|---|-------|----------------------|---------------------------|-----------------------------|
| Insertion Loss | 1 GHz 2 GHz 3 GHz 4 GHz | dB | _ | 0.35 0.4 0.5 0.6 | 0.45 0.5 0.65 0.75 |
| Isolation | 1 GHz 2 GHz 3 GHz 4 GHz | dB | 23 17 13 11 | 26 20 16 14 | |
| VSWR | DC - 4 GHz | dB | _ | <1.3 | _ |
| IP3 | Two Tone +10 dBm, 5 MHz Spacing, >500 MHz P_{IN} = 10 dBm V_{C} = 0/2.6 V | dBm | _ | 55 | _ |
| P0.1dB | V _C = 0V/2.6V | dBm | _ | 26 | |
| P1dB | $V_{C} = 0V/2.6V$ | dBm | _ | 32 | _ |
| 2nd Harmonic | 1 GHz, +16 dBm | dBc | _ | 83 | |
| 3rd Harmonic | 1 GHz, +16 dBm | dBc | _ | 93 | _ |
| Trise, Tfall | 10% to 90% RF, 90% to 10% RF | ns | _ | 15 | |
| Ton, Toff | 50% control to 90% RF, and 50% control to 10% RF | ns | _ | 25 | |
| Transients | In Band | mV | _ | 30 | |
| Gate Leakage | VC = 2.6V | μΑ | _ | 0.2 | 5 |

^{5.} Insertion Loss can be optimized by varying the DC Blocking Capacitor value, ie. 1000 pF for 100 - 500 MHz, 39 pF for 0.5 - 4.0 GHz

Recommended PCB



Off-Chip Component Values

| Component | Value | Package |
|------------|--------|---------|
| C1, C4 | 100 pF | 0201 |
| C2, C3, C5 | 39 pF | 0201 |
| R1, R2 | 0 Ω | 0201 |

Truth Table ^{6,7}

| V1 | V2 | RFC - RF1 | RFC - RF2 |
|---------------|---------------|-----------|-----------|
| +1.6 to 3.5 V | 0 ± 0.2 V | On | Off |
| 0 ± 0.2 V | +1.6 to 3.5 V | Off | On |

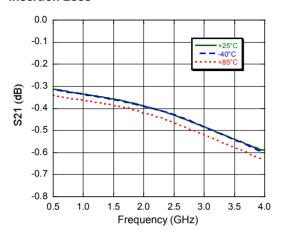
- 6. External DC blocking capacitors are required on all RF ports
- 7. Minimum Control Voltage Delta of 1.6V required



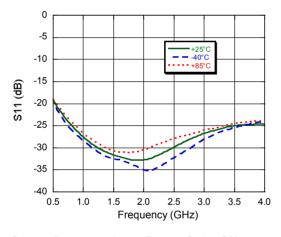
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Typical Performance Curves, V_{CTL} = 0/+2.6 V_{DC}

Insertion Loss



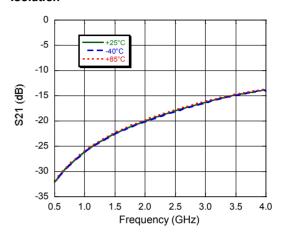
Input Return Loss



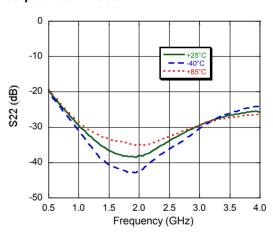
Output Power vs. Input Power @ 2.5 GHz



Isolation



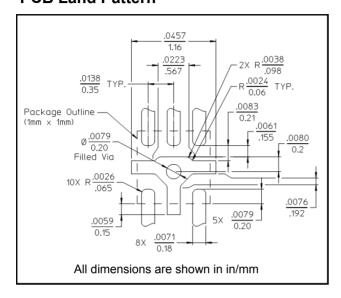
Output Return Loss





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PCB Land Pattern



Qualification

Qualified to M/A-COM specification REL-201, Process Flow –2.

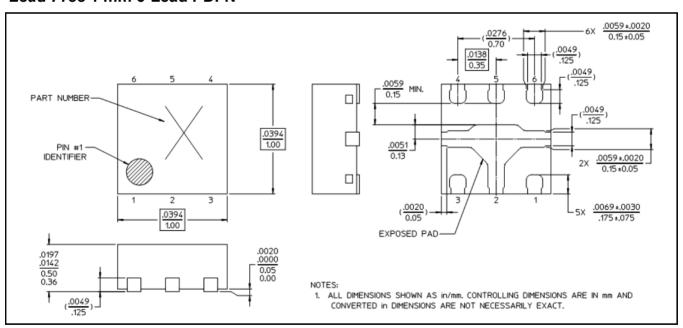
Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Lead-Free 1 mm 6-Lead PDFN[†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

MASW-009588



GaAs SP2T Switch DC - 4.0 GHz

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