

20Amp. Schottky Barrier Rectifiers
MBR20100AE3

$I_{F(AV)}$	$2 \times 10A$
V_{RRM}	100V
T_j	175°C
V_F	0.67V

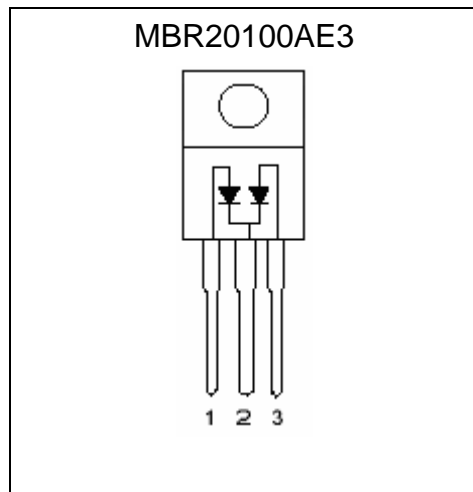
Features

- Low V_F and low I_R type
- Metal silicon junction, major carrier conduction
- High current capability
- Guardring for over voltage protection
- Low power loss, high efficiency
- High surge capability
- High temperature soldering guaranteed : 260°C/10s, 0.25”(6.35mm) from case
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection application
- RoHS compliant package

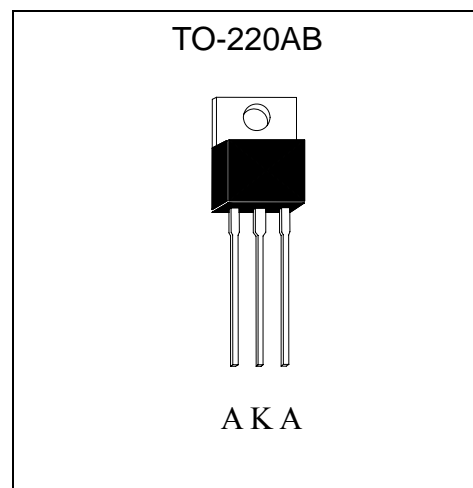
Mechanical Data

- Case: JEDEC TO-220AB molded plastic
- Mounting Position: Any
- Weight: 0.08 ounce, 2.24 grams
- Terminals: Pure tin plated, lead-free, solderable per MIL-STD-750 method 2026
- Epoxy: UL 94V-0 rate flame retardant
- Mounting Torque : 5 in-lbs max

Equivalent Circuit



Outline





Maximum Ratings and Electrical Characteristics (Per Diode Leg)

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

Parameter	Symbol	Min.	Typ.	Max.	Units
Maximum Recurrent peak reverse voltage	V_{RRM}			100	V
Maximum RMS voltage	V_{RMS}			70	V
Maximum DC blocking voltage	V_{DC}			100	V
Maximum instantaneous forward voltage at (Note 1)	$I_F=10A, T_C=25^\circ C$			0.85	V
	$I_F=10A, T_C=125^\circ C$		0.67	0.75	
	$I_F=20A, T_C=25^\circ C$			0.95	
	$I_F=20A, T_C=125^\circ C$			0.85	
Maximum Average forward rectified current @ $T_C=145^\circ C$	Per Diode			10	A
	Per Device			20	
Peak repetitive forward current (Rated V_R , square wave, 20kHz) @ $T_C=135^\circ C$	I_{FRM}			20	A
Peak forward surge current @ 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}			150	A
Peak repetitive reverse surge current (Note 1), $T_J < 175^\circ C$	I_{RRM}			3.0	A
Maximum instantaneous reverse current at	$V_R=100V, T_C=25^\circ C$			5.0	μA
	$V_R=100V, T_C=125^\circ C$			1.0	mA
Voltage rate of change, (rated V_R)	dV/dt			10,000	V/ μs
Typical junction capacitance @ $f=1MHz$ and applied 4V reverse voltage	C_J		260		pF
ESD susceptibility (Note 2)				8000	V
Storage temperature range	T_{stg}	-55		+175	$^\circ C$
Operating junction temperature range	T_J	-65		+175	$^\circ C$

Notes : 1. 2.0 μs pulse width, $f=1.0kHz$
 2. Human body model, 1.5k Ω in series with 100pF

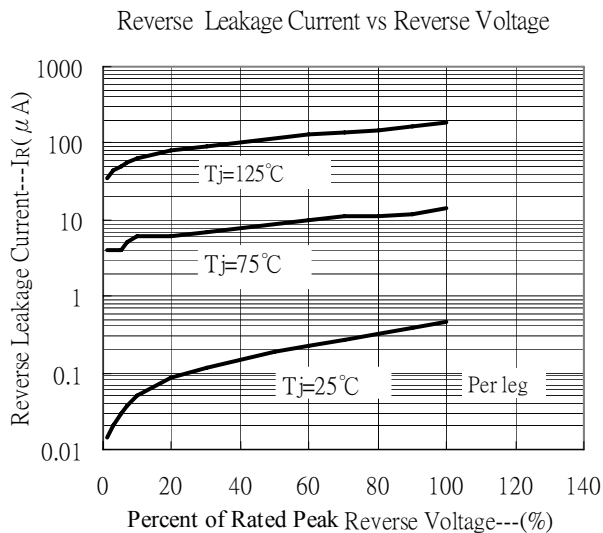
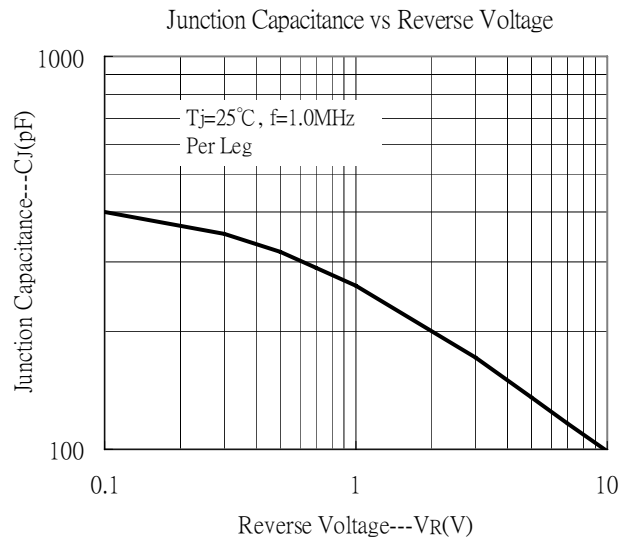
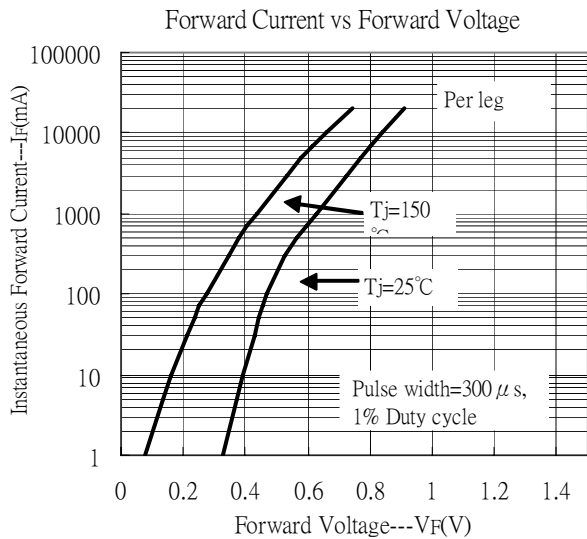
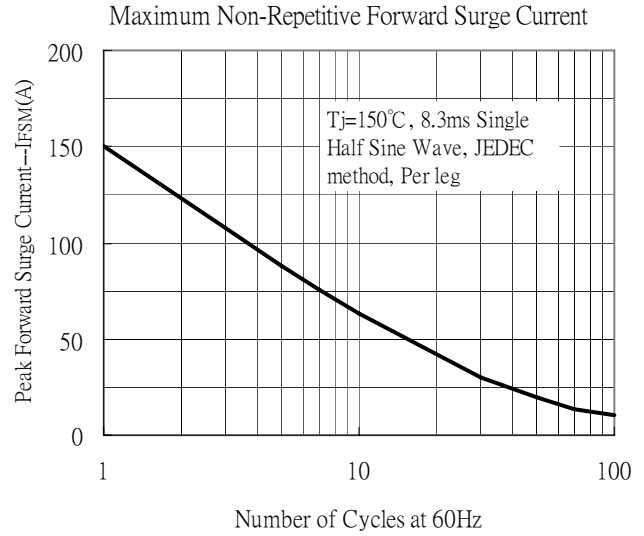
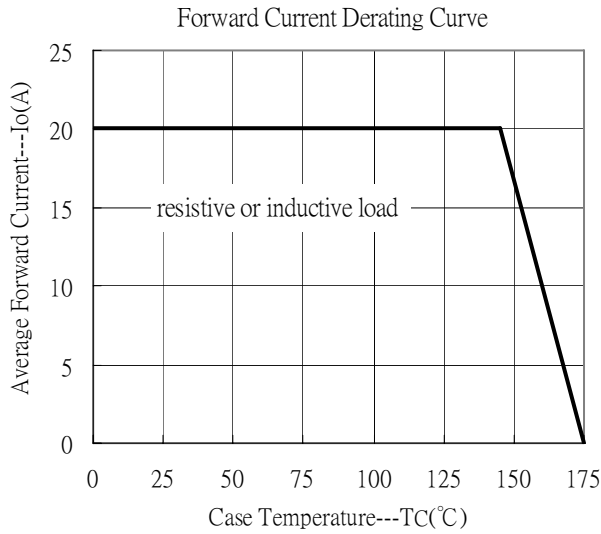
Thermal Data

Parameter	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-case	$R_{th,j-c}$	2	$^\circ C/W$
Maximum Thermal Resistance, Junction-to-ambient	$R_{th,j-a}$	60	$^\circ C/W$

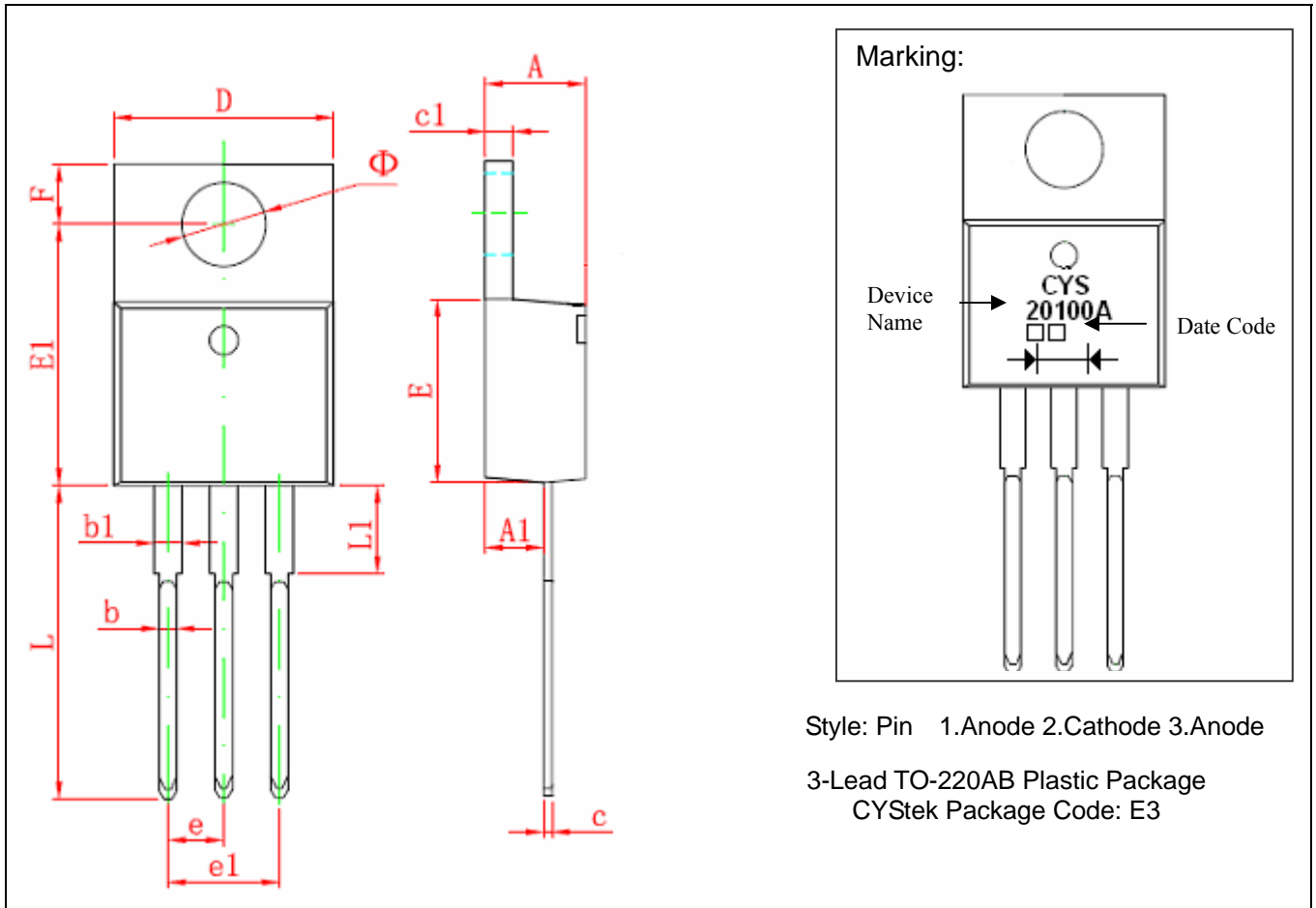
Ordering Information

Device	Package	Shipping	Marking
MBR20100AE3	TO-220AB (RoHS compliant package)	50 pcs / Tube, 40 Tubes/Box	20100A

Characteristic Curves



TO-220AB Dimension (C Forming)



*: Typicalφ

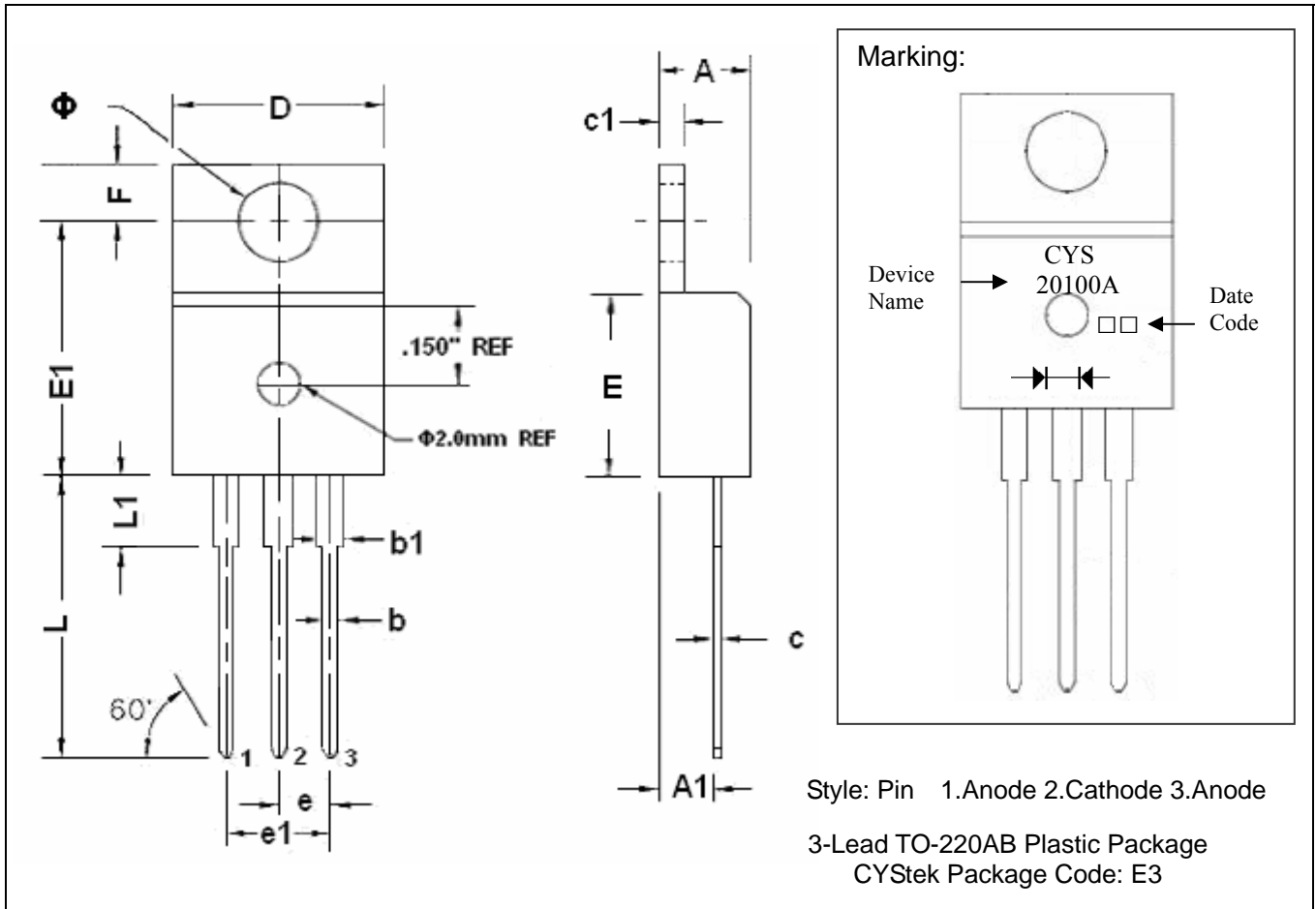
DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.176	0.184	4.470	4.670	E1	0.475	0.491	12.060	12.460
A1	0.099	0.111	2.520	2.820	e	*0.100		*2.540	
b	0.028	0.036	0.710	0.910	e1	0.196	0.204	4.980	5.180
b1	0.046	0.054	1.170	1.370	F	0.102	0.114	2.590	2.890
c	0.012	0.021	0.310	0.530	L	0.528	0.543	13.400	13.800
c1	0.046	0.054	1.170	1.370	L1	0.140	0.156	3.560	3.960
D	0.394	0.406	10.010	10.310	Φ	0.147	0.155	3.735	3.935
E	0.335	0.350	8.500	8.900					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: KFC ; tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

TO-220AB Dimension (M Forming)



*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.170	0.180	4.320	4.570	E1	0.478	0.508	12.140	12.900
A1	0.100	0.110	2.540	2.790	e	*0.100		*2.540	
b	0.028	0.036	0.710	0.910	e1	0.200	BSC	5.080	BSC
b1	0.045	0.055	1.150	1.390	F	0.102	0.112	2.600	2.840
c	0.014	0.021	0.360	0.530	L	0.530	0.550	13.470	13.970
c1	0.048	0.054	1.220	1.370	L1	0.130	0.150	3.310	3.810
D	0.395	0.410	10.040	10.410	Φ	0.149	0.153	3.790	3.880
E	0.335	0.375	8.509	9.520					

Notes: 1.Controlling dimension: millimeters.
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Material:

- Lead: KFC ; tin plated
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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