

MBRS4201P

200 V, 4 A Schottky Fast Soft-Recovery Power Rectifier

SMC Power Surface Mount Package

Features

- Lower Forward Voltage than any Ultrafast Rectifier:
 $V_F < 0.61 \text{ V}$ at 150°C
- Fast Switching Speed: Reverse Recovery Time (t_{RR}) $< 35 \text{ ns}$
- Soft Recovery Characteristics: Softness Factor (t_b/t_d) ≥ 1
- Highly Stable Over Temperature
- These are Pb-Free Packages

Benefits

- Significantly Reduced EMI
- Eliminates the Need of Snubber Circuits
- Low Switching and Heat Losses
- Improved Thermal Management

Applications

- Engine and Convenience Control Systems
- Motor Controls
- Battery Chargers and Switching Power Supplies

Mechanical Characteristics

- Small Compact Surface Mount Package with J-Bend Leads
- Rectangular Package for Automated Handling
- Weight: 217 mg (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds
- ESD Ratings:
 - ♦ Machine Model = A
 - ♦ Human Body Model = 1C
- Cathode Polarity Band



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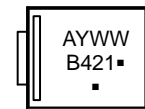
SCHOTTKY RECTIFIER 4 AMPS, 200 VOLTS



SMC
CASE 403AC



MARKING DIAGRAM



B421 = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
MBRS4201PT3G	SMC (Pb-Free)	2,500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	200	V
Average Rectified Forward Current (Rated V_R , $T_L = 70^\circ\text{C}$)	$I_{F(AV)}$	4	A
Nonrepetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	100	A
Operating Junction Temperature	T_J	-55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead	$R_{\theta JL}$	11	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage ($I_F = 4\text{ A}$, $T_J = 25^\circ\text{C}$) ($I_F = 4\text{ A}$, $T_J = 150^\circ\text{C}$)	V_F	0.86 0.62	V
Maximum Instantaneous Reverse Current (Rated V_R) (Rated DC Voltage, $T_J = 25^\circ\text{C}$) (Rated DC Voltage, $T_J = 150^\circ\text{C}$)	I_R	1.0 5.0	mA mA
Maximum Reverse Recovery Time ($I_F = 1.0\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$)	t_{rr}	35	ns

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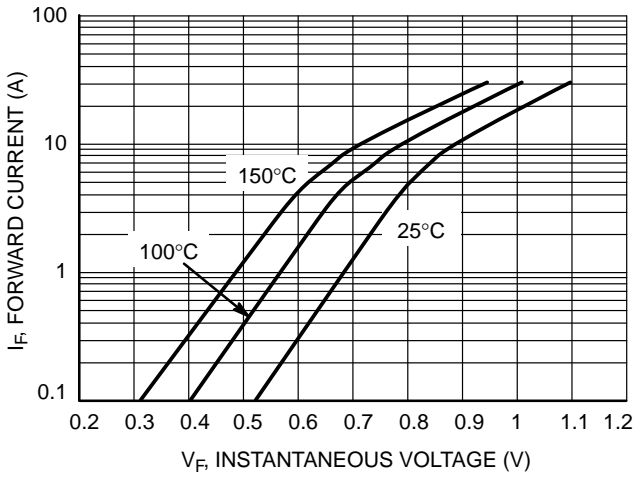


Figure 1. Typical Forward Voltage

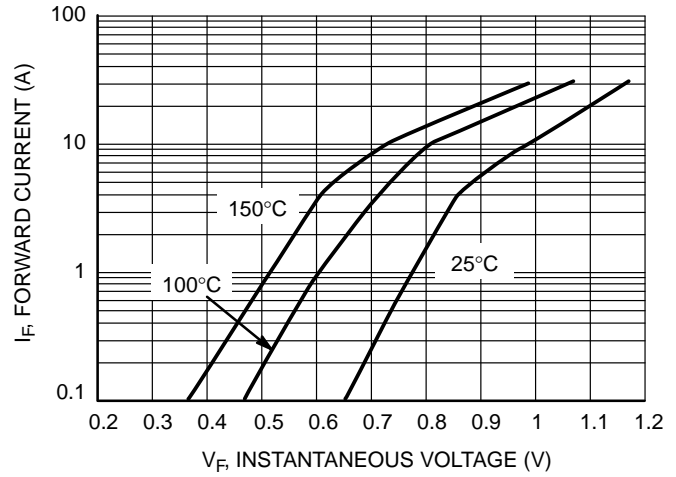


Figure 2. Maximum Forward Voltage

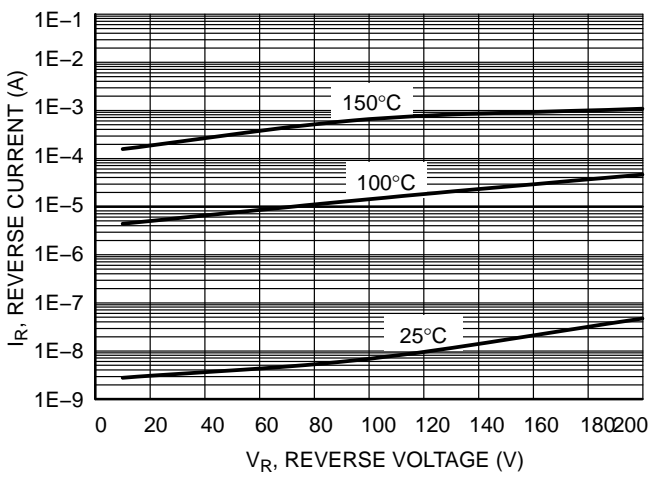


Figure 3. Typical Reverse Current

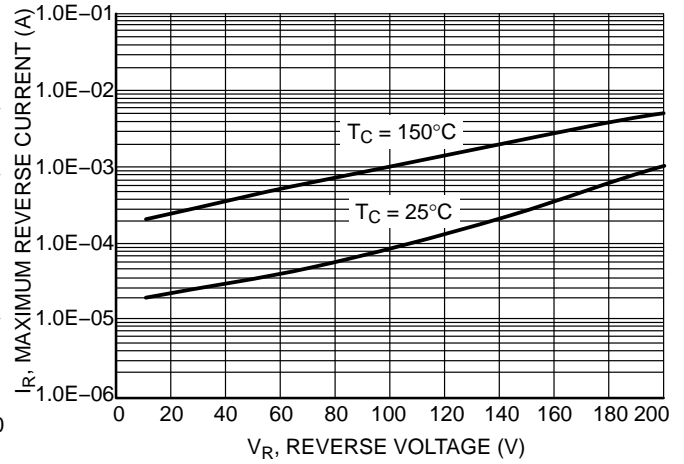


Figure 4. Maximum Reverse Current

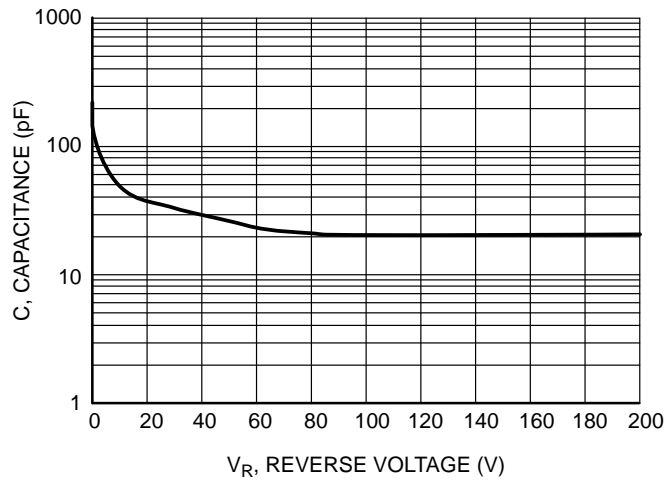


Figure 5. Typical Capacitance

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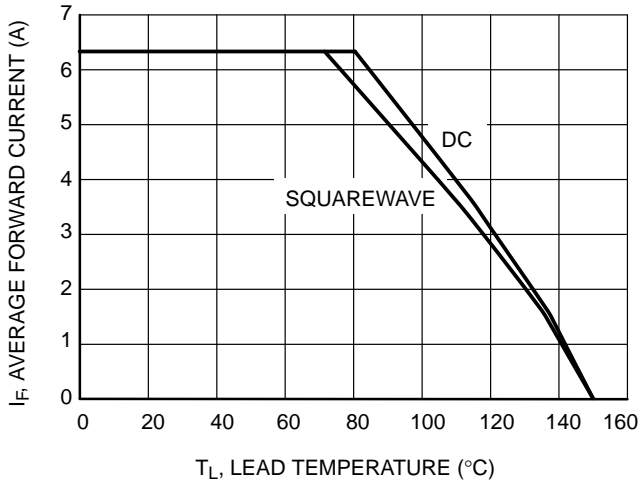


Figure 6. Derating Curve

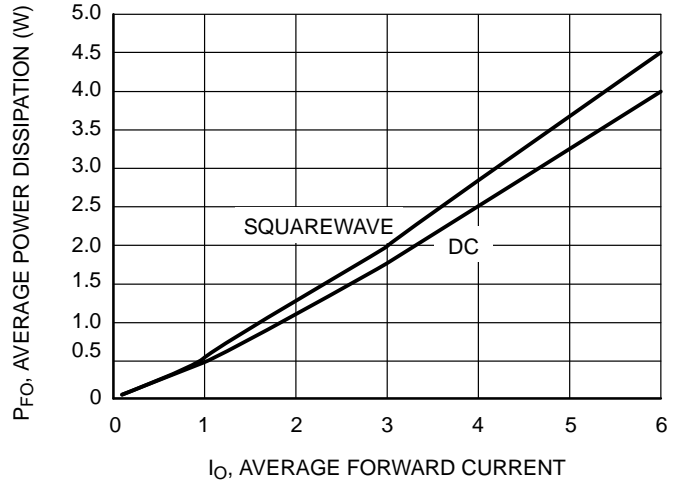
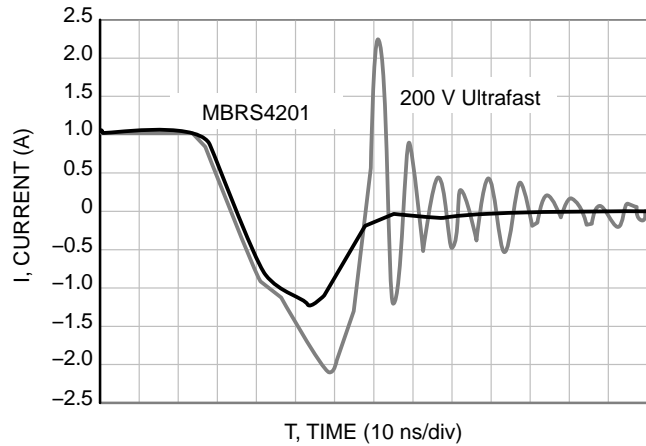


Figure 7. Power Dissipation



ON Semiconductor MBRS4201 eliminates reverse recovery oscillations present in Ultrafast devices in the market, particularly at hot temperatures.

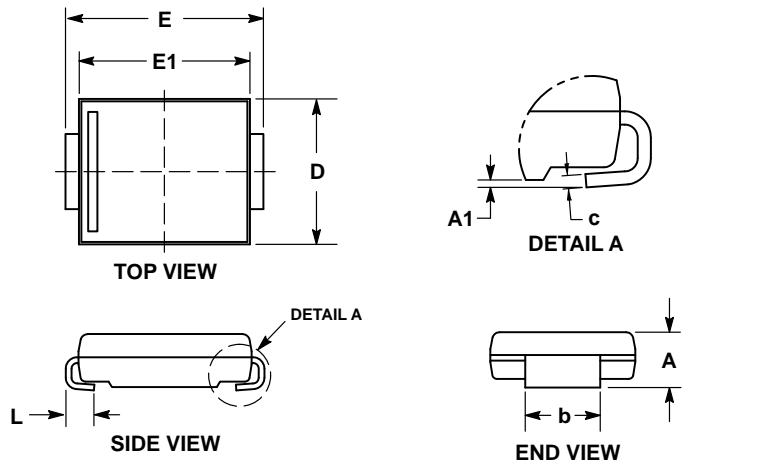
*Test Conditions:
 $I_F = 1 \text{ A}$, $d_i/d_T = 100 \text{ A}/\mu\text{s}$,
 $V_R = 30 \text{ V}$

Figure 8. Reverse Recovery Time* (t_{RR}) at 125°C

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PACKAGE DIMENSIONS

SMC 2-LEAD CASE 403AC ISSUE O

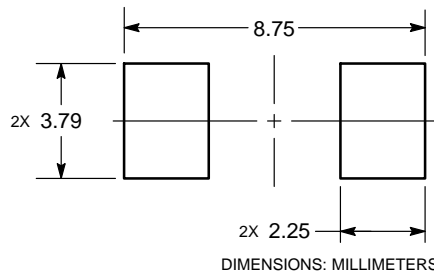


NOTES:


1. DIMENSIONING AND TOLERANCING PER ANME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE.
4. DIMENSIONS D AND E1 TO BE DETERMINED AT DATUM H.
5. DIMENSION b SHALL BE MEASURED WITHIN THE AREA DETERMINED BY DIMENSION L.

DIM	MILLIMETERS	
	MIN	MAX
A	1.95	2.65
A1	0.05	0.20
b	2.90	3.20
c	0.15	0.41
D	5.55	6.25
E	7.75	8.15
E1	6.60	7.15
L	0.75	1.60

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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