



ARCHIVED BY FREESCALE SEMICONDUCTOR, INC. 2005 1.1 GHz Prescaler

The MC12080 is a single modulus divide by 10, 20, 40, 80 prescaler for low power frequency division of a 1.1 GHz high frequency input signal. Divide ratio control inputs SW1, SW2 and SW3 select the required divide ratio of \div 10, \div 20, \div 40, or \div 80.

An external load resistor is required to terminate the output. A 820 Ω resistor is recommended to achieve a 1.2 V_{pp} output swing, when dividing a 1.1 GHz input signal by the minimum divide by ratio of 10, assuming a 8.0 pF load. Output current can be minimized dependent on conditions such as output frequency, capacitive load being driven, and output voltage swing required. Typical values for load resistors are included in the V_{out} specification for various divide ratios at 1.1 GHz input frequency.

- 1.1 GHz Toggle Frequency
- Supply Voltage 4.5 to 5.5 V
- Low Power 3.7mA Typical at V_{CC} = 5.0 V
- Operating Temperature Range of –40 to 85°C

FUNCTIONAL TABLE

SW1	SW2	SW3	Divide Ratio
L	L	L	80
L	L	Н	40
L	Н	L	40
L	Н	Н	20
Н	L	L	40
Н	L	Н	20
Н	Н	L	20
Н	Н	Н	10

NOTE: SW1, SW2 and SW3: $H = V_{CC}$, L = Open.

MAXIMUM RATINGS

Characteristic	Symbol	Range	Unit
Power Supply Voltage, Pin 2	VCC	-0.5 to 7.0	Vdc
Operating Temperature Range	TA	-40 to 85	°C
Storage Temperature Range	T _{stg}	-65 to 150	°C
Maximum Output Current, Pin 4	lo	10	mA

NOTE: ESD data available upon request.

MC12080

MECL PLL COMPONENTS +10/20/40/80 PRESCALER

SEMICONDUCTOR TECHNICAL DATA



D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO-8, Tape and Reel Only)

PIN CONNECTIONS

In 1	0	8	Īn
V _{CC} 2		7	SW3
SW1 3		6	SW2
Out 4		5	Gnd
(Top Viev	v)	

ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC12080DR2	$T_A = -40 \text{ to } 85^{\circ}\text{C}$	SO-8

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ELECTRICAL CHARACTERISTICS ($V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$; $T_A = -40 \text{ to } 85^{\circ}\text{C}$, unless otherwise noted.)

Parameter	Symbol	Min	Тур	Max	Unit
ARGIN FEDERY (SIREWAY) CALE SEMICONDUCTOR, IN	IC. 2 005	0.1	1.4	1.1	GHz
Supply Current Output (Pin 2)	Icc	I	3.7	5.0	mA
Input Voltage Sensitivity 100 to 250 MHz 250 to 1100 MHz	V _{in}	400 100	- -	1000 1000	mVpp
Divide Ratio Control Input High (SW1, SW2, SW3)	VIH	V _{CC} – 0.5 V	Vcc	V _{CC} + 0.5 V	V
Divide Ratio Control Input Low (SW1, SW2, SW3)	V _{IL}	Open	Open	Open	-
Output Voltage Swing [Note] $R_L = 820~\Omega,~I_O = 4.0~\text{mA for } \div 10$ $R_L = 1.6~\text{k}\Omega,~I_O = 2.1~\text{mA for } \div 20$ $R_L = 3.3~\text{k}\Omega,~I_O = 1.1~\text{mA for } \div 40$ $R_L = 6.2~\text{k}\Omega,~I_O = 0.57~\text{mA for } \div 80$	Vout	0.8	1.2	-	V _{pp}

NOTE: Assumes 8.0 pF load and 1.1 GHz input frequency (typical), IO at VCC = 5.0 V and TA = 25°C

Figure 1. Logic Diagram

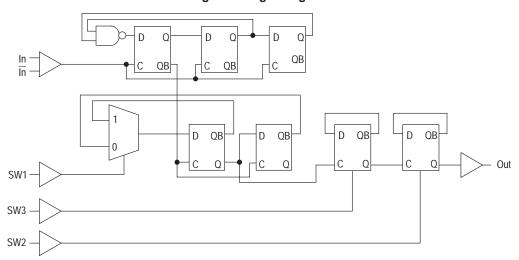
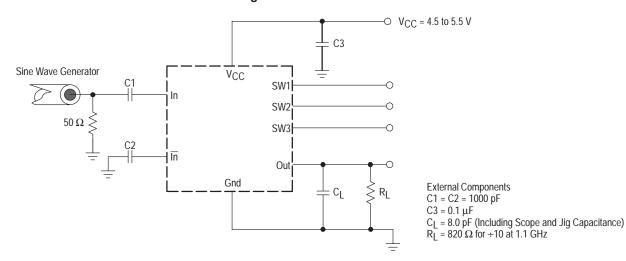


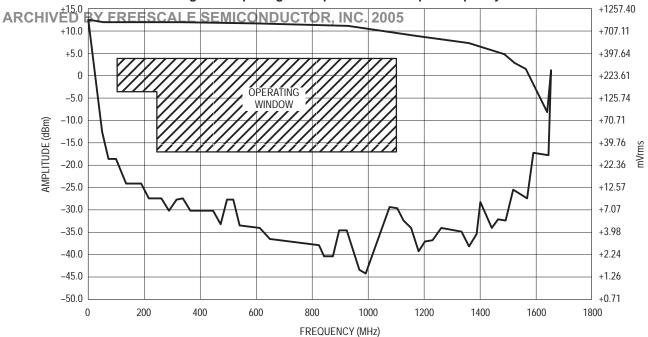
Figure 2. AC Test Circuit





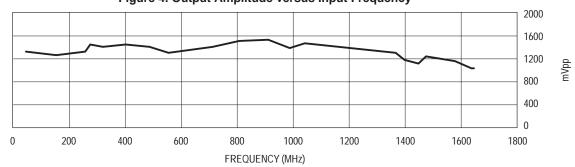
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Figure 3. Input Signal Amplitude versus Input Frequency



Divide Ratio = 10; V_{CC} = 5.0 V; T_A = 25°C

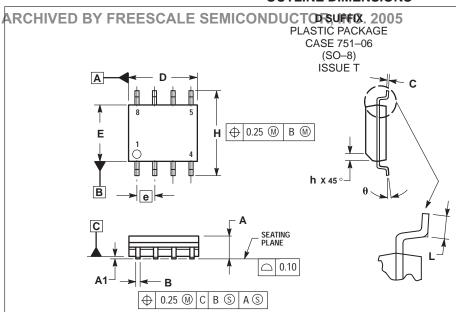
Figure 4. Output Amplitude versus Input Frequency





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OUTLINE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME
 - DIMENSIONS ARE IN MILLIMETER.
- DIMENSION D AND E DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
В	0.35	0.49	
С	0.19	0.25	
D	4.80	5.00	
Ε	3.80	4.00	
е	1.27 BSC		
Н	5.80	6.20	
h	0.25	0.50	
L	0.40	1.25	
θ	0 °	7 °	

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