



MC33078

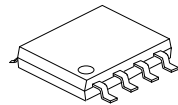
Preliminary

BIPOLAR IC

DUAL LOW NOISE OPERATIONAL AMPLIFIERS

DESCRIPTION

The UTC **MC33178** is a Bipolar dual operational amplifier with high performance concepts. Its all NPN output stage exhibits no deadband crossover distortion, large output voltage swing, and an excellent phase and gain margin. It also provides a low open loop high frequency output impedance with symmetrical source and sink AC frequency performance. This device use of high frequency PNP input transistors to produce amplifiers exhibiting low input voltage noise with high gain bandwidth product and slew rate.



SOP-8

FEATURES

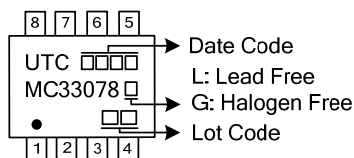
- * Dual supply operation from: $\pm 5.0\text{ V}$ to $\pm 18\text{ V}$
- * Low input offset voltage: 0.15 mV
- * Large output voltage swing: $+14.1\text{ V} / -14.6\text{ V}$
- * High slew rate: $7.0\text{ V}/\mu\text{s}$
- * Low voltage noise: $4.5\text{ nV}/\sqrt{\text{Hz}}$
- * Low total harmonic distortion: 0.002%
- * High gain bandwidth product: 16 MHz
- * High open loop AC gain: $800 @ 20\text{ kHz}$

ORDERING INFORMATION

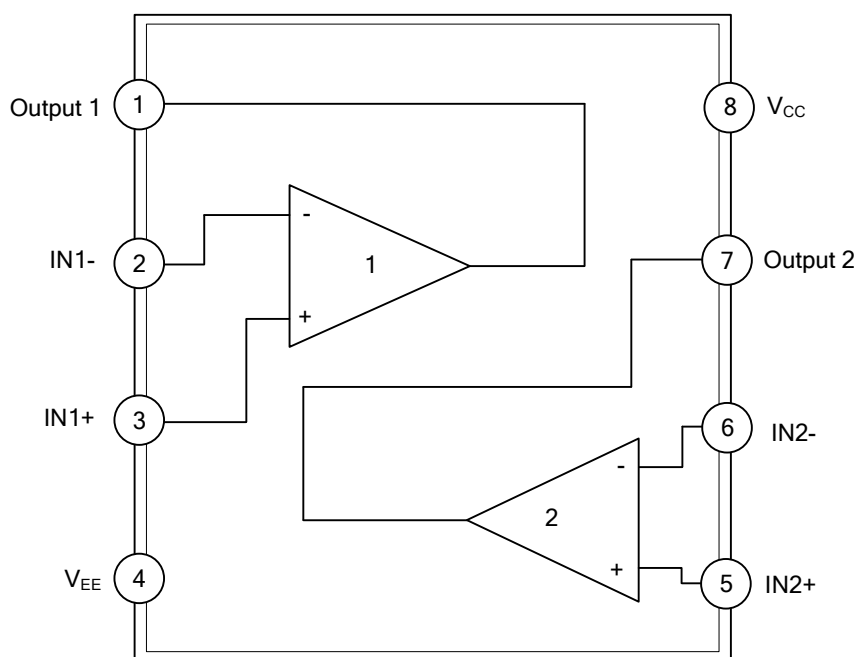
Ordering Number		Package	Packing
Lead Free	Halogen Free		
MC33078L-S08-R	MC33078G-S08-R	SOP-8	Tape Reel

<p>MC33078G-S08-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Green Package 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) S08: SOP-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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MARKING



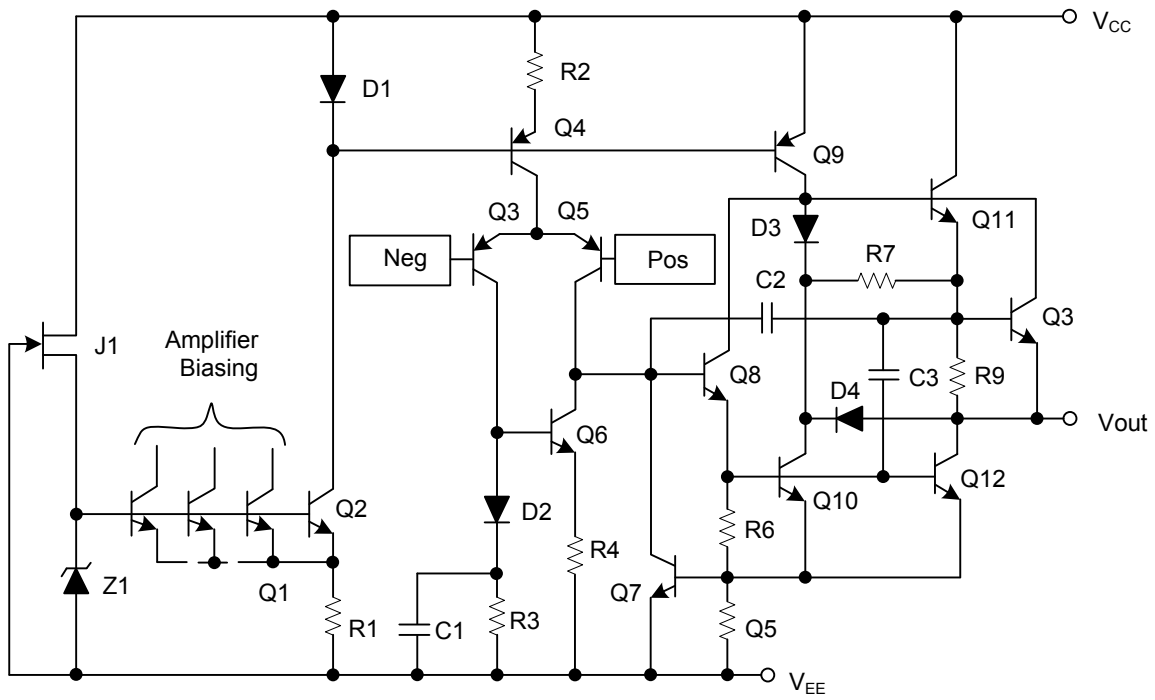
■ PIN CONFIGURATION



■ PIN DESCRIPTION

PIN NO.	PIN NAME	DESCRIPTION
1	Output 1	The output of channel 1
2, 3	IN1-, IN1+	The input of channel 1
4	V_{EE}	Power supply
5, 6	IN2+, IN2-	The input of channel 2
7	Output 2	The output of channel 2
8	V_{CC}	Power supply

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage (V_{CC} to V_{EE})	V_S	+36	V
Input Differential Voltage Range	V_{IDR}	(Note 2)	V
Input Voltage Range	V_{IR}	(Note 2)	V
Output Short Circuit Duration (Note 3)	t_{SC}	Indefinite	sec
Maximum Power Dissipation	P_D	(Note 3)	mW
Maximum Junction Temperature	T_J	+150	°C
Operating Temperature Range	T_{OPR}	-40 ~ 85	°C
Storage Temperature	T_{STG}	-60~+150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Either or both input voltages must not exceed the magnitude of V_{CC} or V_{EE} .

3. Power dissipation must be considered to ensure maximum junction temperature (T_J) is not exceeded.

■ DC ELECTRICAL CHARACTERISTICS ($V_{CC}=+15$ V, $V_{EE}=-15$ V, $T_A=25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
Input Offset Voltage	$ V_{IO} $	$R_S=10\Omega$, $V_{CM}=0V$, $V_O=0V$		0.15	2.0	mV	
Average Temperature Coefficient of Input Offset Voltage	$\Delta V_{IO}/\Delta T$	$R_S=10\Omega$, $V_{CM}=0V$, $V_O=0V$, $T_A=T_{low} \sim T_{high}$		2.0		$\mu\text{V}/^\circ\text{C}$	
Input Bias Current	I_{IB}	$V_{CM}=0V$, $V_O=0V$		300	750	nA	
Input Offset Current	I_{IO}	$V_{CM}=0V$, $V_O=0V$		25	150	nA	
Common Mode Input Voltage Range	V_{ICR}	$\Delta V_{IO}=5.0\text{mV}$, $V_O=0V$	+13	+14 -14	-13	V	
Large Signal Voltage Gain	A_{VOL}	$V_O=\pm 10$ V, $R_L=2.0\text{k}\Omega$	90	110		dB	
Output Voltage Swing	V_{O+}	$V_{ID}=\pm 1.0V$		$R_L=600\Omega$	+10.7	V	
	V_{O-}			$R_L=600\Omega$	-11.9	V	
	V_{O+}			$R_L=2.0\text{k}\Omega$	+13.2	+13.8	V
	V_{O-}			$R_L=2.0\text{k}\Omega$	-13.7	-13.2	V
	V_{O+}			$R_L=10\text{k}\Omega$	+13.5	+14.1	V
	V_{O-}			$R_L=10\text{k}\Omega$	-14.6	-14	V
Common Mode Rejection	CMR	$V_{in}=\pm 13V$	80	100		dB	
Power Supply Rejection (Note 1)	PSR	$V_{CC}/V_{EE}=+15V/-15V \sim +5.0V/-5.0V$	80	105		dB	
Output Short Circuit Current	I_{SC}	$V_{ID}=1.0V$, Output to Ground	Source	+15	+29	mA	
			Sink		-37	-20	mA
Power Supply Current	I_D	$V_O=0V$, All Amplifiers		4.1	5.0	mA	

Note: Measured with V_{CC} and V_{EE} differentially varied simultaneously.

■ AC ELECTRICAL CHARACTERISTICS ($V_{CC}=+15\text{ V}$, $V_{EE}=-15\text{ V}$, $T_A=25^\circ\text{C}$, unless otherwise noted.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Slew Rate	SR	$V_{in}=-10\text{V}\sim+10\text{ V}$, $R_L=2.0\text{k}\Omega$, $C_L=100\text{pF}$, $A_V=+1.0$	5	7		V/ μs
Gain Bandwidth Product	GBW	$f=100\text{kHz}$	10	16		MHz
Unity Gain Frequency	f_U	Open Loop		9		MHz
Gain Margin	A_m	$R_L=2.0\text{k}\Omega$	$C_L=0\text{pF}$	-11		dB
			$C_L=100\text{pF}$	-6		dB
Phase Margin	ϕ_m	$R_L=2.0\text{k}\Omega$	$C_L=0\text{pF}$	55		Degrees
			$C_L=100\text{pF}$	40		Degrees
Channel Separation	CS	$f=20\text{Hz}\sim 20\text{kHz}$		-120		dB
Power Bandwidth	BW_p	$V_O=27\text{V}_{pp}$, $R_L=2.0\text{k}\Omega$, $\text{THD}\leq 1.0\%$		120		kHz
Distortion	THD	$R_L=2.0\text{k}\Omega$, $f=20\text{Hz}\sim 20\text{kHz}$, $V_O=3.0\text{V}_{rms}$, $A_V=+1.0$		0.002		%
Open Loop Output Impedance	$ Z_O $	$V_O=0\text{V}$, $f=9.0\text{MHz}$		37		Ω
Differential Input Resistance	R_{IN}	$V_{CM}=0\text{V}$		175		$\text{k}\Omega$
Differential Input Capacitance	C_{IN}	$V_{CM}=0\text{V}$		12		pF
Equivalent Input Noise Voltage	e_n	$R_S=100\Omega$, $f=1.0\text{kHz}$		4.5		$\text{nV}/\sqrt{\text{Hz}}$
Equivalent Input Noise Current	i_n	$f=1.0\text{kHz}$		0.5		$\text{pA}/\sqrt{\text{Hz}}$

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