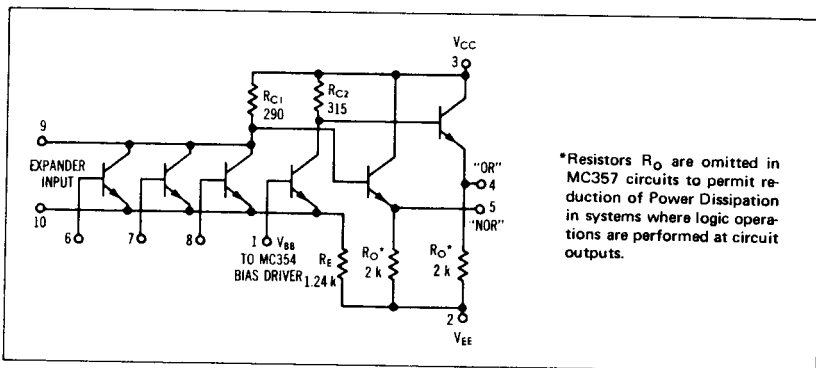


3-INPUT GATES

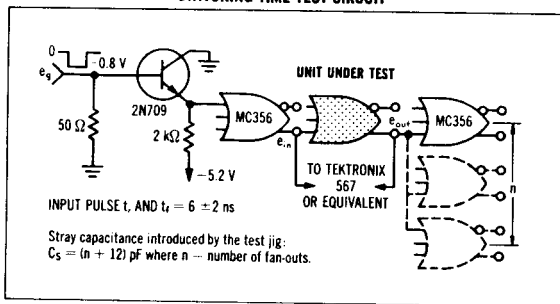
MECL MC350 series

MC356 • MC357

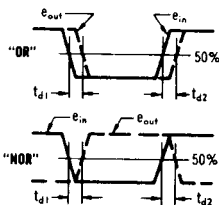
Expandable 3-input gates that provide the positive logic "NOR" function and its complement simultaneously. MC357 omits output pull-down resistors, permitting reduction of power dissipation.



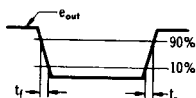
SWITCHING TIME TEST CIRCUIT



PROPAGATION DELAY



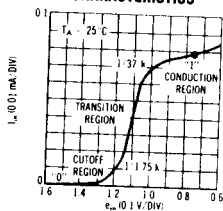
RISE AND FALL TIME



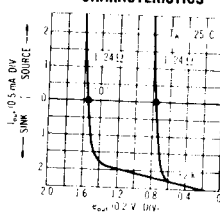
Fan-in obtained with MC355 input expanders; all but driven input connected to -5.2 V.

MC356, MC357 (continued)

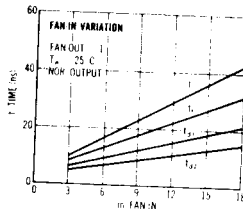
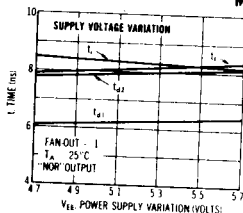
TYPICAL INPUT CHARACTERISTICS



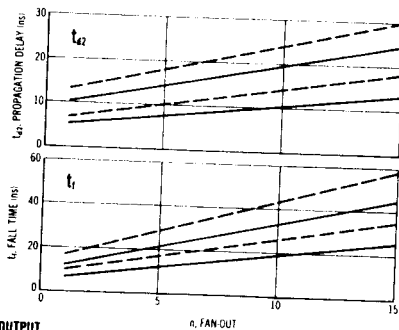
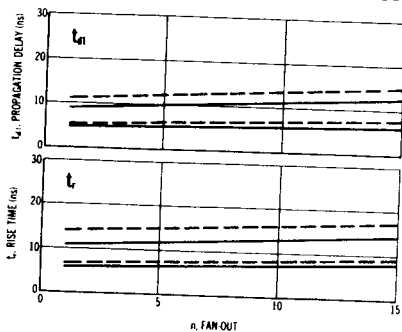
TYPICAL OUTPUT CHARACTERISTICS



TYPICAL SWITCHING TIME VARIATIONS MC356



SWITCHING CHARACTERISTICS (10% to 90% distribution)



"NOR" OUTPUT

— 0°C and $+25^\circ\text{C}$
- - - -75°C

MC356, MC357 (continued)

ELECTRICAL CHARACTERISTICS

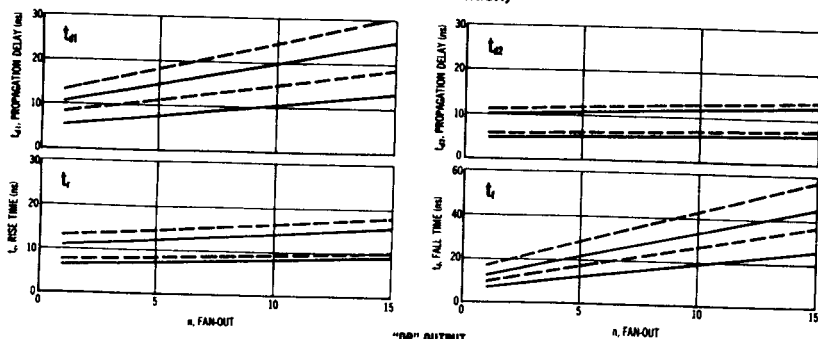
Characteristic		Test Conditions										Test Limits						Unit																																																																																																																																																																																																																																																																																																																																																																																	
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_{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} Pin No.	V _{CC} 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Pins not listed are left open

① Input voltage is adjusted to obtain $dV_{000}/dV_{CC} = 0$

② Current test conditions: no load = 0; full load = -2.5mA ± 5%

SWITCHING CHARACTERISTICS (10% to 90% distribution)



"OR" OUTPUT

— 0°C and +25°C
- - - +75°C