

MC393

Dual Differential Comparators

Version 2.0

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Version History

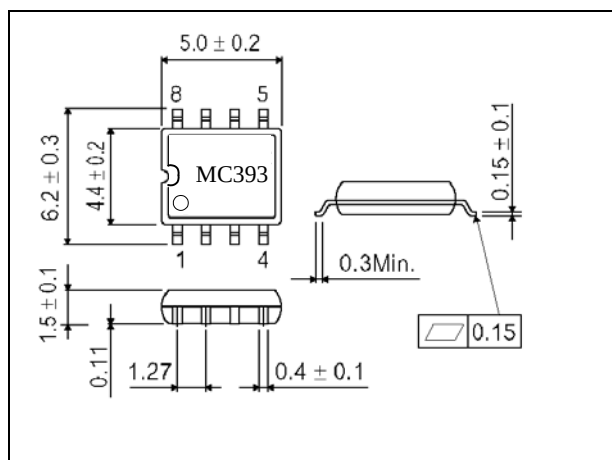
Version	Issued time	Notes
V1.0	March 10, 2007	Datasheet created.
V2.0	Aug. 8, 2010	Update datasheet format.

DESCRIPTION

The MC393 consists of two independent voltage comparators with an offset voltage specification as low as 2.0mV max. The comparators are designed specifically to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as long as the difference between the two supplies is 2V to 36V. Current drain is independent of the magnitude of supply voltage.

The comparators also have a unique characteristic that the input common-mode voltage range includes ground, even though operated from a single power supply voltage.

Outline Drawing

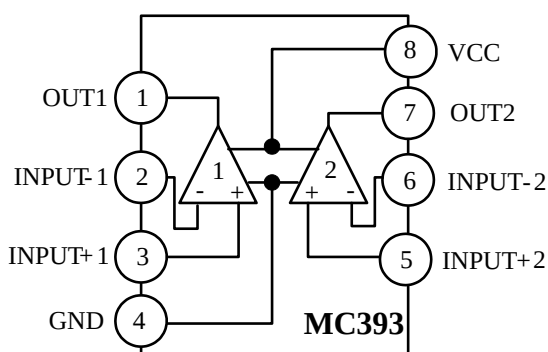


SOP-8 (Unit: mm)

FEATURE

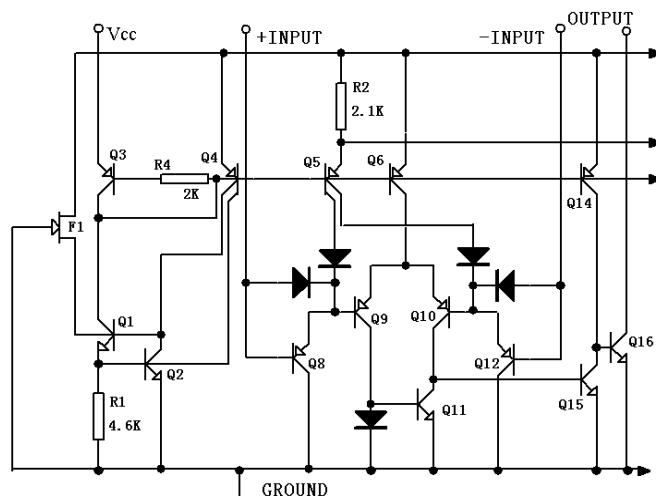
- Single supply or dual supplies
- Wide range of supply voltage
 - Single supply: 2V to 36V
 - Dual supplies: $\pm 1.0\text{V}$ to $\pm 18\text{V}$
- Very low supply current drain (0.8mA), independent of supply voltage (2.0mW/comparator at 5.0V)
- Low input biasing current: 25nA
- Low input offset current: 5.0nA
- Low input offset voltage: 5.0mV
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Output voltage compatible with TTL, DTL, ECL, MOS and CMOS logic systems
- SOP-8 package

PIN DESCRIPTION



Pin No	Symbol	Description
1	OUT1	Output of comparator1
2	INPUT-1	Input (-) of comparator1
3	INPUT+1	Input (+) of comparator1
4	GND	Ground
5	INPUT+2	Input (+) of comparator2
6	INPUT-2	Input (-) of comparator2
7	OUT2	Output of comparator2
8	VCC	Power supply

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Characteristic		Symbol	Value	Unit
Supply Voltage Range	Single supply	V _{CC}	±18	V
	Dual supplies		36	
Differential Input Voltage		V _{DIFFIN}	36	V
Input Common-Mode Voltage		V _{COM}	-0.3~36	V
Output Short-Circuit to GND		I _{OG}	20	mA
Input Current		I _{CC}	50	mA
Maximum Junction Temperature		T _J	125	°C
Power Dissipation		P _{OUT}	300	mW
Operating Temperature		T _{AMB}	-20~+85	°C
Storage Temperature		T _{STG}	-65~150	°C

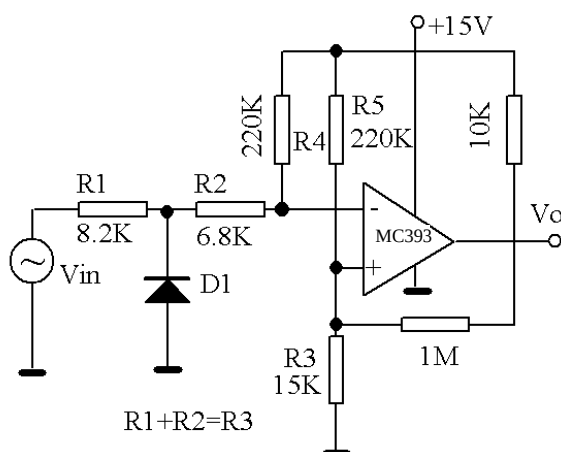
ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: Ta=25°C, VCC=5.0V)

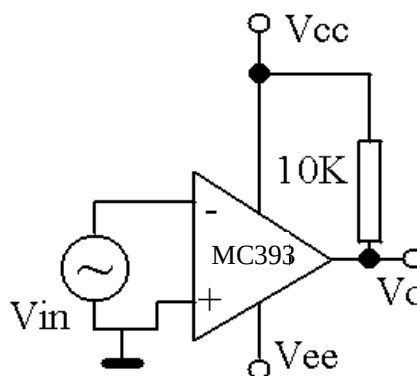
Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Input Offset Voltage	V _{OFFSET}	Ta=25°C		±1.0	±5.0	mV
		0°C ≤ Ta ≤ 70°C			±9.0	
Input Offset Current	I _{OFFSET}	Ta=25°C		±5.0	±50	nA
		0°C ≤ Ta ≤ 70°C			±150	
Input Bias Current	I _{BIAS}	Ta=25°C	-	25	250	nA
		0°C ≤ Ta ≤ 70°C			400	
Input Common-Mode Voltage Range	V _{COM}	Ta=25°C	0		V _{CC} -1.5	V
		0°C ≤ Ta ≤ 70°C	0		V _{CC} -2.0	

Supply Current	I_{CC}	$R_L = \infty$, dual comparators		0.4	1.0	mA
		$R_L = \infty$, dual comparators $V_{CC} = 30V$			2.5	
Voltage Gain	G_{SIG}	$V_{CC} = 15V$, $R_L \geq 15k\Omega$	50	200		V/mV
Large Signal Response Time	T_{RES}	$V_{IN} = \text{TTL Logic Swing}$, $V_{REF} = 1.4V$, $V_{RL} = 5.0V$, $R_L = 5.1k\Omega$		300		ns
Response Time	T_{RES}	$V_{RL} = 5.0V$, $R_L = 5.1k\Omega$		1.3		ns
Input Differential Voltage	V_{IND}				V_{CC}	V
Output Sink Current	I_{SINK}	$V_{IN} (-) \geq 1.0V$, $V_{IN} (+) = 0V$, $V_O \leq 1.5V$	6.0	16		mA
Output Saturation Voltage	V_{SAT}	$V_{IN} (-) \geq 1.0V$, $V_{IN} (+) = 0V$, $I_{SINK} \leq 4.0mA$		130	400	mV
		$V_{IN} (-) \geq 1.0V$, $V_{IN} (+) = 0V$, $I_{SINK} \leq 4.0mA$ $0^\circ C \leq T_a \leq 70^\circ C$			700	
Output Leakage Current	I_{OL}	$V_{IN} (+) \geq 1.0V$, $V_{IN} (-) = 0V$, $V_O = 5.0V$		0.1		nA
		$V_{IN} (+) \geq 1.0V$, $V_{IN} (-) = 0V$, $V_O = 30V$ $0^\circ C \leq T_a \leq 70^\circ C$			1000	

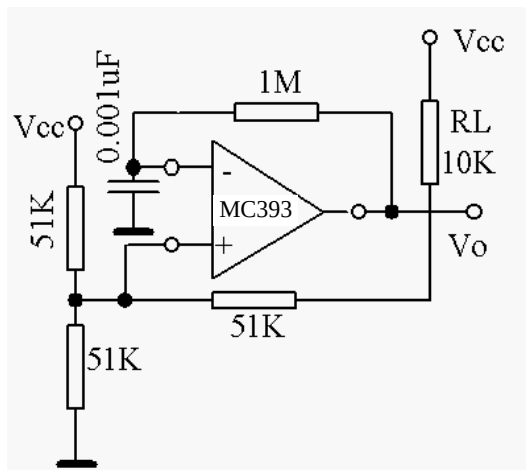
APPLICATION CIRCUIT



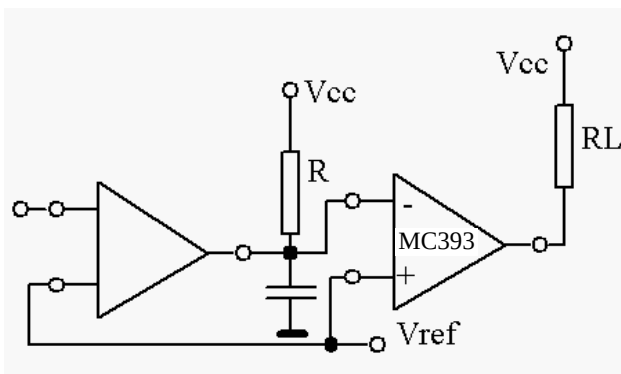
Zero Rectifier (Single Power Supply)



Zero Rectifier (Dual Power Supplies)



Squarewave Generator



Delay Circuit

TYPICAL PERFORMANCE CHARACTERISTICS

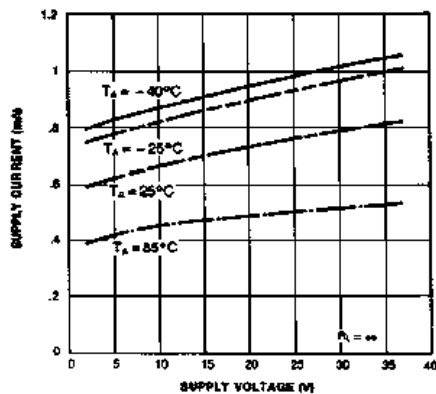


Figure 1. Supply Current vs Supply Voltage

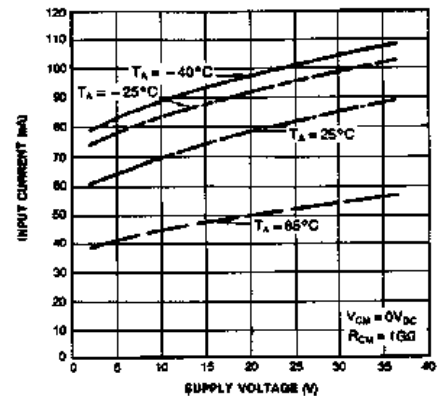


Figure 2. Input Current vs Supply Voltage

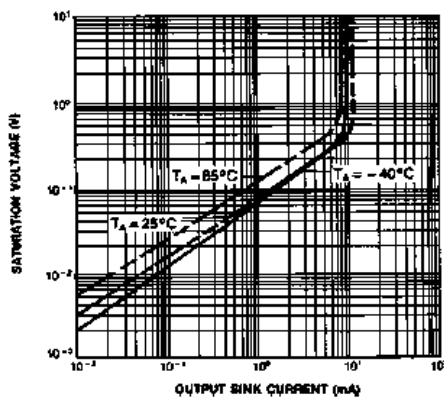


Figure 3. Output Saturation Voltage vs Sink Current

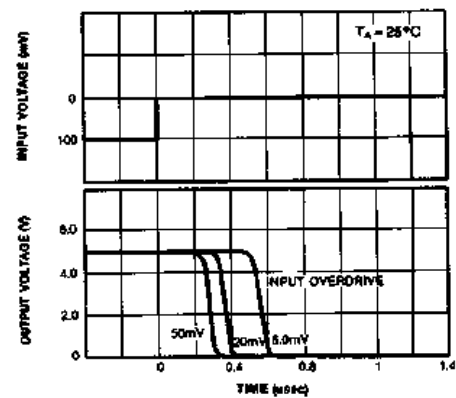


Figure 4. Response Time for Various Input Overdrive-Negative Transition

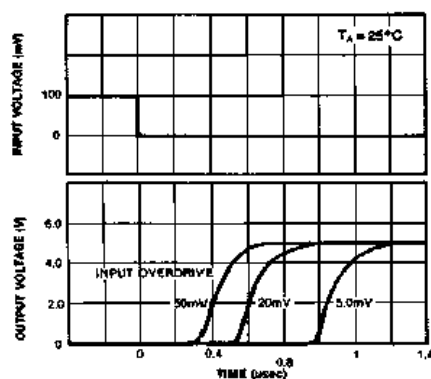
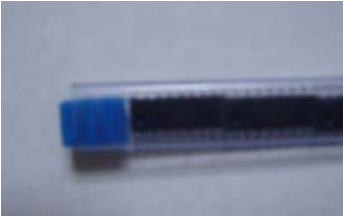


Figure 5. Response Time for Various Input Overdrive-Positive Transition

PACKING

SOP Tube Packing Process:

Packing Type	Pcs/ Tube	Pcs /Box	Pcs / Case
SOP8 (Tube)	100	20,000	50,000

No.	Packing Example	Packing Description
1		1. Make the first pin toward the non-white cork (the other side of the tube is white cork). 2. The direction is as Figure 1, please make the direction of all products the same. 3. 100pcs chips in each tube.
2		1. Tie plastic tubes with the same direction. 2. 20 tubes for each buddle, 2000pcs chips altogether.
3		1. Place tubes in small packaging boxes. 2. 20,000pcs chips in each box.
4		1. Cover small boxes, and stick the product label in the middle of right side. 2. Box size: 545x127x55mm
5		Place small boxes with the same direction in cartoon boxes.
6		1. Cover cartoon boxes, and seal with transparent adhesive tape. 2. Stick product label in top left corner of cartoon boxes. 3. 50,000pcs chips in each box. 4. Carton size:565x360x145mm
7		Product label on cartoon boxes

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