DUAL 4-CHANNEL DATA SELECTOR

MC4300 MC4000

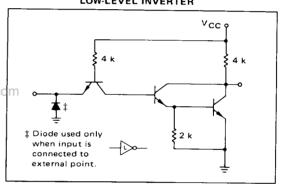
1/2 OF DEVICE SHOWN (Numbers and symbols in parenthesis are for other half of device.) To Other Data Selector X0 6 O (Y0) (2) X1 7 O-(Y1) (1) Н 011 Z (12) (W) X2 8 O (Y2) (14) VCC = PIN 4 GND = PIN 10 хз 9 **о**-(Y3) (13) DataSheet4U.com $Z = ABX0 + A\overline{B}X1 + \overline{A}BX2 + \overline{A}\overline{B}X3$ $W = ABY0 + A\overline{B}Y1 + \overline{A}BY2 + \overline{A}\overline{B}Y3$ Input Loading Factor = 1 Output Loading Factor = 10 Total Power Dissipation = 150 mW typ/pkg

This device consists of two four-channel data selectors with common control lines, constructed from high-level AND-OR gates and low-level inverters. By selecting one of four logic combinations, information on one of the four data inputs will be routed to the output.

Data selectors are useful in applications where digital data is to be routed from one of several registers or locations to another register or location for processing.

TYPICAL PROPAGATION DELAY TIMES (ns) $T_A = 25^{\circ}C$ INPUT Z CONDITIONS A 18 X0 = X2 = X3 = logic "0", X1 = logic "1". A and B are defined by the logic equations.

LOW-LEVEL INVERTER



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HIGH-LEVEL "AND-OR" GATE

4 k

4 k

4 k

5 compared to external points.

98

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