

4-BIT SHIFT REGISTER

MC4312

MC4012

This 4-bit register provides parallel or serial data entry and retrieval, determined by the logic state of the mode control input. For parallel operation, set the mode control to the logic "1" state and strobe the information at the Dp inputs into the register. Serial left-shift operation is achieved in this mode by connecting the Q outputs to the Dp inputs of the previous stage. For serial right-shift operation, set the mode control to logic "0" and clock data into the register from D_S.

