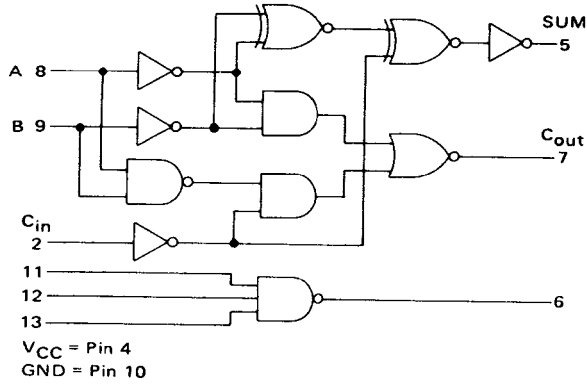


FULL ADDERS

MC4326 • MC4327
MC4026 • MC4027



Input Loading Factor:

A, B = 2
C_{in}, Pins 11, 12, 13 = 1

Output Loading Factor:

MC4326 = 15 MTTL I Loads
MC4327 = 7 MTTL I Loads
MC4026 = 12 MTTL I Loads
MC4027 = 6 MTTL I Loads

These full adders are designed for serial and ripple-carry parallel adder systems. True Sum and Carry are produced at the output from the input information. A separate 3-input NAND gate is provided on the monolithic chip to provide the inverted Sum or Carry output.

TRUTH TABLE

Input Pins			Output Pins	
8 A	9 B	2 C _{in}	5 SUM	7 C _{out}
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

Total Power Dissipation = 90 mW typ/pkg

Add Delay = 25 ns typ

Carry Delay = 13 ns typ

CIRCUIT SCHEMATIC

