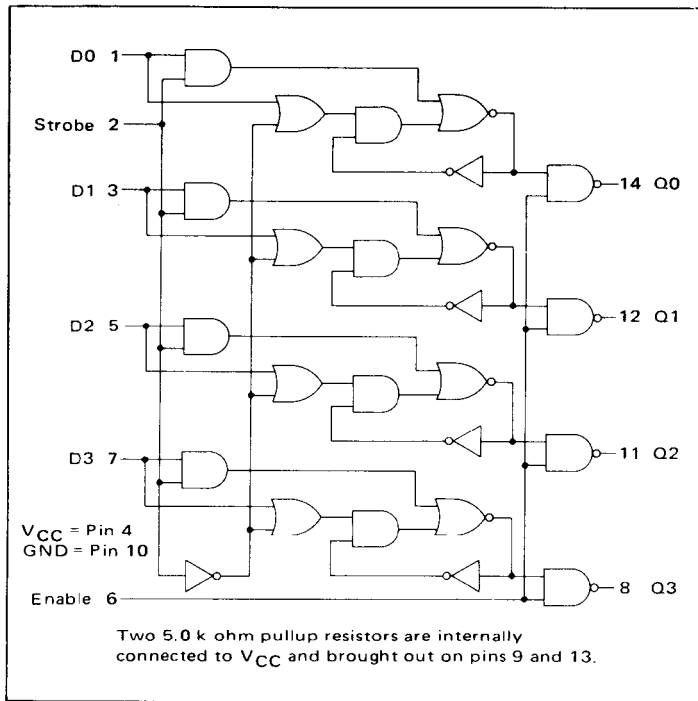


**QUAD LATCH  
(Open Collector)**

**MC4335  
MC4035**



This monolithic device consists of four latch circuits with open collector outputs, common Strobe input, and output enable input. The output of each latch will follow the data input when the Strobe input is in a logical "1" state. When the Strobe is in a logical "0" state, the latch will store the logic state of the data input just prior to the change of the Strobe from a "1" level to a "0" level.

The open collector outputs make this device useful for bussing or wire ORing outputs together. Two 5.0 k ohm resistors are available in the package to provide the passive pullup function in wired-OR or bussed operation. The output enable is useful where it is desirable to gate information out of the latches according to a predetermined timing scheme.

**Input Loading Factor (MTTL I Loads):**

Data Input (Strobe High) – MC4335 = 4.2  
MC4035 = 4.0

Data Input (Strobe Low) – MC4335 = 1.1  
MC4035 = 0.9

Output Enable – MC4335 = 4.0  
MC4035 = 3.6

Strobe – MC4335 = 5.2  
MC4035 = 5.2

**Output Loading Factor (MTTL I Loads):**

MC4335 = 7 ( $I_{OL} = 9.3 \text{ mAdc}$ )

MC4035 = 7 ( $I_{OL} = 11.6 \text{ mAdc}$ )

Total Power Dissipation = 140 mW typ/pkg

Propagation Delay Time = 25 ns typ

**CIRCUIT SCHEMATIC**

**1/4 OF DEVICE SHOWN**

