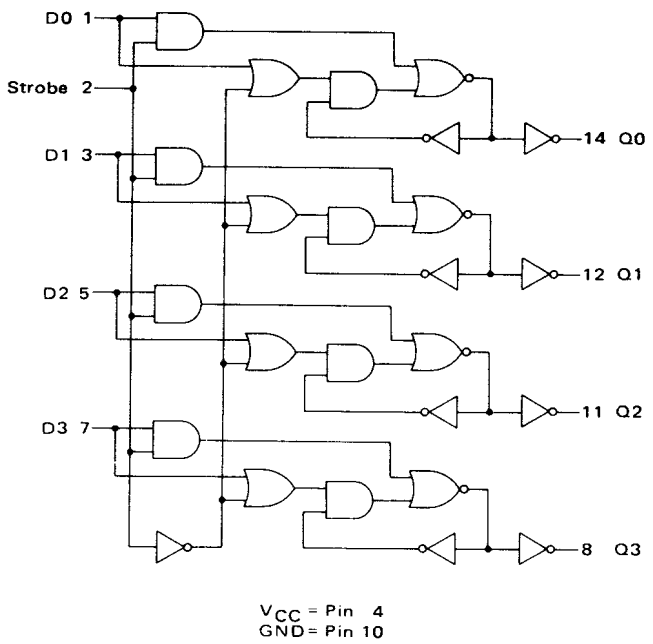
QUAD LATCH

MC4337
MC4037



This monolithic device consists of four latch circuits with active pullup networks for high capacitive load drive capability. Separate data inputs and a common Strobe input are provided. Information present on the data inputs prior to the negative edge of the strobe input will be stored in the latch. When the strobe input is high, the Q output will follow the data input.

Input Loading Factor (MTTL I Loads):
Data Input (Strobe High) — MC4337 = 4.2
MC4037 = 4.0
Data Input (Strobe Low) — MC4337 = 1.1
MC4037 = 0.9
Strobe — MC4337 = 5.2
MC4037 = 5.2
Output Loading Factor (MTTL I Loads):
MC4337 = 10 (I_{OL} = 13.3 mAdc)
MC4037 = 10 (I_{OL} = 16.6 mAdc)
Total Power Dissipation = 150 mW typ/pkg
Propagation Delay Time = 25 ns typ

CIRCUIT SCHEMATIC

1/4 OF DEVICE SHOWN