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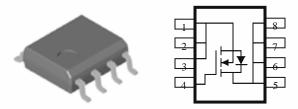
# N-Channel 30-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOIC-8 saves board space
- Fast switching speed
- High performance trench technology

PRODU	CT SUMMARY	

V <sub>DS</sub> (V)	$r_{DS(on)} m(\Omega)$	I <sub>D</sub> (A)
30	$6 @ V_{GS} = 4.5V$	18.6
	8 @ $V_{GS} = 2.5V$	16.1



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C UNLESS OTHERWISE NOTED)				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V <sub>DS</sub>	30	V
Gate-Source Voltage		V <sub>GS</sub>	12	v
Continuous Drain Current <sup>a</sup>	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	In	18.6	
	$T_A=70^{\circ}C$	тр	15.7	А
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	60	
Continuous Source Current (Diode Conduction) <sup>a</sup>		Is	2.9	А
Power Dissipation <sup>a</sup>	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	P	3.1	W
	T <sub>A</sub> =70°C	I D	2.2	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Maximum	Units	
Maximum Junction-to-Ambient <sup>a</sup>	t <= 10 sec	R <sub>θJA</sub>	40	°C/W	
	Steady State		80	°C/W	

#### Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

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SPECIFICATIONS (T <sub>A</sub> = $25^{\circ}$ C UNLESS OTHERWISE NOTED)							
<b>Parame te r</b>	Sympol		Limits			Unit	
	Symbol	Test Conditions	Min	Тур	Max	Umt	
Static							
Gate-Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	Igss	$V_{DS} = 0 \text{ V},  V_{GS} = 12 \text{ V}$			100	nA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	IDSS	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			5	uA	
On-State Drain Current <sup>A</sup>	Ι	$\frac{V}{V_{GS}^{DS} = 4.5 \text{ V}, V} = 10 \text{ V}$	30			А	
Drain-Source On-Resistance <sup>A</sup>	D(on) IDS(on)	$V_{GS}^{DS} = 4.5 \text{ V}, I_D^{GS} = 18.6 \text{ A}$			6	mΩ	
		$V_{GS} = 2.5 V$ , $I_D = 16.1 A$			8		
Forward Tranconductance <sup>A</sup>	g	V = 15 V, I = 18.6 A		90		S	
Diode Forward Voltage	Vsd	$I_{S}^{DS} = 2.3 \text{ A}, V_{GS} = 0 \text{ V}$		0.7		V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg	$V_{DS} = 15 V, V_{GS} = 4.5 V,$		25		nC	
Gate-Source Charge	Qgs	$v_{DS} = 15 v, v_{GS} = 4.5 v,$ ID = 18.6 A		6			
Gate-Drain Charge	Qgd	ID = 10.0  A		9			
Turn-On Delay Time	td(on)			20		nS	
Rise Time	tr	$V_{DD}=15 \ V, \ R_L=6 \ \Omega \ , \ ID=1 \ A,$		13			
Turn-Off Delay Time	t <sub>d(off)</sub>	VGEN = 10 V		82			
Fall-Time	tf	7		43			

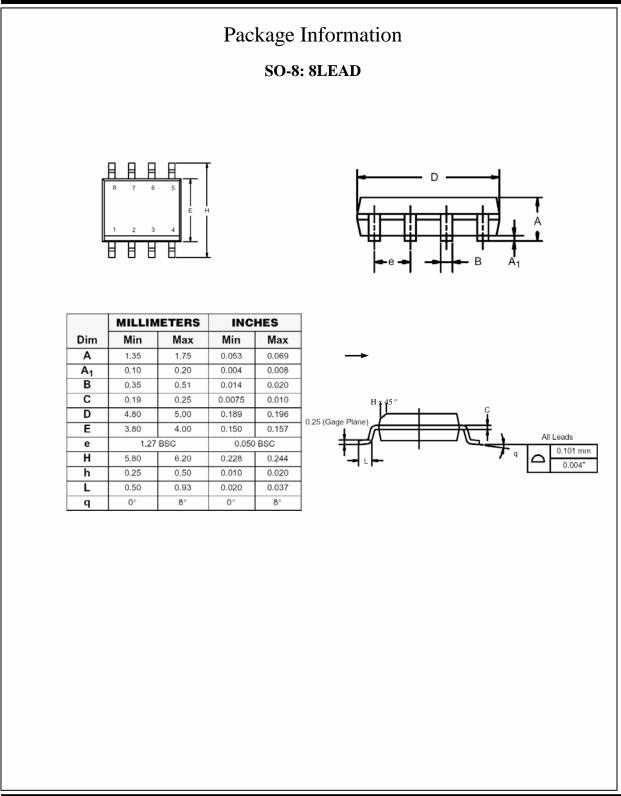
Notes

- a. Pulse test:  $PW \le 300$  us duty cycle  $\le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

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