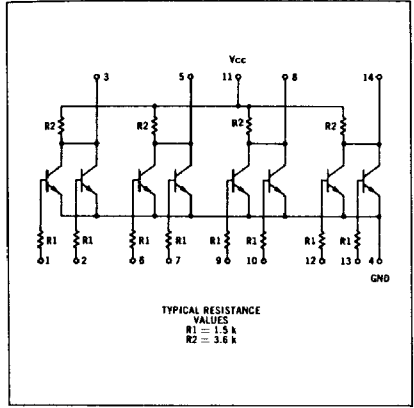


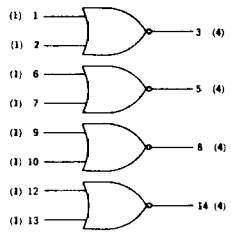
QUAD 2-INPUT GATES

PLASTIC mW MRTL MC700P/800P series

MC717P • MC817P

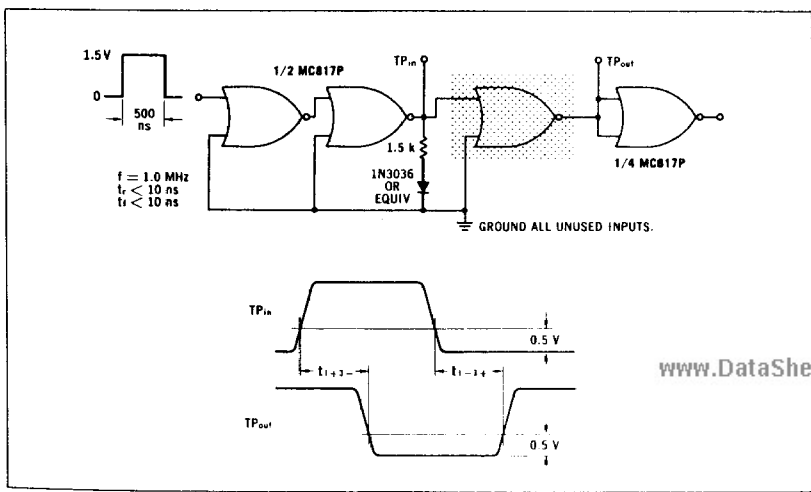


Four 2-input positive logic NOR gates in a single package. Each may be used independently, paralleled for increasing the number of inputs (subject to loading rules), or cross-coupled to form bistable elements.



$3 = 1 + 2$
 NUMBER IN PARENTHESES INDICATES LOADING FACTOR
 $t_{pd} \leq 27$ ns
 $P_o \leq 20$ mW (input High)
 5.0 mW (input Low)

SWITCHING TIMES TEST CIRCUIT AND WAVEFORMS



MC717P, MC817P (continued)

Characteristic	Symbol	Pin Under Test	TEST VOLTAGE VALUES																			
			(Volts)						APPLIED TO PINS LISTED BELOW:													
			V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}	V _{DD}								
Input Current	I _{in}	1	0.880	0.850	1.80	0.500	3.60	0.880	0.850	1.80	0.500	3.60	0.880	0.850	1.80	0.500	3.60	0.880	0.850	1.80	0.500	3.60
Output Current	I _{OA}	3	0.740	0.710	1.80	0.400	3.60	0.740	0.710	1.80	0.400	3.60	0.740	0.710	1.80	0.400	3.60	0.740	0.710	1.80	0.400	3.60
Output Voltage	V _{out}	3	0.865	0.865	1.80	0.475	3.60	0.865	0.865	1.80	0.475	3.60	0.865	0.865	1.80	0.475	3.60	0.865	0.865	1.80	0.475	3.60
Saturation Voltage	V _{CE(sat)}	3	0.850	0.850	1.80	0.460	3.60	0.850	0.850	1.80	0.460	3.60	0.850	0.850	1.80	0.460	3.60	0.850	0.850	1.80	0.460	3.60
Switching Time	t _{on} + t _{off}	1, 3	0.800	0.800	1.80	0.430	3.60	0.800	0.800	1.80	0.430	3.60	0.800	0.800	1.80	0.430	3.60	0.800	0.800	1.80	0.430	3.60

Test Procedure	Test Limits	Test Limits									
		MC717P		MC817P		MC717P					
		0°C	+25°C	+75°C	+15°C	+25°C	+55°C				
Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max
1	150	140	140	140	150	150	150	150	150	150	150
2	150	140	140	140	150	150	150	150	150	150	150
3	570	570	585	570	570	570	570	570	570	570	570
3	400	350	300	400	400	300	300	300	300	300	320
3	400	350	300	400	400	300	300	300	300	300	320
3	250	250	250	220	220	230	230	230	230	230	320
3	250	250	250	220	220	230	230	230	230	230	320
ns	ns	ns	ns	ns	ns	ns	ns	ns	ns	ns	ns
90	90	90	90	90	90	90	90	90	90	90	90

ELECTRICAL CHARACTERISTICS

Test procedures are shown for one gate only.
The other gates are tested in the same manner.

Ground input pins are not under test. Other pins not listed are left open.