

Features

- Advanced Trench MOSFET Process Technology
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

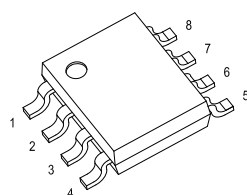
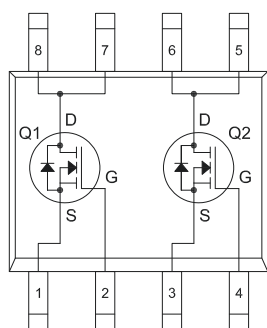
Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 73.5°C/W Junction to Ambient^(Note2)

Parameter	Symbol	Rating	Unit
Drain -Source Voltage	V_{DS}	60	V
Gate -Source Voltage	V_{GS}	±20	V
Drain Current-Continuous	I_D	5	A
Pulsed Drain Current ^(Note1)	I_{DM}	30	A
Power Dissipation	P_D	1.7	W

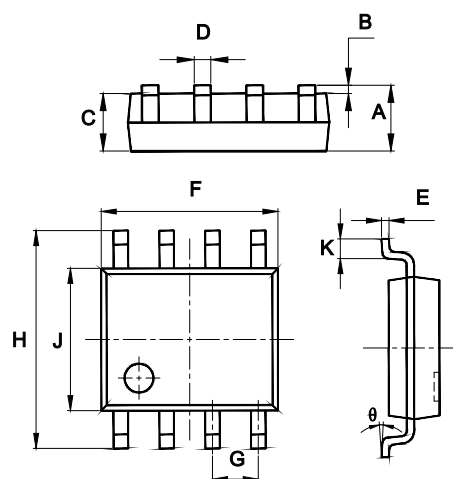
Note: 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.

Internal Structure



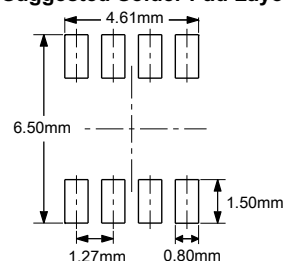
Dual N-Channel Power MOSFET

SOP-8



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.35	1.75	
B	0.004	0.010	0.10	0.25	
C	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
H	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Threshold Voltage ^(Note3)	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0		3.0	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =± 20V, V _{DS} =0V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
Drain-Source On-Resistance ^(Note3)	R _{DS(on)}	V _{GS} =10V, I _D =5A		37	45	mΩ
Forward Transconductance ^(Note3)	g _{fs}	V _{DS} =5V, I _D =4.5A	11			S
Dynamic Characteristics ^(Note4)						
Input Capacitance	C _{iss}	V _{DS} =30V,V _{GS} =0V, f=1MHz		500		pF
Output Capacitance	C _{oss}			60		
Reverse Transfer Capacitance	C _{rss}			25		
Switching Characteristics ^(Note4)						
Total Gate Charge	Q _g	V _{DS} =48V,V _{GS} =10V,I _D =15A		12		nC
Gate-Source Charge	Q _{gs}			4.1		
Gate-Drain Charge	Q _{gd}			4.5		
Turn-on Delay Time	t _{d(on)}	V _{DD} =30V,V _{GS} =10V,I _D =2A,R _G =3Ω, R _L =6.7Ω		5.0		ns
Turn-on Rise Time	t _r			2.6		
Turn-off Delay Time	t _{d(off)}			16.1		
Turn-off Fall Time	t _f			2.3		
Drain-Source Diode Characteristics						
Diode Forward Voltage ^(Note3)	V _{SD}	V _{GS} =0V, I _s =20A			1.2	V
Diode Forward Current ^(Note2)	I _S				20	A
Reverse Recovery Time	t _{rr}	I _F =20A,di/dt=100A/us ^(Note4)		35		nS
Reverse Recovery Charge	Q _{rr}			53		μC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

Note: 3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.

4. Guaranteed by design, not subject to production.

Curve Characteristics

Fig. 1 - $R_{DS(ON)} - I_D$

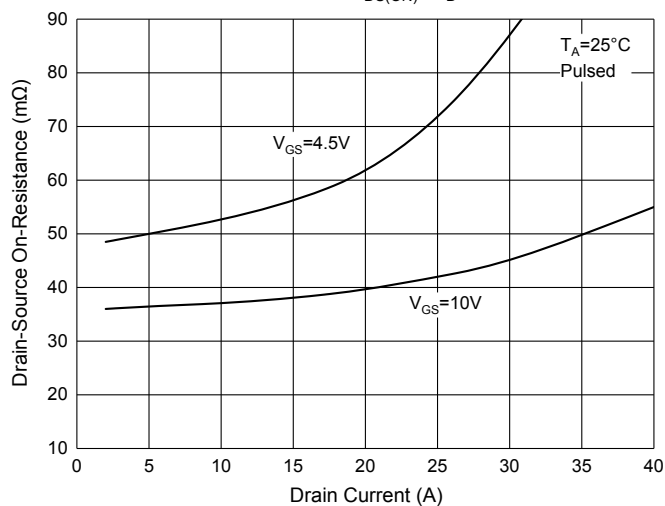


Fig. 2 - Gate Charge

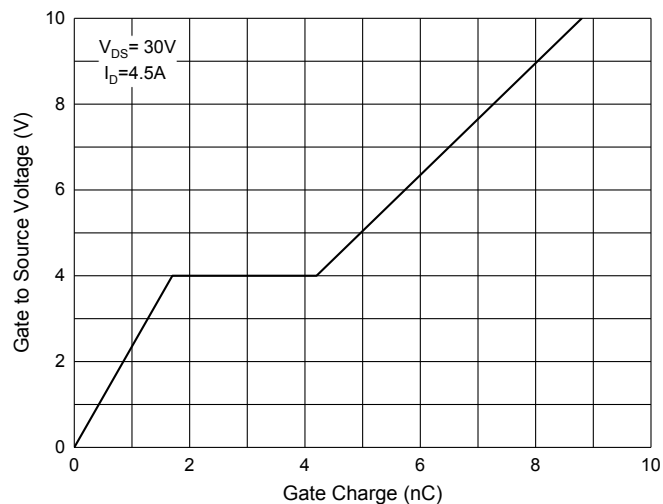


Fig. 3 - Capacitance Characteristics

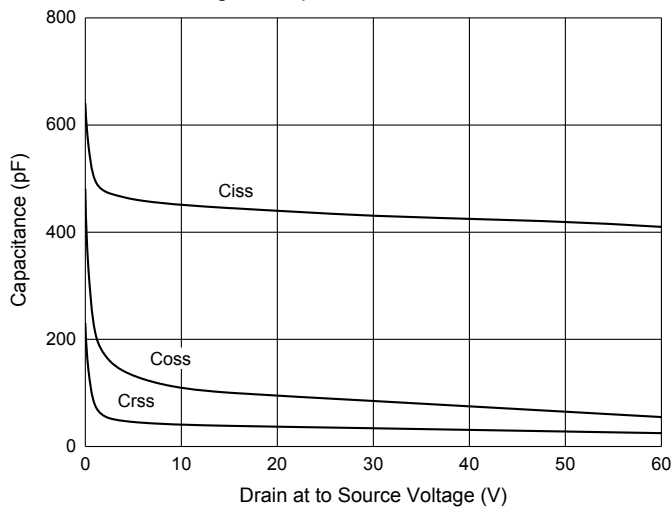
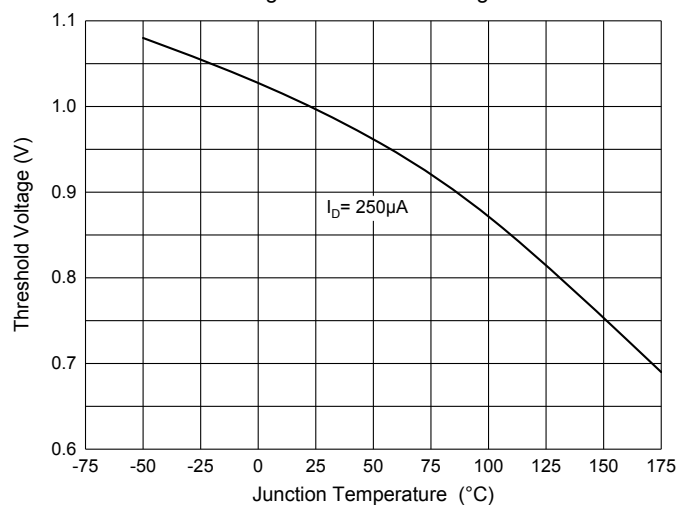


Fig. 4 - Threshold Voltage



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:4Kpcs/Reel

Note : Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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