

MCSOF

Overall stability ± 100ppm 5V Power Supply

SMD Ceramic Clock-Oscillator opm Very low jitter 40 to 160MHz

FREQUENCY STABILITY

OVER:

OPERATING TEMP. RANGE: See note 1 OVERALL STABILITY: $<\pm100$ ppm * INCLUDING:

OVER OPERATING TEMPERATURE RANGE

ADJUSTMENT @ 25 ℃

LONG TERM AGING (10 YEARS)

STABILITY OVER SUPPLY VOLTAGE ±10%

• STABILITY OVER LOAD (MIN. TO MAX.)

POWER SUPPLY

SUPPLY VOLTAGE: $Vdd = 5V \pm 10\%$ *

INPUT CURRENT: < 50mA*

OUTPUT

 OUTPUT SIGNAL:
 AC-MOS compatible *

 SYMMETRY:
 40/60% (min.) @ Vdd/2*

 RISE & FALL TIME:
 tr < 3ns tf < 3ns *

 LEVEL "0" & "1":
 < 0.4V > Vdd - 0.5V

 START-UP TIME:
 < 5ms</td>

 FAN OUT (LOAD):
 10 TTL / LS *

 JITTER:
 < 1ps</td>

ENVIRONMENT

OPERABLE TEMP. RANGE: -55 to +125 °C -65 to -125 °C -125 °C

PACKAGE DIMENSIONS: 14.1 x 9.3 x 2.4mm (see packaging info)

PROCESSING: Reflow soldering 260 ℃ / 10s max.

(see packaging info)

MISCELLANEOUS

* Customer's specification on request

Note 1: Operating Temperature Range

 MCSOF-A:
 0 to +70 ℃

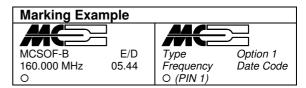
 MCSOF-B:
 -40 to +85 ℃

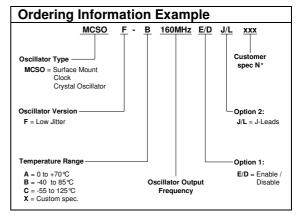
 MCSOF-C:
 -55 to +125 ℃

See application circuit on page 2 for details Pin 1: Pin 3 (Fout):: Open Clock H Clock L High Z

Option 2: J / Leads (on request)

With tinned J / Leads pins Height: 3.8mm included J / Leads





STANDARD FREQUENCIES [MHz]

Preliminary

 Date :
 June 2003
 Revision No. : 3
 Revision Date : 11-05

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In accordance with our policy of continuous development and improvement, we reserve the right to modify the design or the specifications of our products without prior notice.

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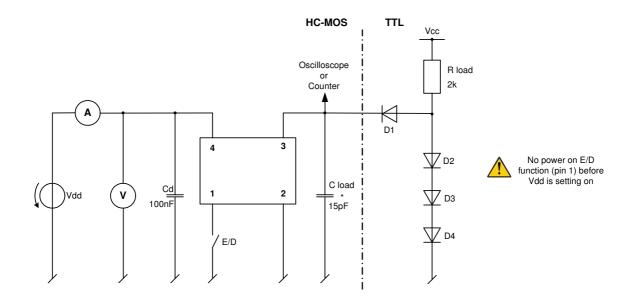


SMD Ceramic Clock-Oscillator

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Very low jitter 40 to 160MHz

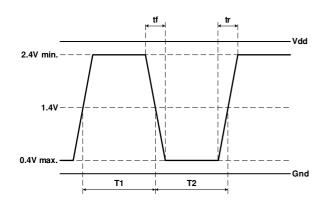
Application and Test Circuit:

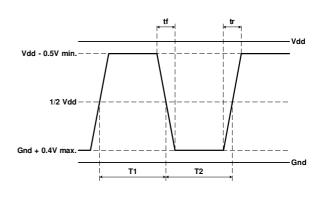


Waveform Output:

Waveshape TTL

Waveshape HC-MOS





$$Duty\ Cycle = 100 \times \frac{T1}{T1 + T2} [\%]$$

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