MD3250,A,F,AF
MD3251,A,F,AF
MQ3251

MD3250,A
MD3251,A
CASE 654-07, STYLE 1

MD3250F,AF
MD3251F,AF
CASE 610A-04, STYLE 1

MQ3251
CASE 607-04, STYLE 1

DUAL
AMPLIFIER TRANSISTOR

PNP SILICON

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MAXIMUM RATINGS

Rating	Symbol	Value		Unit
Collector-Emitter Voltage	V_{CEO}	40		Vdc
Collector-Base Voltage	V_{CBO}	50		Vdc
Emitter-Base Voltage	V_{EBO}	5.0		Vdc
Collector Current — Continuous	I_C	50		mAdc
		One Die	All Die Equal Power	
Total Device Dissipation @ T_A = 25°C	P_D			mW
MD3250,A, MD3251,A		575	625	
MD3250F,AF, MD3251F,AF		350	400	
MQ3251		400	600	
Derate above 25°C				mW/°C
MD3250,A, MD3251,A		3.29	3.57	
MD3250F,AF, MD3251F,AF		2.0	2.28	
MQ3251		2.28	3.42	
Total Device Dissipation @ T_C = 25°C	P_D			Watts
MD3250,A, MD3251,A		1.8	2.5	
MD3250F,AF, MD3251F,AF		1.0	2.0	
MQ3251		0.9	3.6	
Derate above 25°C				mW/°C
MD3250,A, MD3251,A		10.3	14.3	
MD3250F,AF, MD3251F,AF		5.71	11.4	
MQ3251		5.13	20.5	
Operating and Storage Junction Temperature Range	T_J, T_{stg}	−65 to +200		°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	One Die	All Die Equal Power	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$			°C/W
MD3251,A, MD3251,A		97	70	
MD3250F,AF, MD3251F,AF		175	87.5	
MQ3251		195	48.8	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$(1)			°C/W
MD3250,A, MD3251,A		304	280	
MD3250F,AF, MD3251F,AF		500	438	
MQ3251		438	292	
		Junction to Ambient	Junction to Case	
Coupling Factors				%
MD3250,A, MD3251,A		84	44	
MD3250F,AF, MD3251F,AF		75	0	
MQ3251 (Q1-Q2)		57	0	
(Q1-Q3 or Q1-Q4)		55	0	

(1) $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage(2) (I_C = 10 mAdc, I_B = 0)	$V_{(BR)CEO}$	40	—	—	Vdc
Collector-Base Breakdown Voltage (I_C = 10 μAdc, I_E = 0)	$V_{(BR)CBO}$	50	—	—	Vdc
Emitter-Base Breakdown Voltage (I_E = 10 μAdc, I_C = 0)	$V_{(BR)EBO}$	5.0	—	—	Vdc
Collector Cutoff Current	I_{CBO}				
(V_{CB} = 40 Vdc, I_E = 0)		—	—	10	nAdc
(V_{CB} = 40 Vdc, I_E = 0, T_A = 150°C)		—	—	10	μAdc
Emitter Cutoff Current (V_{BE} = 3.0 Vdc, I_C = 0)	I_{EBO}	—	—	10	nAdc

ELECTRICAL CHARACTERISTICS (continued) (T_A = 25°C unless otherwise noted.)

Characteristic		Symbol	Min	Typ	Max	Unit
ON CHARACTERISTICS(2)						
DC Current Gain		h_{FE}				—
(I_C = 10 μAdc, V_{CE} = 5.0 Vdc)	MD3250,A,F,AF		25	75	—	
	MD3251,A,F,AF		50	100	—	
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc)	MD3250,A,F,AF		50	82	150	
	MD3251,A,F,AF		80	170	300	
	MQ3251		80	170	—	
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc, T_A = −55°C)	MD3250,A,F,AF		25	35	—	
	MD3251,A,F,AF		50	75	—	
(I_C = 1.0 mAdc, V_{CE} = 5.0 Vdc)	MD3250,A,F,AF		50	87	150	
	MD3251,A,F,AF		100	180	300	
	MQ3251		100	180	—	
(I_C = 10 mAdc, V_{CE} = 5.0 Vdc)	MD3250,A,F,AF		50	92	—	
	MD3251,A,F,AF		100	190	—	
	MQ3251		100	190	300	
(I_C = 50 mAdc, V_{CE} = 5.0 Vdc)	MD3250,A,F,AF		15	50	—	
	MD3251,A,F,AF		30	90	—	
	MQ3251		30	90	—	
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$				Vdc
(I_C = 10 mAdc, I_B = 1.0 mAdc)			—	0.11	0.25	
(I_C = 50 mAdc, I_B = 5.0 mAdc)			—	0.18	0.5	
Base-Emitter Saturation Voltage		$V_{BE(sat)}$				Vdc
(I_C = 10 mAdc, I_B = 1.0 mAdc)			0.6	0.78	0.9	
(I_C = 50 mAdc, I_B = 5.0 mAdc)			—	0.88	1.2	
SMALL-SIGNAL CHARACTERISTICS						
Current-Gain — Bandwidth Product		f_T				MHz
(I_C = 10 mAdc, V_{CE} = 20 Vdc, f = 100 MHz)	MD3250,A,F,AF		200	600	—	
	MD3251,A,F,AF		250	600	—	
	MQ3251		300	600	—	
Output Capacitance (V_{CB} = 5.0 Vdc, I_E = 0, f = 100 kHz)		C_{obo}	—	2.5	6.0	pF
Input Capacitance (V_{BE} = 1.0 Vdc, I_C = 0, f = 100 kHz)		C_{ibo}	—	6.0	8.0	pF
MATCHING CHARACTERISTICS (MD3250,A,F,AF & MD3251,A,F,AF ONLY)						
DC Current Gain Ratio(3)		h_{FE1}/h_{FE2}				—
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc)			0.9	—	1.0	
(I_C = 1.0 mAdc, V_{CE} = 5.0 Vdc)			0.9	—	1.0	
Base-Emitter Voltage Differential		$\lvert V_{BE1}-V_{BE2}\rvert$				mVdc
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc)			—	—	3.0	
(I_C = 10 μAdc, V_{CE} = 5.0 Vdc)			—	—	5.0	
(I_C = 10 mAdc, V_{CE} = 5.0 Vdc)			—	—	5.0	
Base-Emitter Voltage Differential Change Due to Temperature		$\Delta\lvert V_{BE1}/V_{BE2}\rvert$				mVdc
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc, T_A = −55 to +25°C)			—	—	0.8	
(I_C = 100 μAdc, V_{CE} = 5.0 Vdc, T_A = +25 to +125°C)			—	—	1.0	

(2) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.
(3) The lowest h_{FE} reading is taken as h_{FE1} for this ratio.

FIGURE 1 – CAPACITANCE

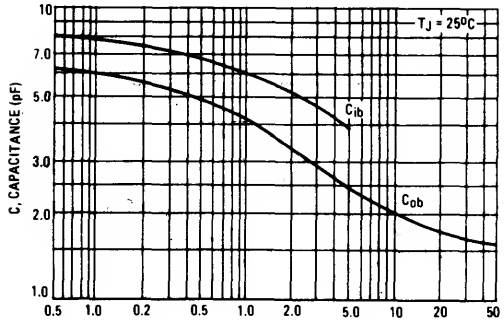
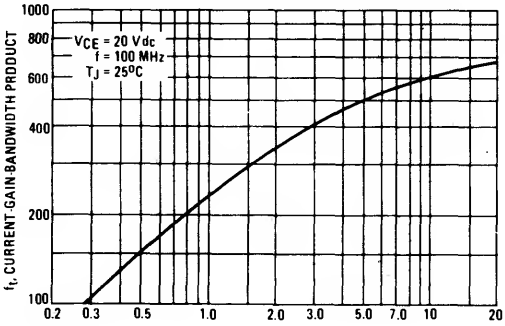


FIGURE 2 – CURRENT-GAIN BANDWIDTH PRODUCT



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NOISE FIGURE VARIATIONS

(V_{CE} = 6.0 V, T_A = 25°C)

FIGURE 3 – EFFECTS OF FREQUENCY

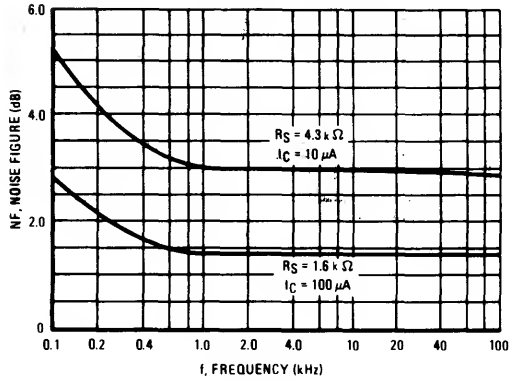

FIGURE 4 – EFFECTS OF SOURCE RESISTANCE

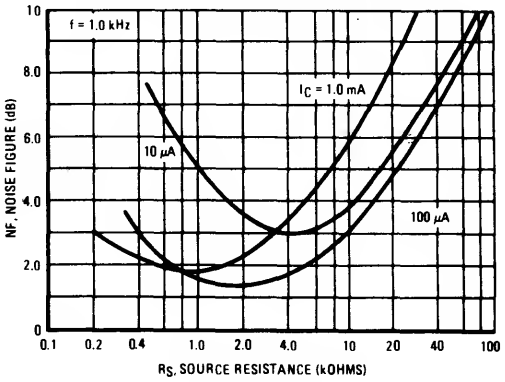

FIGURE 5 – DC CURRENT GAIN

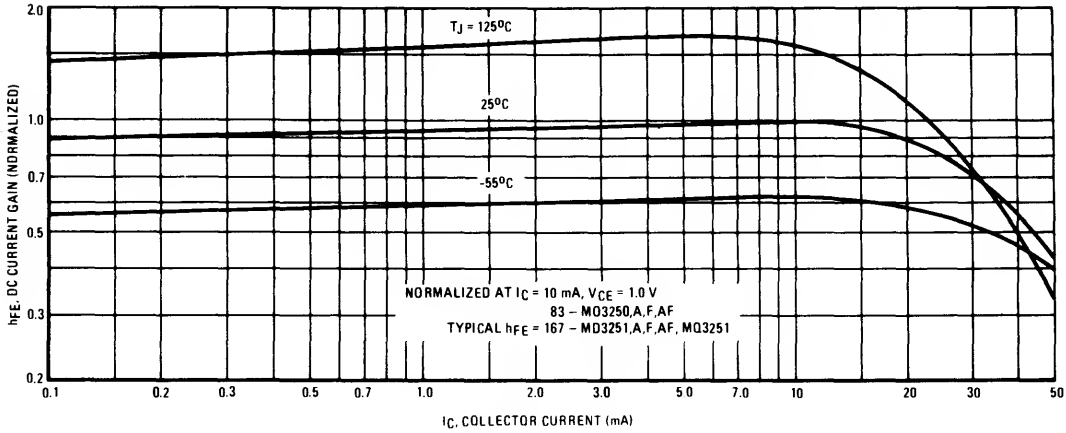

FIGURE 6 – "ON" VOLTAGE

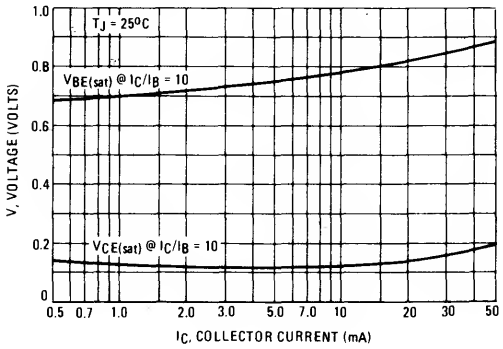

FIGURE 7 – TEMPERATURE COEFFICIENTS

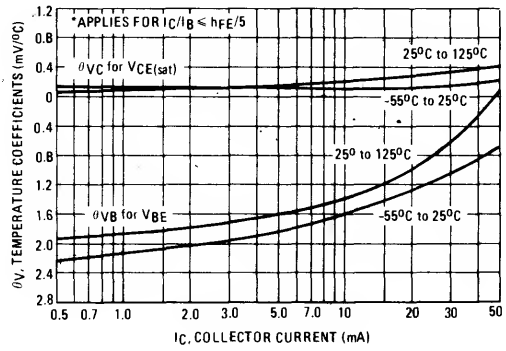

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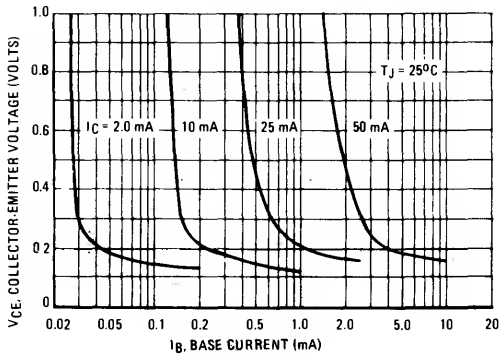
FIGURE 8 – COLLECTOR SATURATION REGION

MD3250



MD3251, MQ3251