

General Description

The MDD1754 uses advanced MagnaChip's Trench MOSFET Technology to provided high performance in on-state resistance, switching performance and reliability.

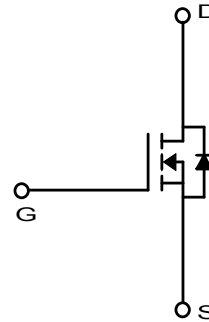
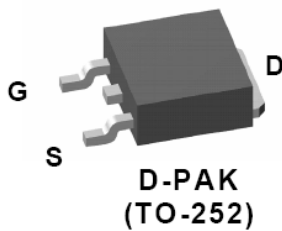
Low $R_{DS(ON)}$, Low Gate Charge can be offering superior benefit in the application.

Features

- $V_{DS} = 40V$
- $I_D = 20.5A (V_{GS} = 10V)$
- $R_{DS(ON)} < 27m\Omega @ V_{GS} = 10V$
 $< 35m\Omega @ V_{GS} = 4.5V$

Applications

- Inverters
- General purpose applications



Absolute Maximum Ratings ($T_C = 25^\circ$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	40	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current (Note 2)	$T_C = 25^\circ C$	I_D	20.5	A
	$T_C = 100^\circ C$		13	A
Pulsed Drain Current		I_{DM}	50	A
Power Dissipation	$T_C = 25^\circ C$	P_D	16.7	W
	$T_C = 100^\circ C$		6.7	
Single Pulse Avalanche Energy (Note 3)		E_{AS}	18	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~+150	$^\circ C$

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	60	$^\circ C/W$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	7.5	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDD1754RH	-55~150°C	TO-252	Tape & Reel	Halogen Free

Electrical Characteristics (T_J =25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	40	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.8	3.0	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 32V, V _{GS} = 0V	-		1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 8A	-	20	27	mΩ
		V _{GS} = 4.5V, I _D = 6A		26	35	
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D = 8A		20	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DD} = 28V, I _D = 8A, V _{GS} = 10V	-	9.2	-	nC
Gate-Source Charge	Q _{gs}		-	1.7	-	
Gate-Drain Charge	Q _{gd}		-	2.2	-	
Input Capacitance	C _{iSS}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	440	-	pF
Reverse Transfer Capacitance	C _{rSS}		-	38	-	
Output Capacitance	C _{oss}		-	76	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V ,V _{DD} = 20V, I _D = 1A, R _{GEN} =3.3Ω	-	5.9	-	ns
Turn-On Rise Time	t _r		-	17.3	-	
Turn-Off Delay Time	t _{d(off)}		-	16.5	-	
Turn-Off Fall Time	t _f		-	10.7	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = 8A, V _{GS} = 0V	-	0.86	1.2	V
Reverse Recovery Time	t _{rr}	I _S = 8A, di/dt=100A/us	-	35	-	ns
Reverse Recovery Charge	Q _{rr}		-	8.8	-	nC

Notes :

1. Surface mounted RF4 board with 2oz. Copper.
2. P_D is based on T_{J(MAX)}=150°C
P_D (T_C=25°C) is based on R_{θJC},
3. Starting T_J=25°C, L=1mH, I_{AS}=6A V_{DD}=20V, V_{GS}=10V

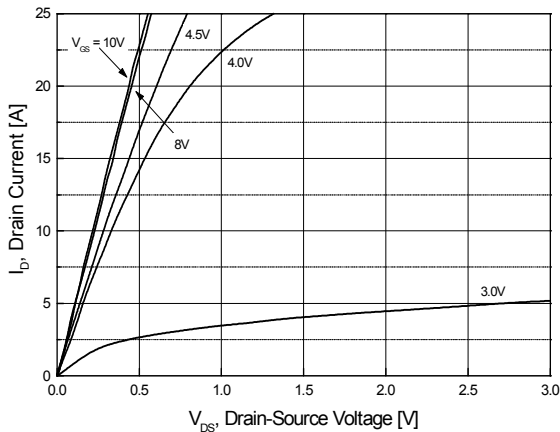


Fig.1 On-Region Characteristics

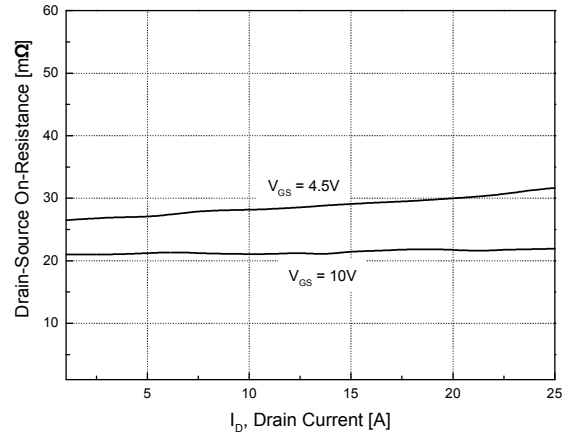


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

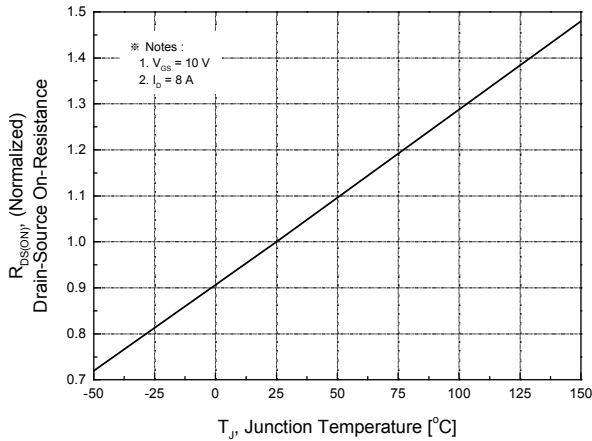


Fig.3 On-Resistance Variation with Temperature

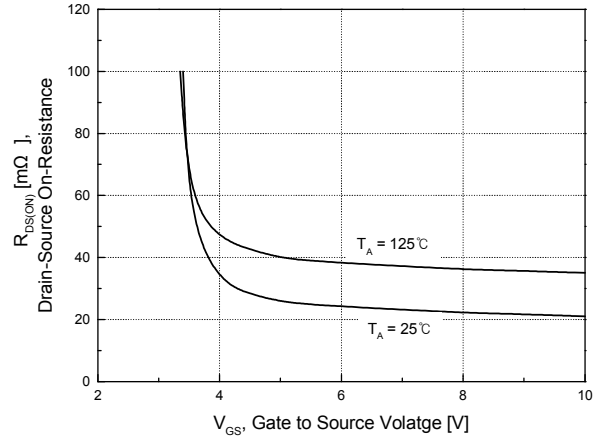


Fig.4 On-Resistance Variation with Gate to Source Voltage

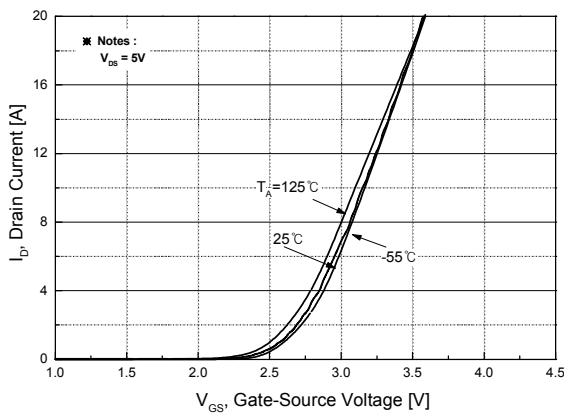


Fig.5 Transfer Characteristics

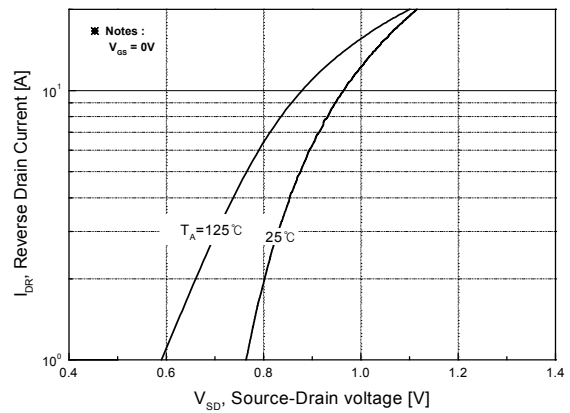
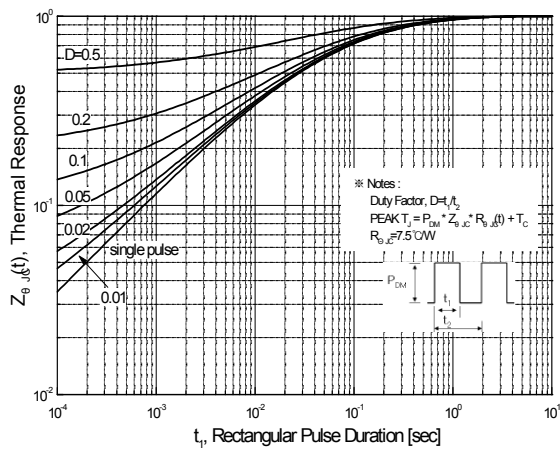
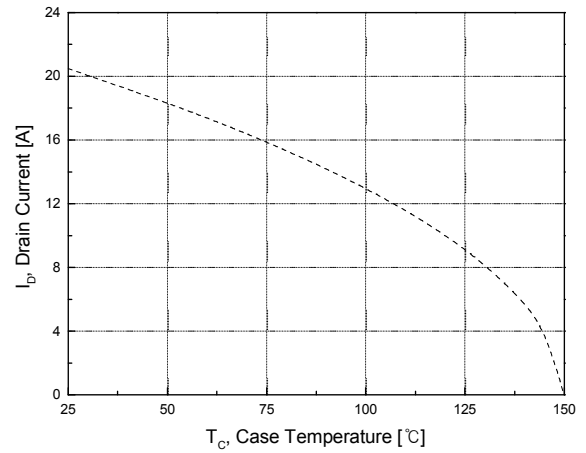
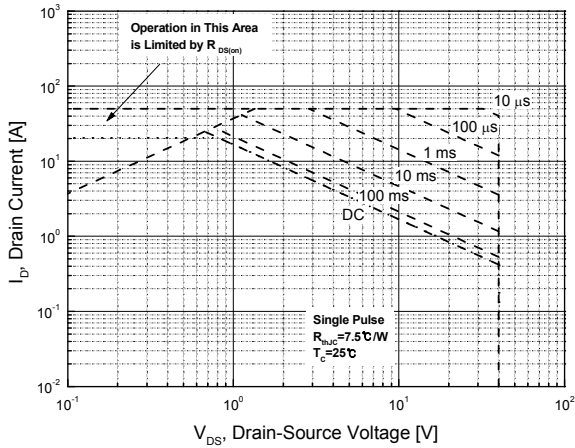
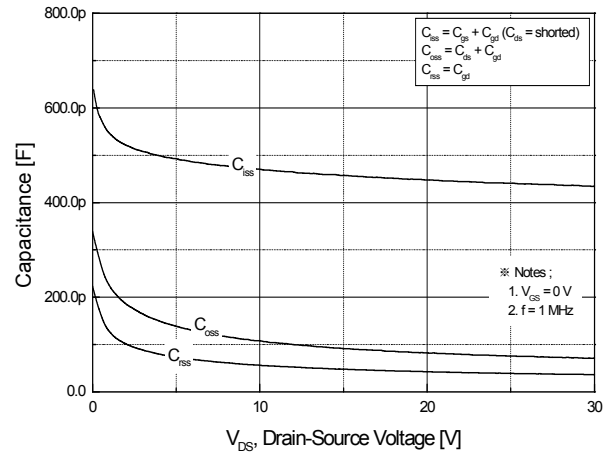
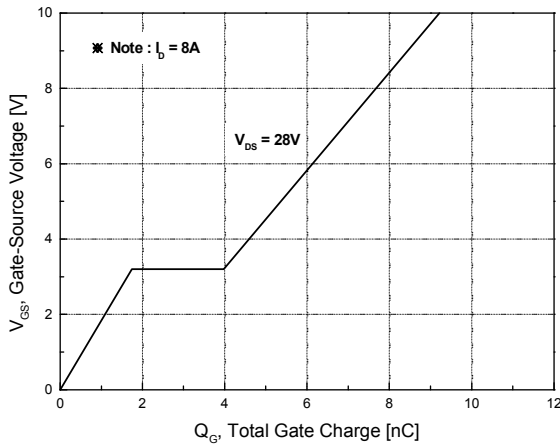


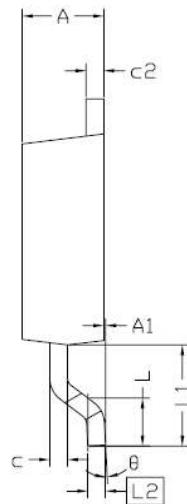
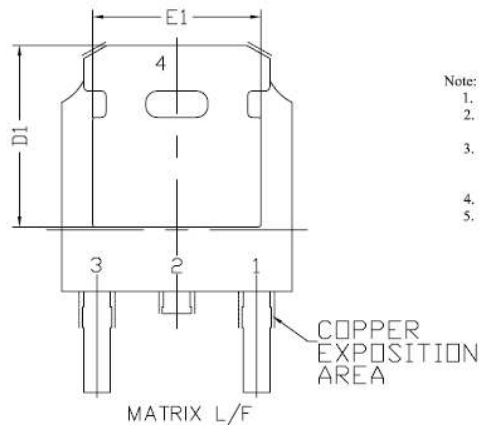
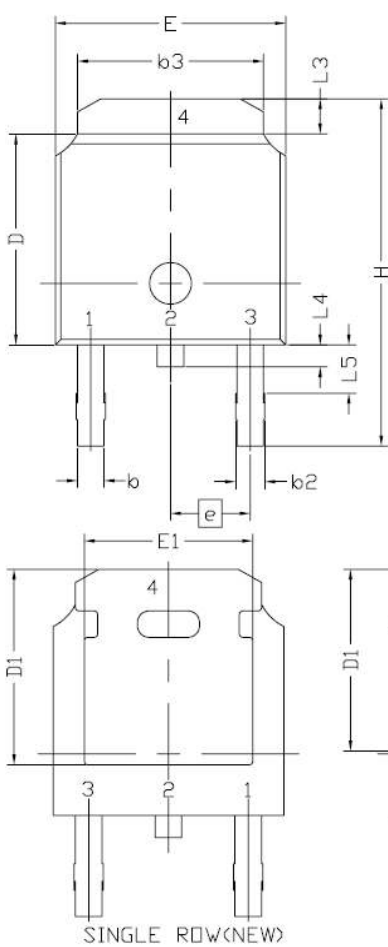
Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature



Physical Dimensions

2 Leads, DPAK (TO252)

Dimensions are in millimeters unless otherwise specified



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1	2.743 REF		
L2	0.508 BSC		
L3	0.89	--	1.27
L4	0.64	--	1.01
L5	--	--	--
D	6.00	6.10	6.223
H	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e	2.286 BSC		
A	2.20	2.30	2.38
A1	0	--	0.127
c	0.45	0.50	0.60
c2	0.45	0.50	0.58
D1	5.30	--	--
E1	4.40	--	--
θ	0°	--	10°

Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
4. The Package Top May Be Smaller Than The Package Bottom.
5. Dimension "b" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.10 mm Total In Excess Of "b" Dimension At Maximum Material Condition. The Dambar Cannot Be Located On The Lower Radius Of The Foot.

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