

MDIB6N70C

N-Channel MOSFET 700V, 5.0A, 1.8Ω

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General Description

The MDIB6N70C use advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

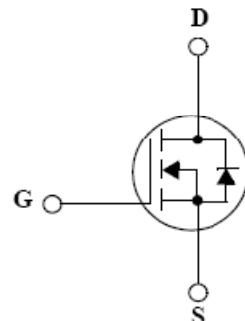
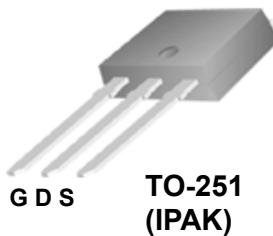
MDIB6N70C is suitable device for SMPS, HID and general purpose applications.

Features

- $V_{DS} = 700V$
- $I_D = 5.0A$
- $R_{DS(ON)} \leq 1.8\Omega$
- $@V_{GS} = 10V$
- $@V_{GS} = 10V$

Applications

- Power Supply
- PFC
- High Current, High Speed Switching



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	700	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current	I_D	5.0	A
		3.15	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	20	A
Power Dissipation	P_D	112	W
		0.89	W/ $^\circ C$
Peak Diode Recovery $dv/dt^{(3)}$	dv/dt	4.5	V/ns
Repetitive Pulse Avalanche Energy ⁽⁴⁾	E_{AR}	11.2	mJ
Single Pulse Avalanche Energy ⁽⁴⁾	E_{AS}	186	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	$^\circ C$

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	110	$^\circ C/W$
Thermal Resistance, Junction-to-Case ⁽¹⁾	$R_{\theta JC}$	1.12	$^\circ C/W$

Ordering Information

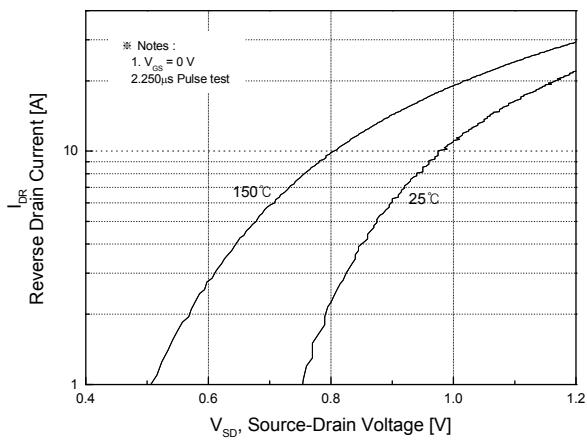
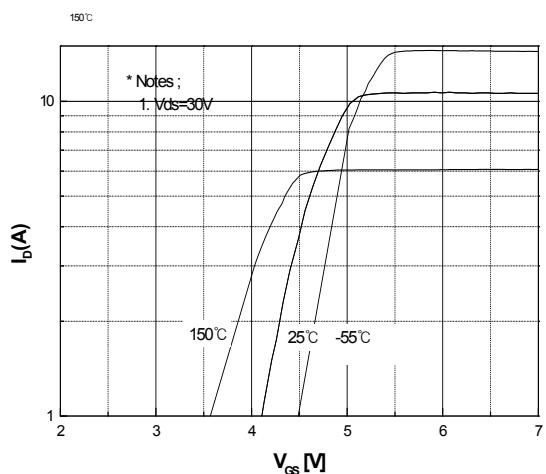
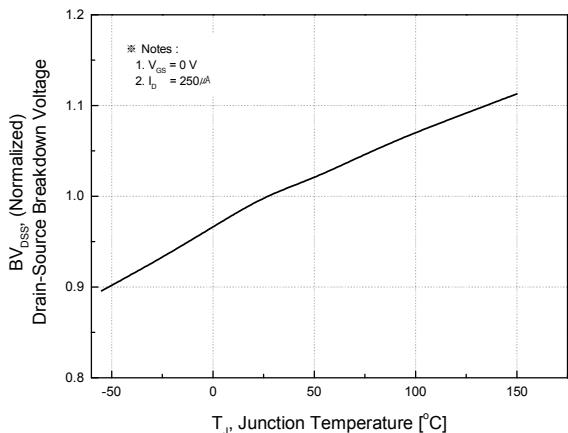
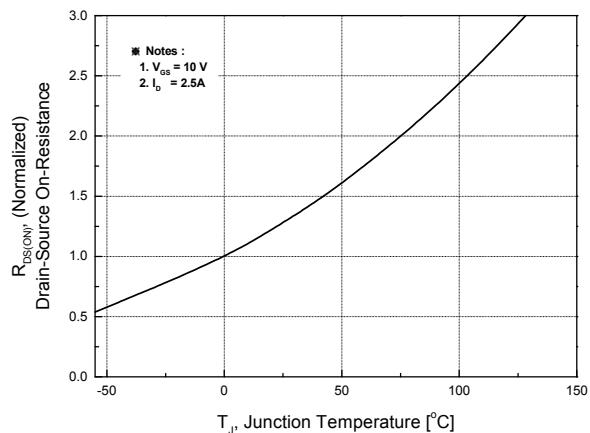
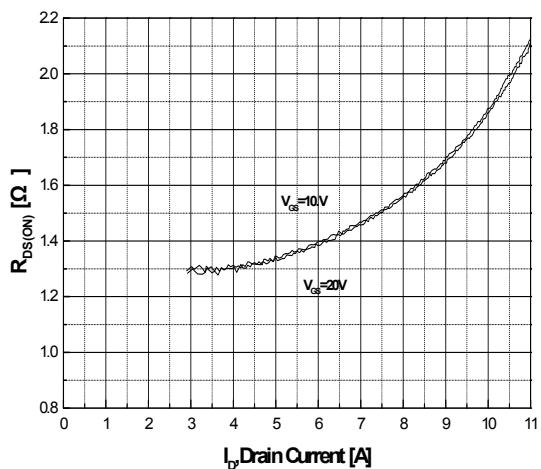
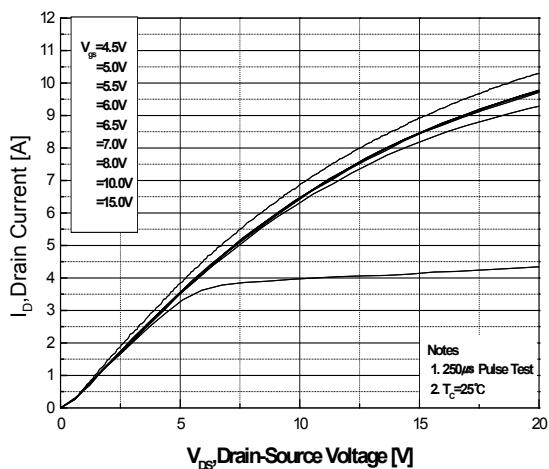
Part Number	Temp. Range	Package	Packing	RoHS Status
MDIB6N70CTH	-55~150°C	TO-251 (IPAK)	Tube	Halogen Free

Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	700	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 700V, V _{GS} = 0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V	-	-	100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 2.5A		1.48	1.8	Ω
Forward Transconductance	g _{fs}	V _{DS} = 30V, I _D = 2.5A	-	7.0	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = 560V, I _D = 6.0A, V _{GS} = 10V	-	18.4		nC
Gate-Source Charge	Q _{gs}		-	3.5	-	
Gate-Drain Charge	Q _{gd}		-	7	-	
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	893	-	pF
Reverse Transfer Capacitance	C _{rss}		-	8.8	-	
Output Capacitance	C _{oss}		-	95	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DS} = 350V, I _D = 6.0A, R _G = 25Ω	-	15.2	-	ns
Rise Time	t _r		-	23.3	-	
Turn-Off Delay Time	t _{d(off)}		-	48	-	
Fall Time	t _f		-	40	-	
Drain-Source Body Diode Characteristics						
Maximum Continuous Drain to Source Diode Forward Current	I _S		-	-	5.0	A
Source-Drain Diode Forward Voltage	V _{SD}	I _S = 5.0, V _{GS} = 0V	-		1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 6.0A, di/dt = 100A/μs	-	311	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	2.5	-	μC

Note :

1. Pulse width is based on R_{θJC} & R_{θJA} and the maximum allowed junction temperature of 150°C
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature T_{J(MAX)}=150°C
3. I_{SD} ≤5.0A, di/dt≤200A/us, V_{DD}≤BV_{DSS}, R_G =25Ω, Starting T_j=25°C
4. L=13.8mH, I_{AS}=5.0A, V_{DD}=50V, R_G=25Ω, Starting T_j=25°C



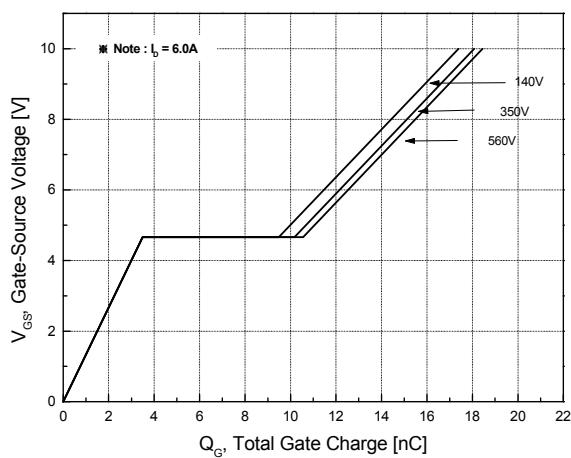


Fig.7 Gate Charge Characteristics

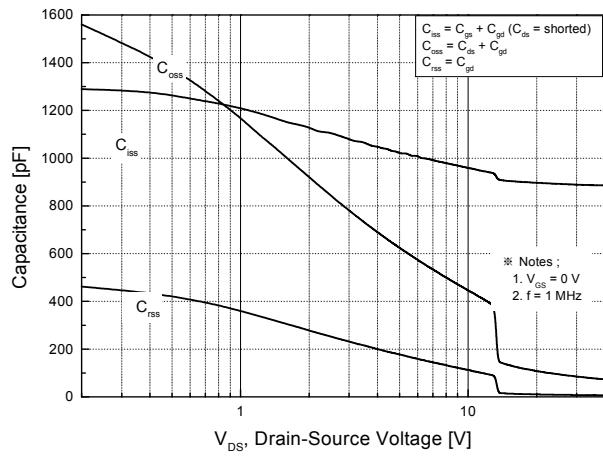


Fig.8 Capacitance Characteristics

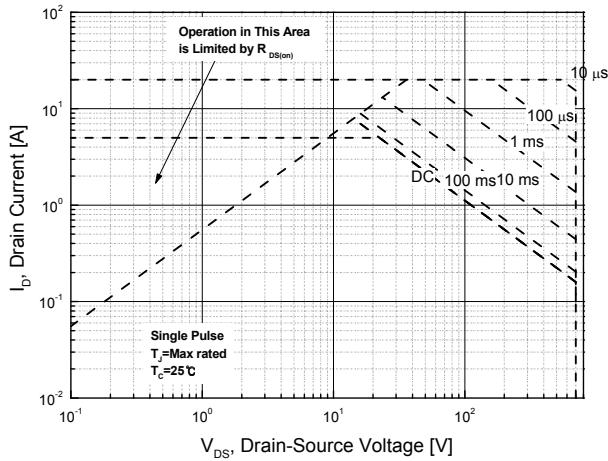


Fig.9 Maximum Safe Operating Area

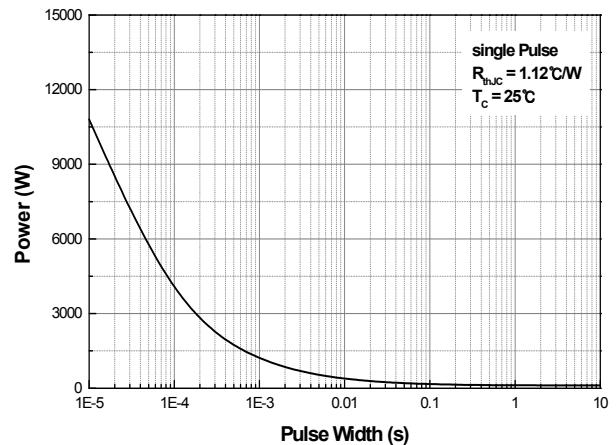


Fig.10 Single Pulse Maximum Power Dissipation

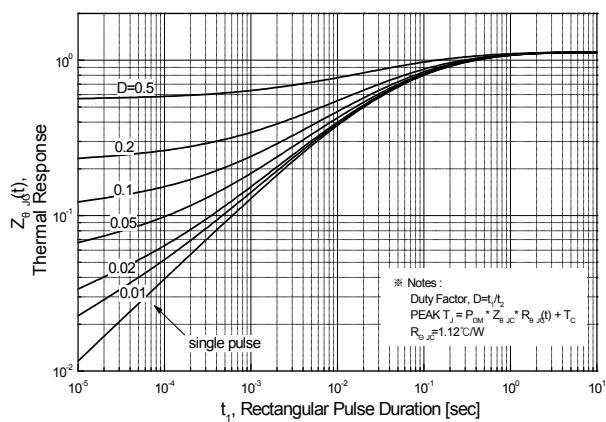


Fig.11 Transient Thermal Response Curve

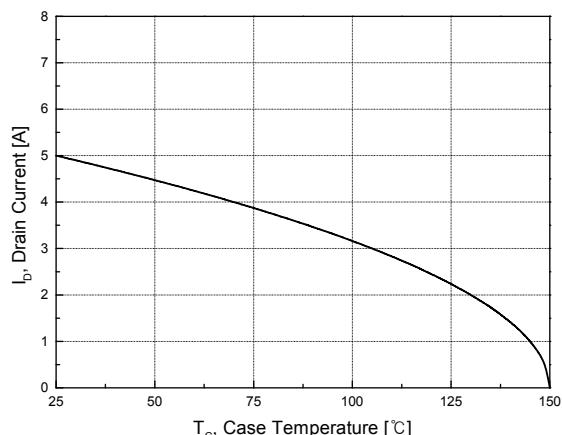
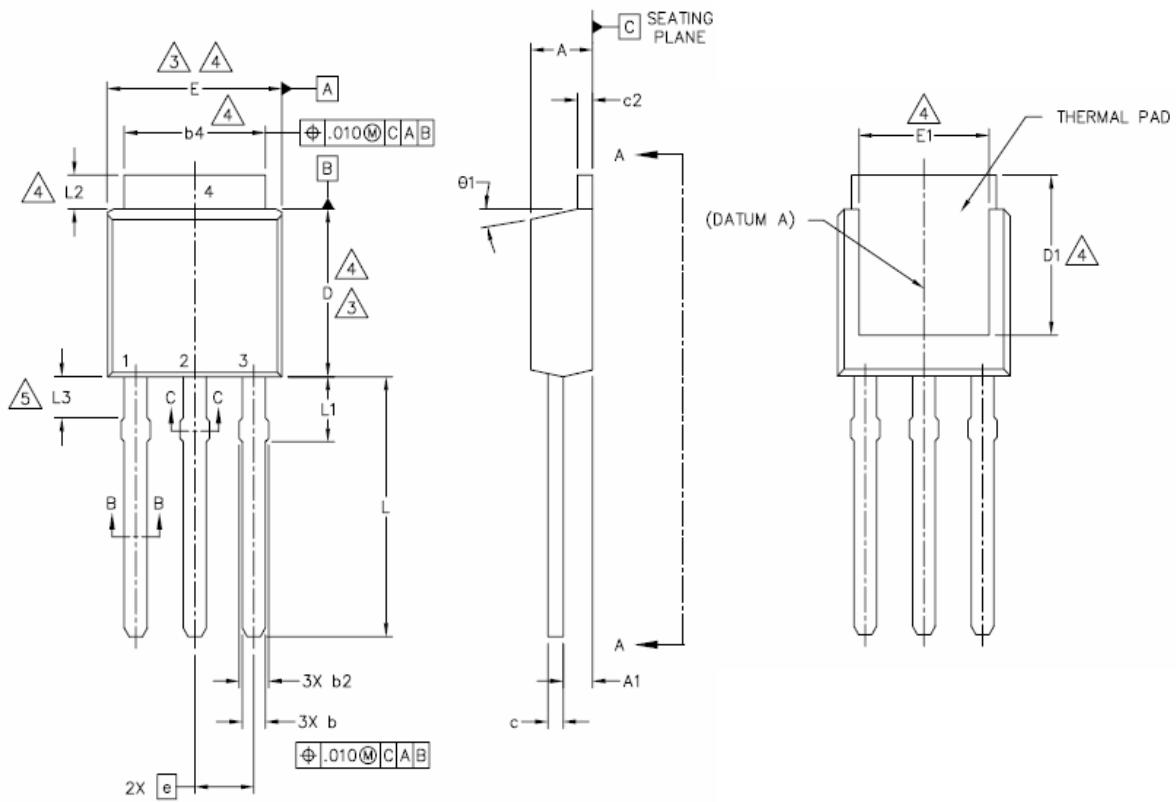


Fig.15 Maximum Drain Current vs. Case Temperature

Physical Dimension

TO-251 (IPAK)

Dimensions are in millimeters, unless otherwise specified



SYMBOL	MIN	NOM	MAX
A	2.18	-	2.39
A1	0.89	-	1.14
b	0.64	-	0.89
b1	0.64	0.71	0.79
b2	0.76	-	1.14
b4	4.95	-	5.46
c	0.46	-	0.61
c2	0.46	-	0.89
D	5.97	6.10	6.22
D1	4.75	-	
E	6.35	-	6.73
E1	4.32	-	0.00
e	2.30 BSC		
L	8.89	-	9.65
L1	1.80	-	2.29
L2	0.70	-	1.27
L3	1.14	-	1.52

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