

### General Description

These N-channel MOSFET are produced using advanced MagnaChip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

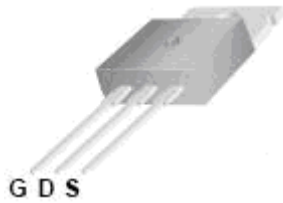
These devices are suitable device for SMPS, high Speed switching and general purpose applications.

### Features

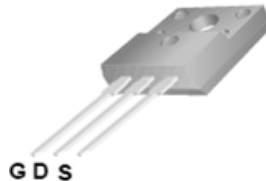
- $V_{DS} = 500V$
- $V_{DS} = 550V$  @  $T_{jmax}$
- $I_D = 16A$  @  $V_{GS} = 10V$
- $R_{DS(ON)} \leq 0.35\Omega$  @  $V_{GS} = 10V$

### Applications

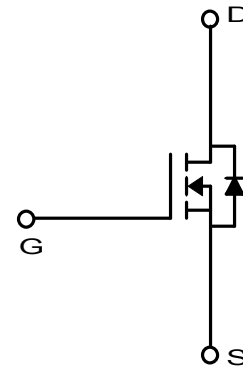
- Power Supply
- HID
- Lighting



TO-220  
MDP Series



TO-220F  
MDF Series



### Absolute Maximum Ratings ( $T_a = 25^\circ C$ )

Characteristics		Symbol	MDP16N50G	MDF16N50G	Unit
Drain-Source Voltage		$V_{DSS}$	500		V
Drain-Source Voltage @ $T_{jmax}$		$V_{DSS} @ T_{jmax}$	550		V
Gate-Source Voltage		$V_{GSS}$	±30		V
Continuous Drain Current	$T_C=25^\circ C$	$I_D$	16	16*	A
	$T_C=100^\circ C$		10.1	10.1*	A
Pulsed Drain Current <sup>(1)</sup>		$I_{DM}$	64	64*	A
Power Dissipation	$T_C=25^\circ C$	$P_D$	204.9	49.4	W W/°C
	Derate above 25 °C		1.64	0.39	
Repetitive Avalanche Energy <sup>(1)</sup>		$E_{AR}$	20.5		mJ
Peak Diode Recovery $dv/dt$ <sup>(3)</sup>		$dv/dt$	4.5		V/ns
Single Pulse Avalanche Energy <sup>(4)</sup>		$E_{AS}$	780		mJ
Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~150		°C

\*  $I_D$  limited by maximum junction temperature

### Thermal Characteristics

Characteristics	Symbol	MDP16N50G	MDF16N50G	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	62.5	62.5	°C/W
Thermal Resistance, Junction-to-Case <sup>(1)</sup>	$R_{\theta JC}$	0.61	2.53	

## Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDP16N50GTH	-55~150°C	TO-220	Tube	Halogen Free
MDF16N50GTH	-55~150°C	TO-220F	Tube	Halogen Free

## Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D = 250\mu A, V_{GS} = 0V$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	3.0	-	5.0	
Drain Cut-Off Current	$I_{DSS}$	$V_{DS} = 500V, V_{GS} = 0V$	-	-	1	$\mu A$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 8A$		0.30	0.35	$\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS} = 30V, I_D = 8A$	-	14.8	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 400V, I_D = 16A, V_{GS} = 10V^{(3)}$	-	34.9		nC
Gate-Source Charge	$Q_{gs}$		-	12.4		
Gate-Drain Charge	$Q_{gd}$		-	14.2		
Input Capacitance	$C_{iss}$	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	-	1724		pF
Reverse Transfer Capacitance	$C_{rSS}$		-	8.3		
Output Capacitance	$C_{oss}$		-	226		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DS} = 250V, I_D = 16A, R_G = 25\Omega^{(3)}$	-	46		ns
Rise Time	$t_r$		-	88.5		
Turn-Off Delay Time	$t_{d(off)}$		-	96.5		
Fall Time	$t_f$		-	41		
<b>Drain-Source Body Diode Characteristics</b>						
Maximum Continuous Drain to Source Diode Forward Current	$I_S$	$I_S = 16A, V_{GS} = 0V$	-	16	-	A
Source-Drain Diode Forward Voltage	$V_{SD}$		-		1.4	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 16A, di/dt = 100A/\mu s^{(3)}$	-	325		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	3.34		$\mu C$

Notes :

- Pulse width is based on  $R_{\theta JC}$  &  $R_{\theta JA}$  and the maximum allowed junction temperature of 150°C.
- Pulse test: pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ , pulse width limited by junction temperature  $T_J(MAX) = 150^\circ C$ .
- $I_{SD} \leq 16.0A$ ,  $di/dt \leq 200A/\mu s$ ,  $V_{DD} = 50V$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ C$
- $L = 5.48mH$ ,  $I_{AS} = 16.0A$ ,  $V_{DD} = 50V$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ C$

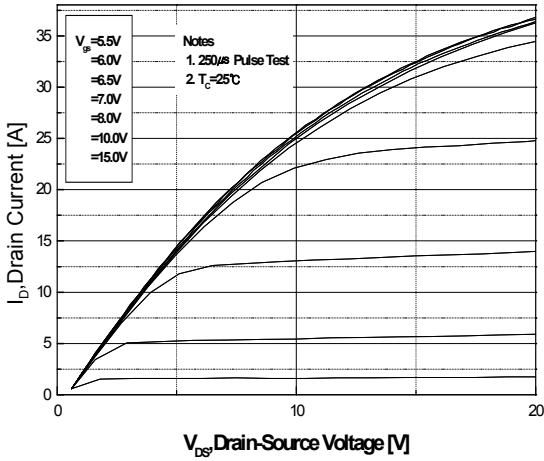


Fig.1 On-Region Characteristics

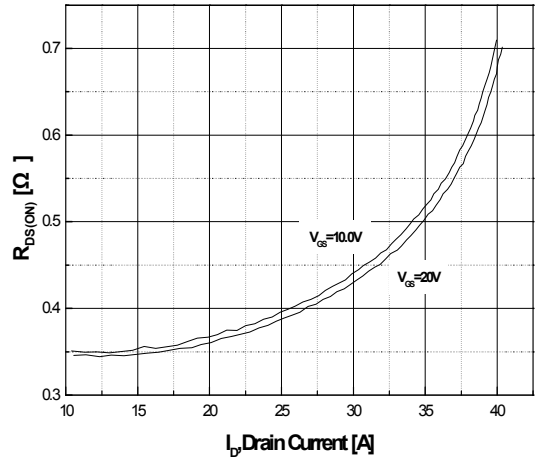


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

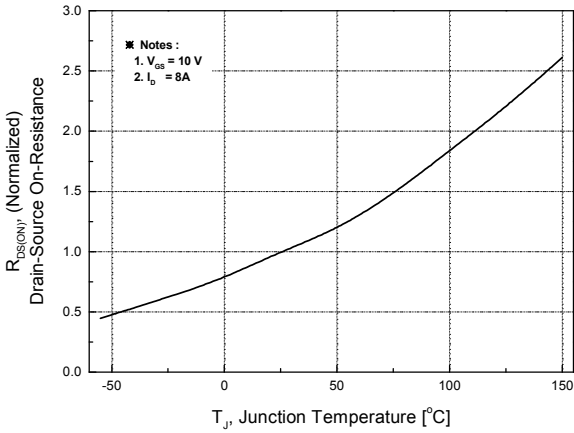


Fig.3 On-Resistance Variation with Temperature

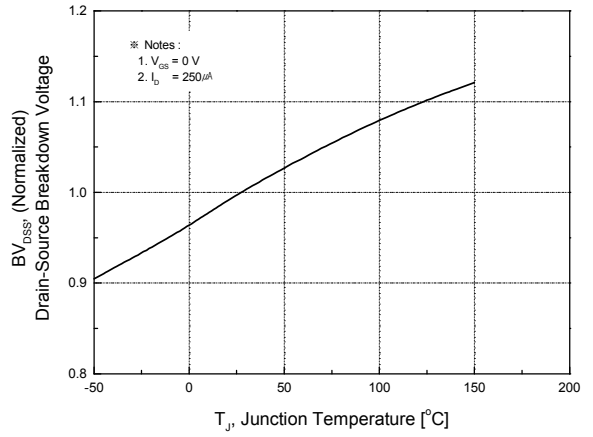


Fig.4 Breakdown Voltage Variation vs. Temperature

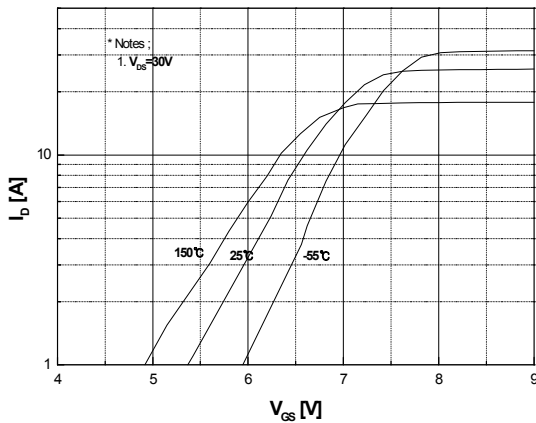


Fig.5 Transfer Characteristics

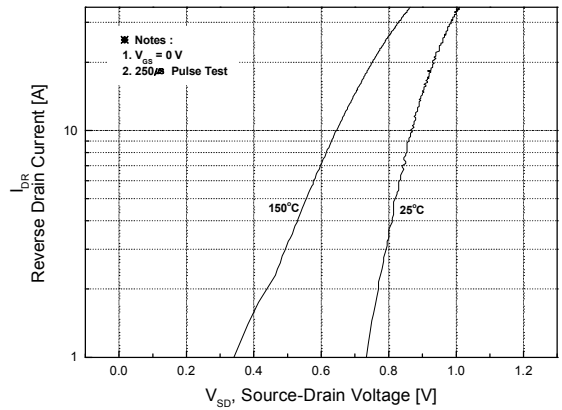


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

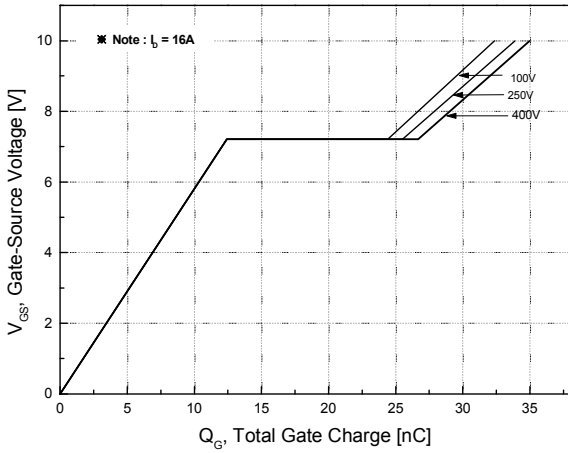


Fig.7 Gate Charge Characteristics

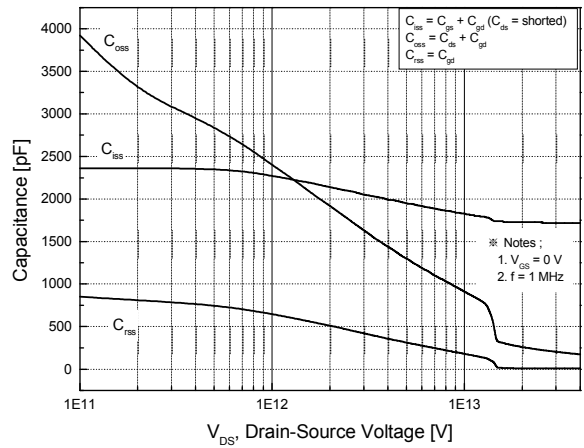


Fig.8 Capacitance Characteristics

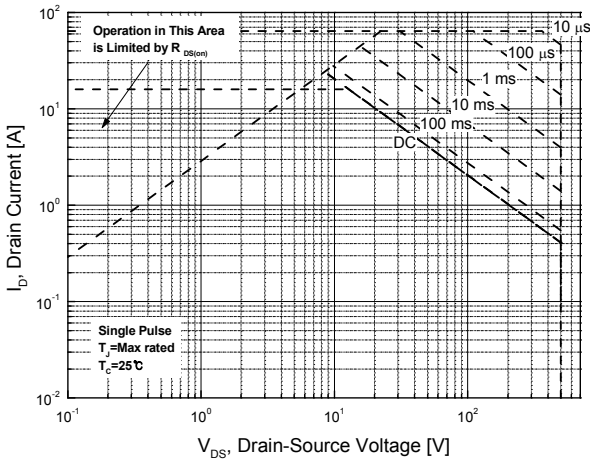


Fig.9 Maximum Safe Operating Area  
MDP16N50G (TO-220)

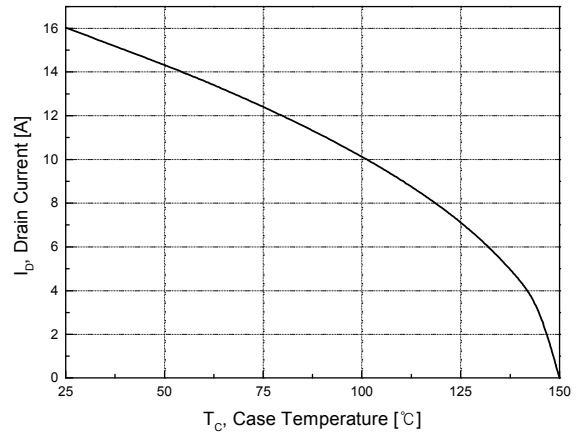


Fig.10 Maximum Drain Current vs. Case Temperature

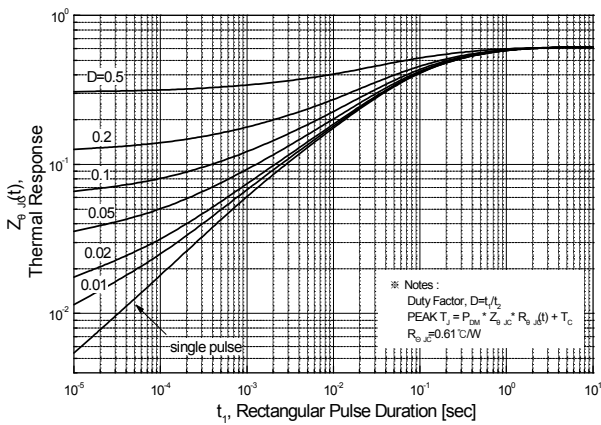


Fig.11 Transient Thermal Response Curve  
MDP16N50G (TO-220)

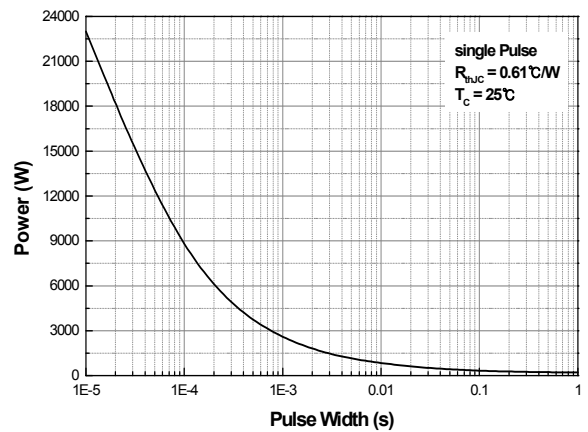
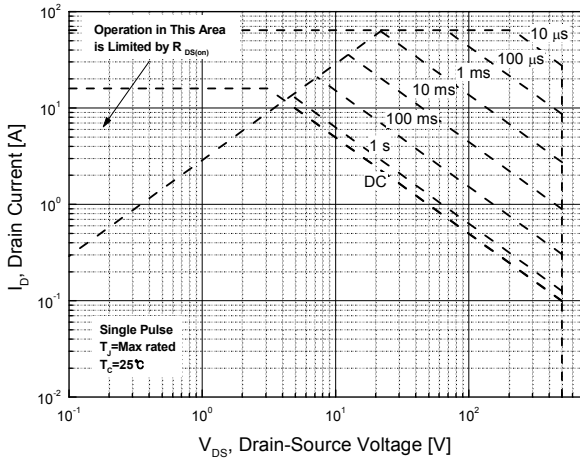
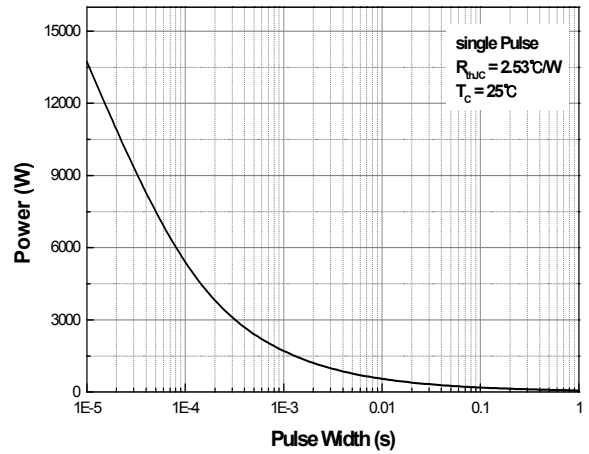


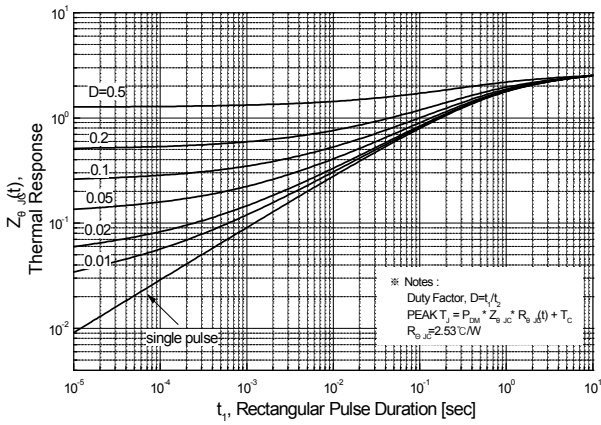
Fig.12 Single Pulse Maximum Power  
Dissipation MDP16N50G (TO-220)



**Fig.13 Maximum Safe Operating Area  
MDF16N50G (TO-220F)**



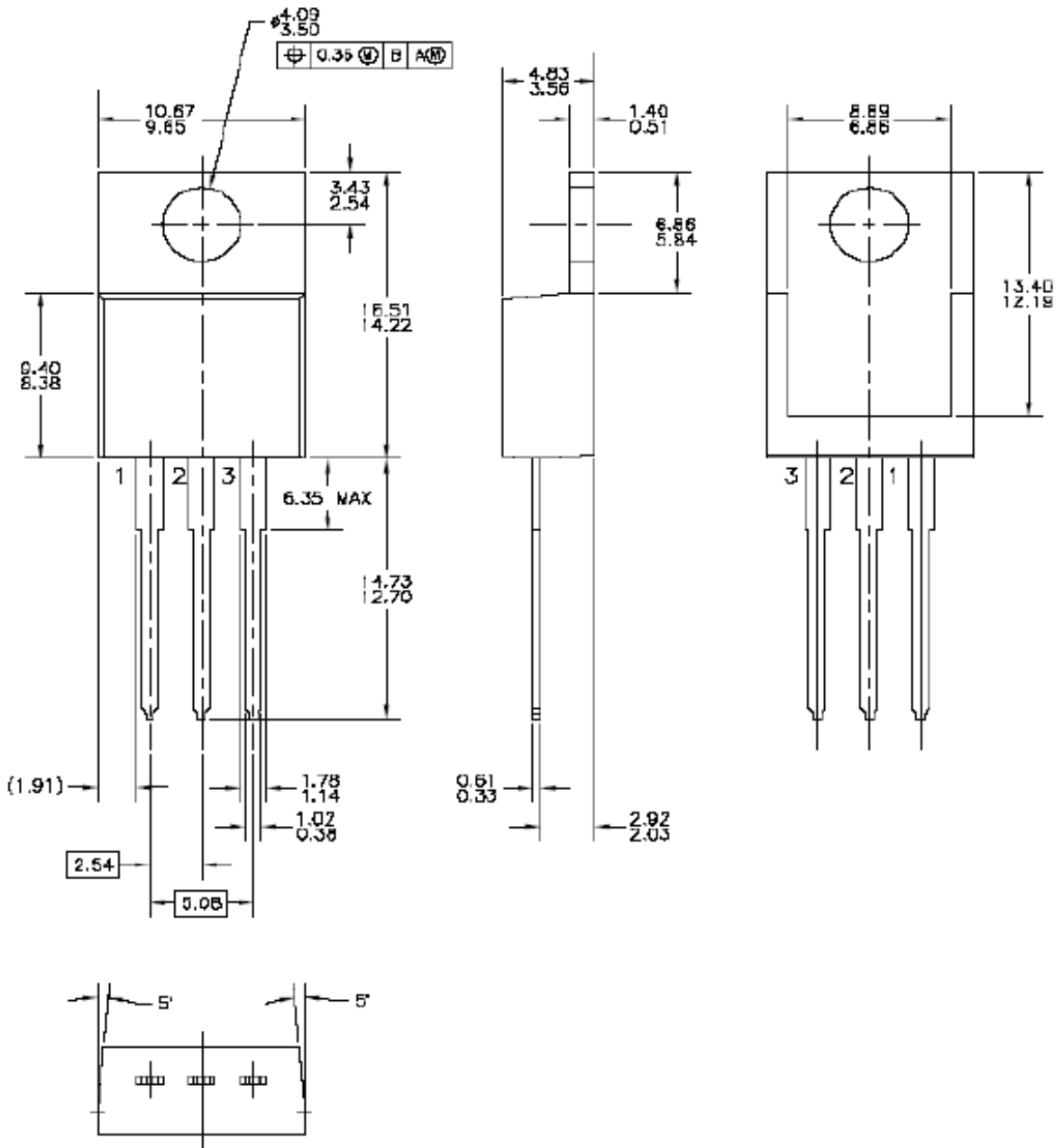
**Fig.12 Single Pulse Maximum Power  
Dissipation MDF16N50G (TO-220F)**



**Fig.11 Transient Thermal Response Curve  
MDF16N50G (TO-220F)**

Physical Dimensions

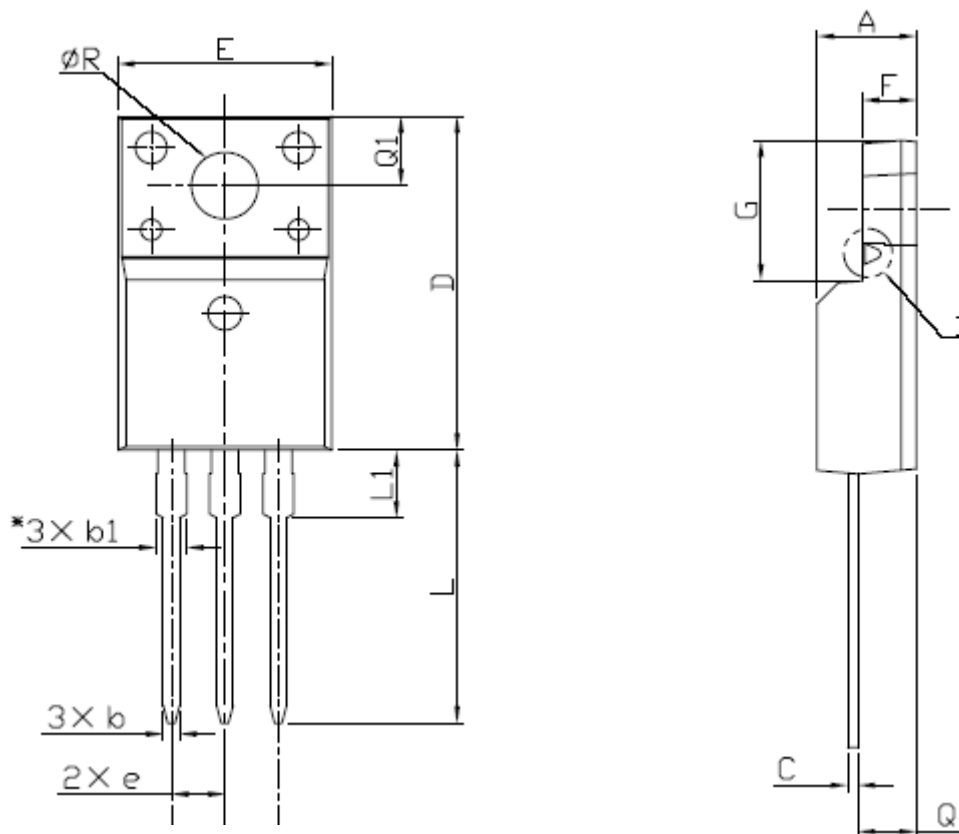
TO-220



## Physical Dimensions

## 3 Leads, TO-220F

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	4.50		4.93
b	0.63		0.91
b1	1.15		1.47
C	0.33		0.63
D	15.47		16.13
E	9.60		10.71
e		2.54	
F	2.34		2.84
G	6.48		6.90
L	12.24		13.72
L1	2.79		3.67
Q	2.52		2.96
Q1	3.10		3.50
ØR	3.00		3.55

**DISCLAIMER:**

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