

# MDP5N50

## N-Channel MOSFET 500V, 5.0 A, 1.4Ω

MDP5N50 N-channel MOSFET 500V

### General Description

The MDP5N50 uses advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

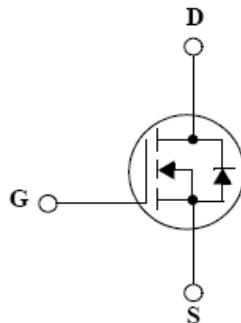
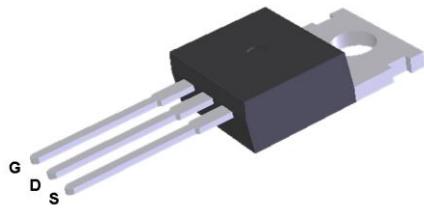
MDP5N50 is suitable device for SMPS, HID and general purpose applications.

### Features

- $V_{DS} = 500V$
- $I_D = 5.0A$  @  $V_{GS} = 10V$
- $R_{DS(ON)} \leq 1.4\Omega$  @  $V_{GS} = 10V$

### Applications

- Power Supply
- PFC
- Ballast



### Absolute Maximum Ratings ( $T_a = 25^\circ C$ )

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DSS}$	500	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	$T_c=25^\circ C$	$I_D$	5.0	A
	$T_c=100^\circ C$		3.2	A
Pulsed Drain Current <sup>(1)</sup>		$I_{DM}$	20	A
Power Dissipation	$T_c=25^\circ C$	$P_D$	93	W
	Derate above 25 °C		0.74	W/°C
Peak Diode Recovery $dv/dt$ <sup>(3)</sup>		$dv/dt$	4.5	V/ns
Single Pulse Avalanche Energy <sup>(4)</sup>		$E_{AS}$	230	mJ
Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~150	°C

### Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case <sup>(1)</sup>	$R_{\theta JC}$	1.35	

## Ordering Information

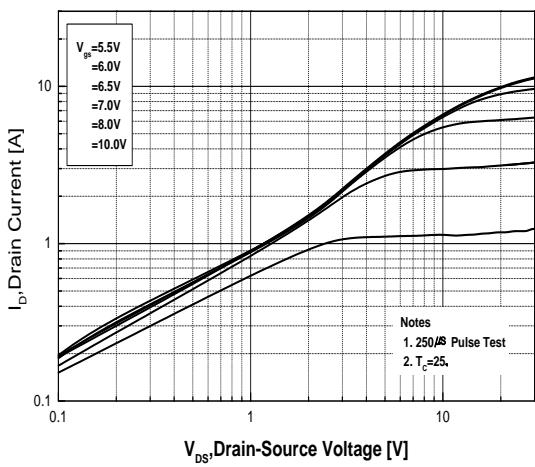
Part Number	Temp. Range	Package	Packing	RoHS Status
MDP5N50TP	-55~150°C	TO-220	Tube	Pb Free
MDP5N50TH	-55~150°C	TO-220	Tube	Halogen Free

## Electrical Characteristics (Ta = 25°C)

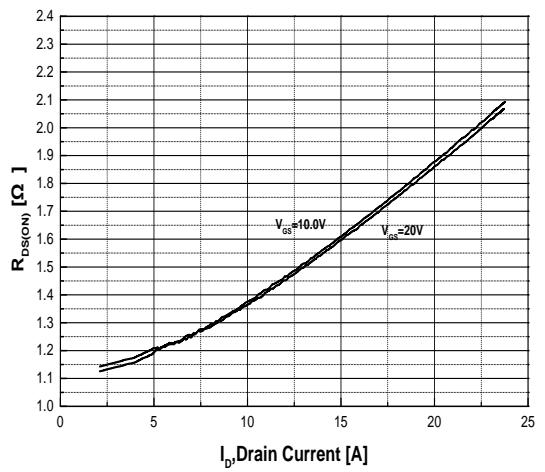
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	500	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	3.0	-	5.0	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 500V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V	-	-	100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 2.5A	-	1.15	1.4	Ω
Forward Transconductance	g <sub>f</sub>	V <sub>DS</sub> = 30V, I <sub>D</sub> = 2.5A	-	5	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 400V, I <sub>D</sub> = 5.0A, V <sub>GS</sub> = 10V <sup>(3)</sup>	-	12.2	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.6	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	4.7	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	500	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	3	-	
Output Capacitance	C <sub>oss</sub>		-	65	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 250V, I <sub>D</sub> = 5.0A, R <sub>G</sub> = 25Ω <sup>(3)</sup>	-	12	-	ns
Rise Time	t <sub>r</sub>		-	24	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	24	-	
Fall Time	t <sub>f</sub>		-	22	-	
<b>Drain-Source Body Diode Characteristics</b>						
Maximum Continuous Drain to Source Diode Forward Current	I <sub>S</sub>		-	5.0	-	A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 5.0A, V <sub>GS</sub> = 0V	-	-	1.4	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 5.0A, dI/dt = 100A/μs <sup>(3)</sup>	-	232	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	1.3	-	μC

Note :

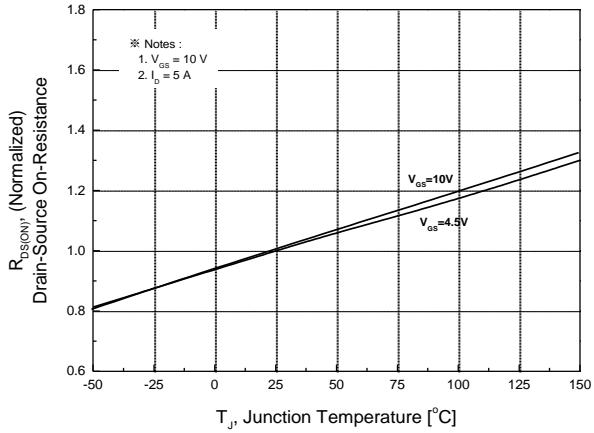
1. Pulse width is based on R<sub>ESC</sub> & R<sub>aja</sub>, and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
3. I<sub>D</sub>≤5.0A, di/dt≤200A/us, V<sub>DD</sub>=50V, R<sub>g</sub>=25Ω, Starting T<sub>j</sub>=25°C
4. L=16.5mH, I<sub>S</sub>=5.0A, V<sub>DD</sub>=50V, , R<sub>g</sub>=25Ω, Starting T<sub>j</sub>=25°C



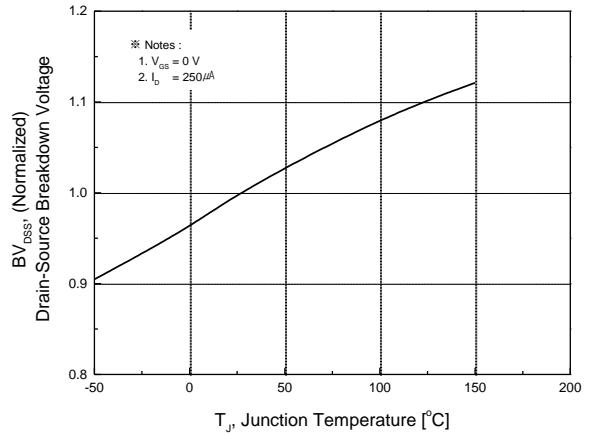
**Fig.1 On-Region Characteristics**



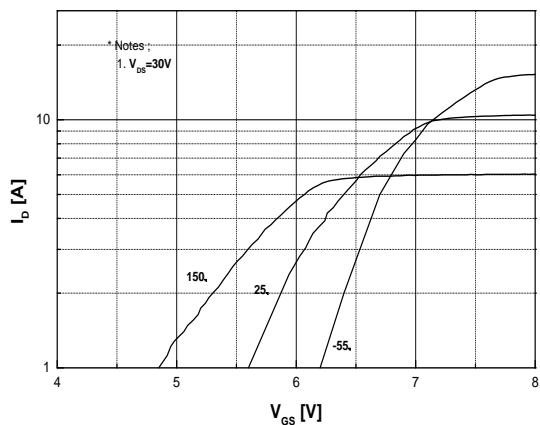
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



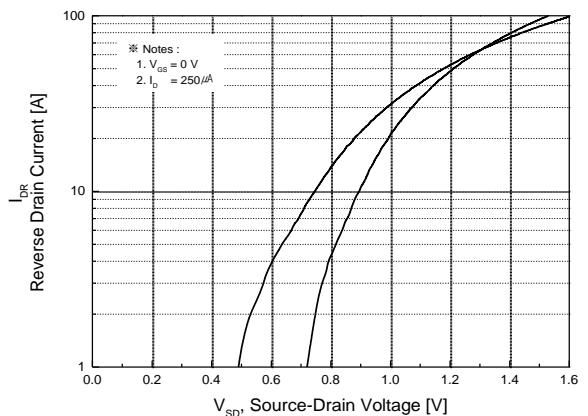
**Fig.3 On-Resistance Variation with Temperature**



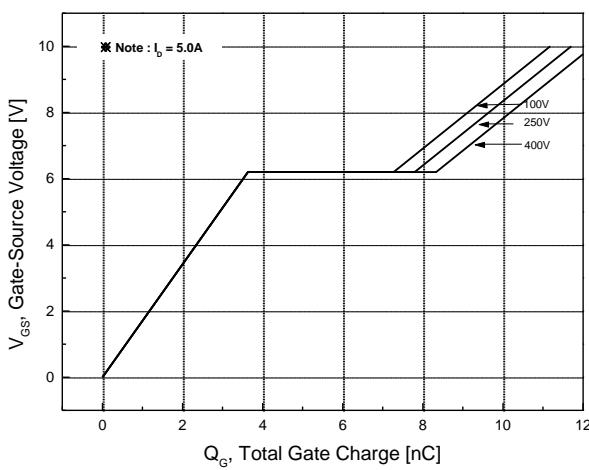
**Fig.4 Breakdown Voltage Variation vs. Temperature**



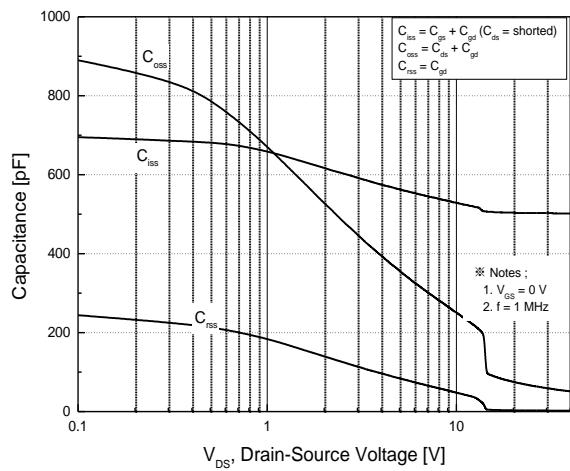
**Fig.5 Transfer Characteristics**



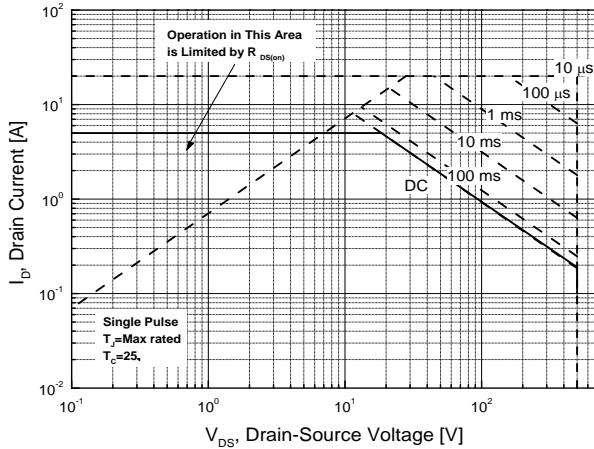
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



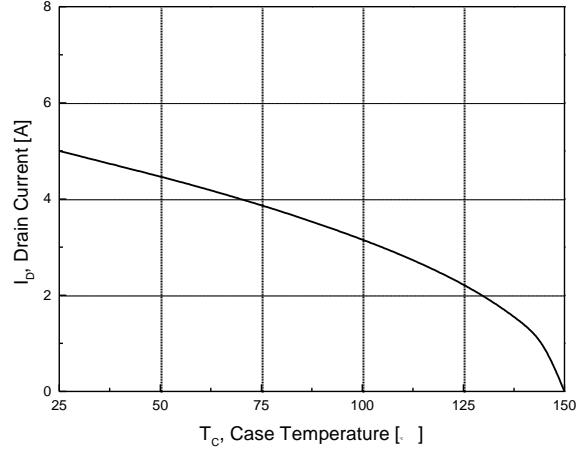
**Fig.7 Gate Charge Characteristics**



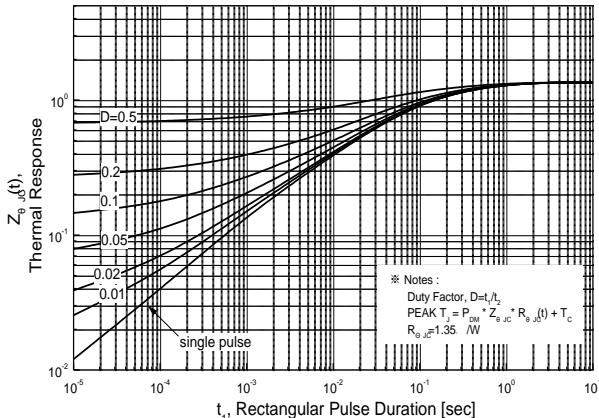
**Fig.8 Capacitance Characteristics**



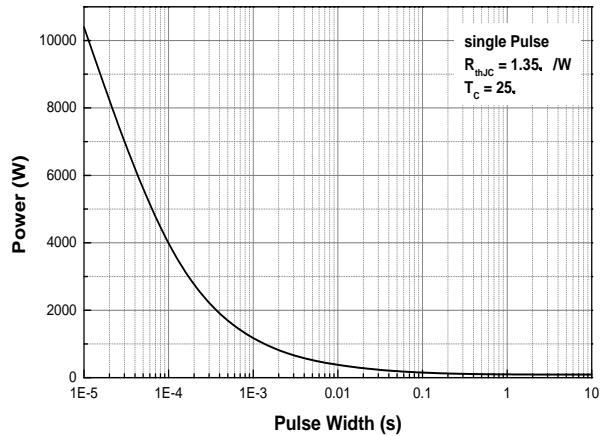
**Fig.9 Maximum Safe Operating Area**



**Fig.10 Maximum Drain Current vs. Case Temperature**



**Fig.11 Transient Thermal Response Curve**

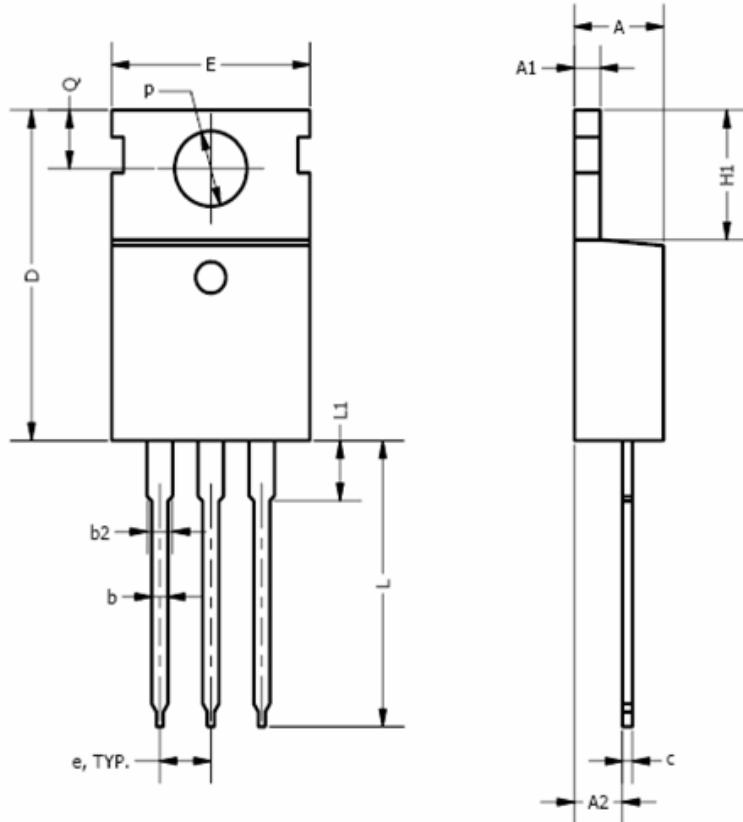


**Fig.12 Single Pulse Maximum Power Dissipation**

## Physical Dimensions

### 3 Leads, TO-220

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	3.56		4.83
A1	0.50		1.40
A2	2.03		2.92
b	0.38	0.69	1.02
b2	1.14	1.45	1.78
c	0.36		0.61
D	14.22		16.51
e	2.54 TYP		
E	9.65		10.67
H1	5.84		6.86
L	12.70		14.73
L1			6.35
φP	3.53		4.09
Q	2.54		3.43

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