

General Description

The MDP9N50 uses advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

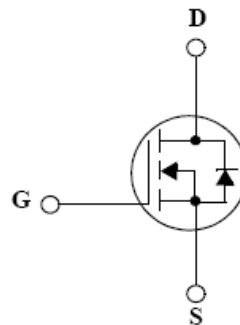
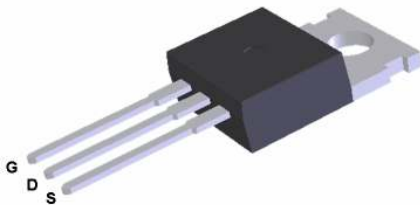
MDP9N50 is suitable device for SMPS, HID and general purpose applications.

Features

- $V_{DS} = 500V$
- $I_D = 9.0A$ @ $V_{GS} = 10V$
- $R_{DS(ON)} < 0.85\Omega$ @ $V_{GS} = 10V$

Applications

- Power Supply
- HID
- Lighting



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	500	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	$T_C = 25^\circ C$	I_D	9.0	A
	$T_C = 100^\circ C$		5.5	A
Pulsed Drain Current ⁽¹⁾		I_{DM}	36	A
Power Dissipation	$T_C = 25^\circ C$	P_D	120	W
	Derate above $25^\circ C$		0.95	W/°C
Peak Diode Recovery dv/dt ⁽³⁾		Dv/dt	4.5	V/ns
Single Pulse Avalanche Energy ⁽⁴⁾		E_{AS}	300	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case ⁽¹⁾	$R_{\theta JC}$	1.05	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDP9N50TH	-55~150°C	TO-220	Tube	Halogen Free

Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	500	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0	-	5.0	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 500V, V _{GS} = 0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V	-	-	100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 4.5A		0.72	0.85	Ω
Forward Transconductance	g _{fs}	V _{DS} = 30V, I _D = 4.5A	-	7	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = 400V, I _D = 9.0A, V _{GS} = 10V ⁽³⁾	-	19		nC
Gate-Source Charge	Q _{gs}		-	5.7		
Gate-Drain Charge	Q _{gd}		-	7.6		
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	780		pF
Reverse Transfer Capacitance	C _{rss}		-	4.0		
Output Capacitance	C _{oss}		-	100		
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DS} = 250V, I _D = 9.0A, R _G = 25Ω ⁽³⁾	-	18		ns
Rise Time	t _r		-	34		
Turn-Off Delay Time	t _{d(off)}		-	38		
Fall Time	t _f		-	27		
Drain-Source Body Diode Characteristics						
Maximum Continuous Drain to Source Diode Forward Current	I _S	I _S = 9.0A, V _{GS} = 0V	-	9.0	-	A
Source-Drain Diode Forward Voltage	V _{SD}		-		1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 9.0A, dI/dt = 100A/μs ⁽³⁾	-	272		ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	2.0		μC

Note :

1. Pulse width is based on R θ_{JC} & R θ_{JA} and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$, pulse width limited by junction temperature $T_J(MAX)=150^\circ C$.
3. $I_{SD} \leq 9.0A$, $di/dt \leq 200A/\mu s$, $V_{DD}=50V$, $R_g = 25\Omega$, Starting $T_J=25^\circ C$
4. $L=6.7mH$, $I_{AS}=9.0A$, $V_{DD}=50V$, $R_g = 25\Omega$, Starting $T_J=25^\circ C$

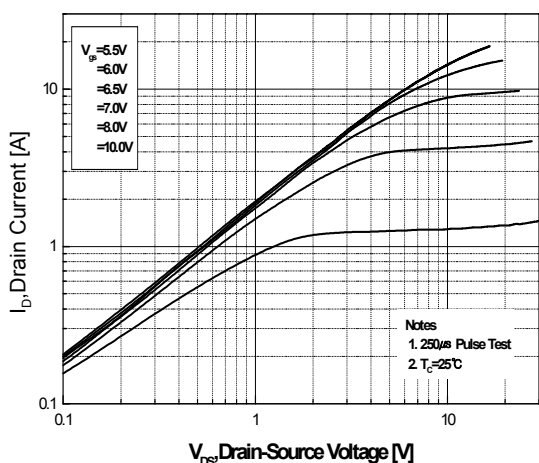


Fig.1 On-Region Characteristics

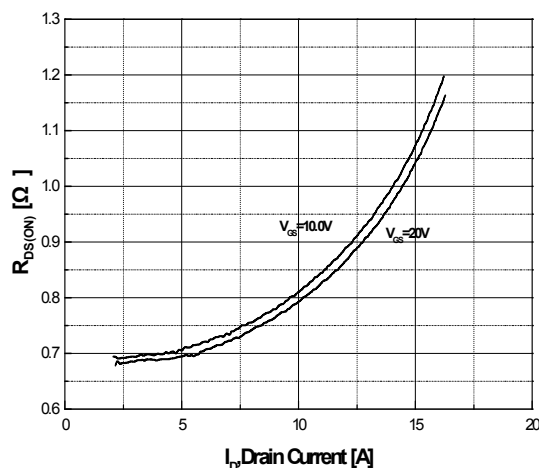


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

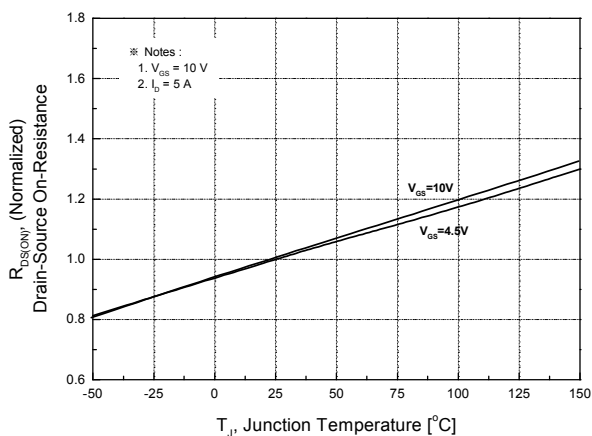


Fig.3 On-Resistance Variation with Temperature

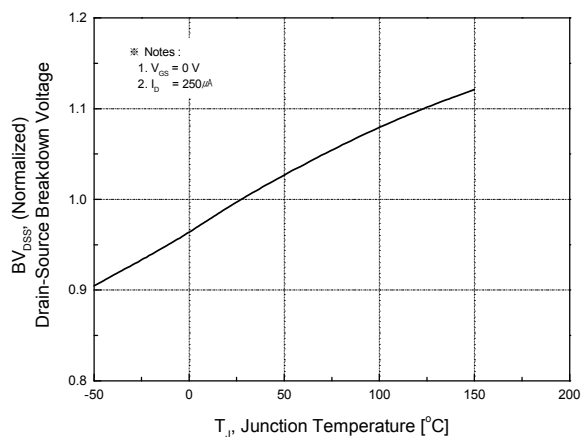


Fig.4 Breakdown Voltage Variation vs. Temperature

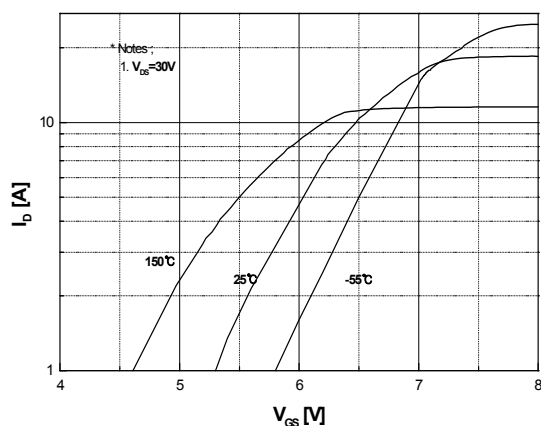


Fig.5 Transfer Characteristics

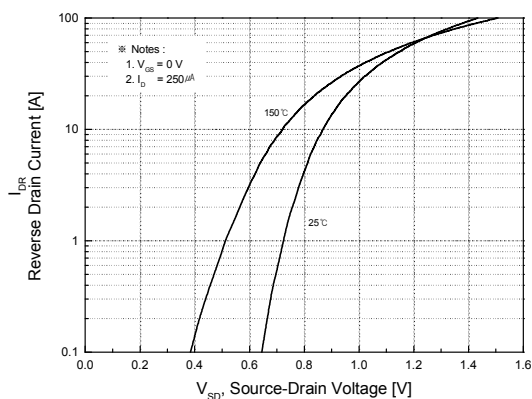


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

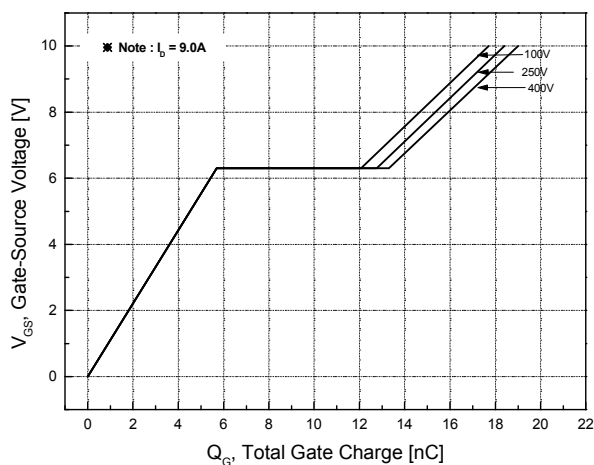


Fig.7 Gate Charge Characteristics

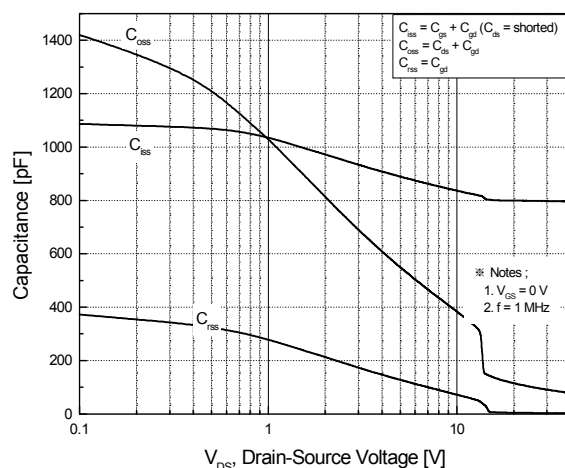


Fig.8 Capacitance Characteristics

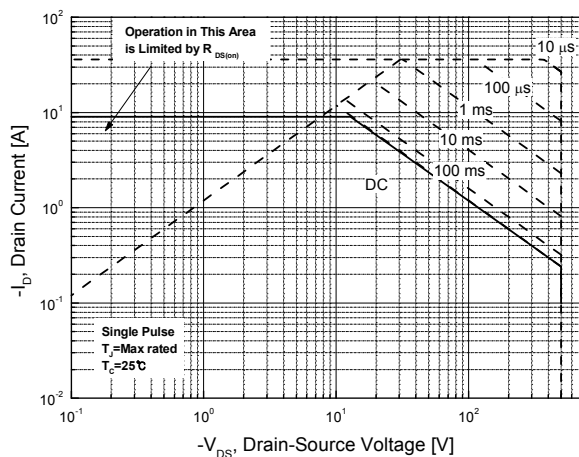


Fig.9 Maximum Safe Operating Area

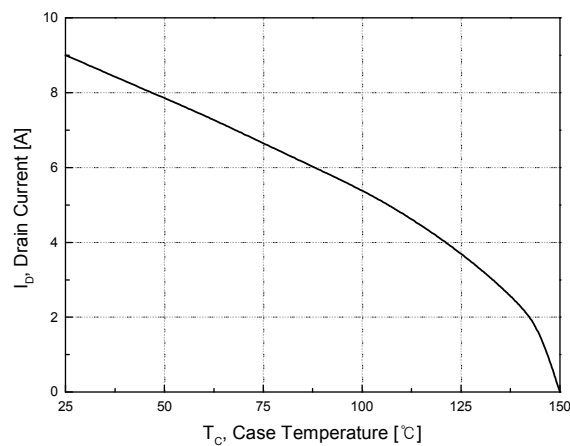


Fig.10 Maximum Drain Current vs. Case Temperature

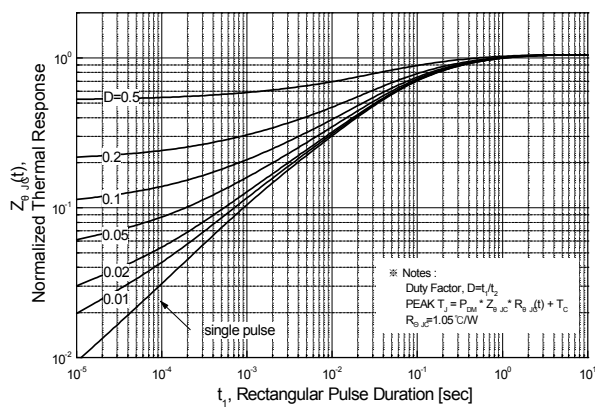


Fig.11 Transient Thermal Response Curve

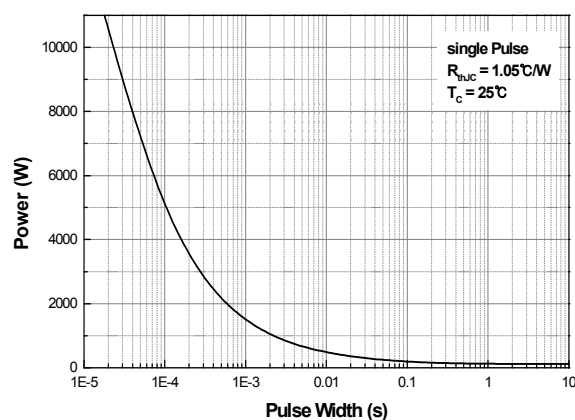
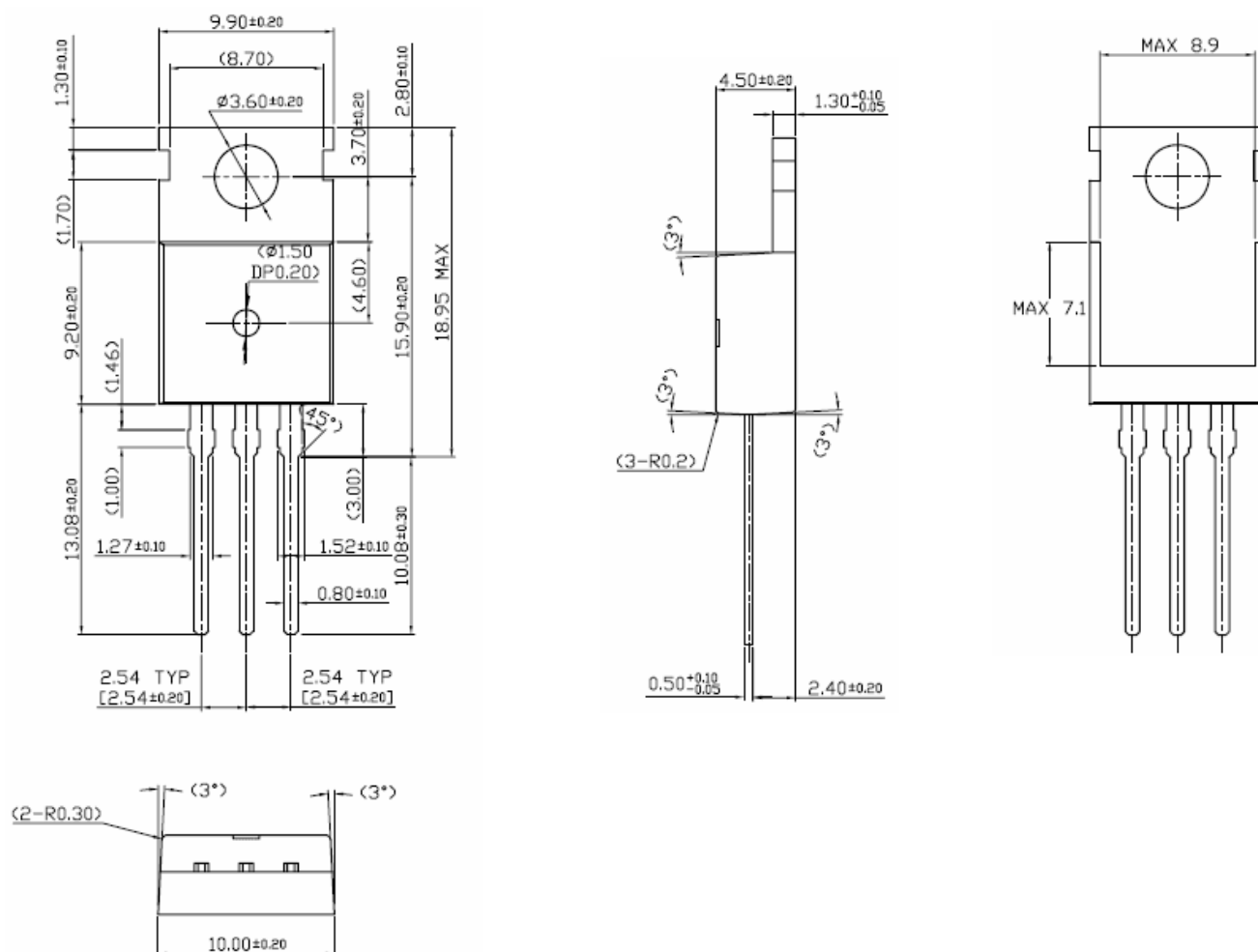


Fig.12 Single Pulse Maximum Power Dissipation

Physical Dimension

TO220, 3L

Dimensions are in millimeters, unless otherwise specified



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