

**Fast Logic**

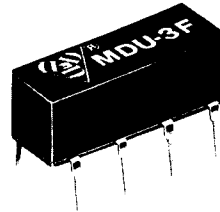
# Multiple Digital Delay Units

**SERIES: MDU-3F**  
(14 pins DIP) TTL Interfaced



**Features:**

- Auto-insertable.
- Completely interfaced for TTL & DTL application.
- P.C. board space economy achieved.
- Fits standard 14 pins DIP socket.
- 3 equal delay units/package.



**Specifications:**

- Delay tolerance:  $\pm 5\%$  or 2 ns whichever is greater.
- Rise-time: 2 ns typically.
- Temperature coefficient: 100 PPM/ $^{\circ}\text{C}$ .
- Temperature range:  $0^{\circ}$  to  $+70^{\circ}\text{C}$ .
- Input pulse width: Min. 100% of total delay.
- Input rep. rate = Min.  $3 \times$  pulse width.
- Supply voltage: 5 Vdc  $\pm 5\%$ .
- Supply Current:
  - $I_{CC1}$ : 74 ma.
  - $I_{CC2}$ : 14 ma.
- DC parameters: See TTL-Fast Schottky Logic Table on Page 6.

**Test Conditions:**

- Input Pulse Width:  $>150\%$  of total delay.
- Time delay measured @ 1.5 V on rising edge.
- Rise-time is measured from .75 V to 2.4 V of leading time.
- All measurements made @  $V_{CC} = 5\text{V}$ ;  $T_A = +25^{\circ}\text{C}$ , no loads on taps.

Part No.	Delay Per Line (ns)
MDU-3F-5	5
MDU-3F-10	10
MDU-3F-15	15
MDU-3F-20	20
MDU-3F-25	25
MDU-3F-30	30
MDU-3F-35	36
MDU-3F-40	40
MDU-3F-45	45
MDU-3F-50	50
MDU-3F-60	60
MDU-3F-75	75
MDU-3F-100	100

Other delay times available on request.

