



RoHS Compliant



MDU04N010VRH

Single N-channel Trench MOSFET 40V, 100A, 1.0mΩ

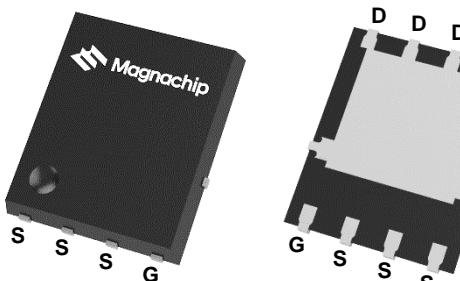
MDU04N010VRH- Single N-Channel Trench MOSFET 40V

General Description

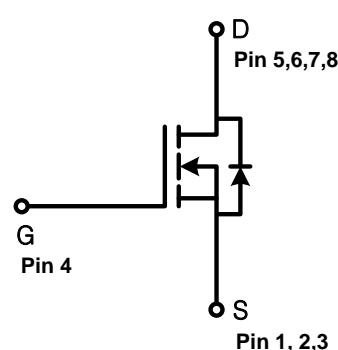
The MDU04N010VRH uses advanced Magnachip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDU04N010VRH is suitable device for Synchronous Rectification for Server and general purpose applications.

Features

- $V_{DS} = 40V$
- $I_D = 100A @ V_{GS} = 10V$
- Very low on-resistance $R_{DS(ON)}$
 $< 1.0m\Omega @ V_{GS} = 10V$
- 100% UIL Tested
- 100% R_g Tested



PDFN56



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	40	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	I_D	100.0	A
$T_c=25^\circ C$		152.7	
$T_c=100^\circ C$		39.0 ⁽³⁾	
Pulsed Drain Current	I_{DM}	400.0	
Power Dissipation	P_D	96.2	W
$T_c=25^\circ C$		38.5	
$T_c=100^\circ C$		2.5 ⁽³⁾	
Single Pulse Avalanche Energy ⁽²⁾	E_{AS}	450	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	50	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.3	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDU04N010VRH	-55~150°C	PDFN56	Tape & Reel	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	40	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.0	-	2.0	
Drain Cut-Off Current	I_{DSS}	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 0.1	
Drain-Source ON Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 50\text{A}$	-	0.8	1.0	$\text{m}\Omega$
Forward Transconductance	g_{fs}	$V_{DS} = 10\text{V}, I_D = 50\text{A}$	-	185	-	S
Dynamic Characteristics						
Total Gate Charge	$Q_{g(10.0\text{V})}$	$V_{DS} = 20\text{V}, I_D = 50\text{A}, V_{GS} = 10\text{V}$	-	103.5	-	nC
Gate-Source Charge	Q_{gs}		-	18.5	-	
Gate-Drain Charge	Q_{gd}		-	13.7	-	
Input Capacitance	C_{iss}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	6,892	-	pF
Reverse Transfer Capacitance	C_{rss}		-	147	-	
Output Capacitance	C_{oss}		-	2,058	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 20\text{V}, I_D = 50\text{A}, R_G = 3.0\Omega$	-	21.8	-	ns
Rise Time	t_r		-	18.8	-	
Turn-Off Delay Time	$t_{d(off)}$		-	96.3	-	
Fall Time	t_f		-	44.1	-	
Gate Resistance	R_g	$f=1.0 \text{ MHz}$	-	1.0	-	Ω
Drain-Source Body Diode Characteristics						
Diode continuous forward current	I_S	-	-	-	100	A
Diode pulse current	$I_{S,pulse}$	pulsed; $t_p \leq 10 \mu\text{s}$	-	-	400	
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 50\text{A}, V_{GS} = 0\text{V}$	-	0.80	1.20	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 50\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	57.7	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	105.4	-	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at $T_c=25^\circ\text{C}$ is silicon limited
2. E_{AS} is tested at starting $T_j = 25^\circ\text{C}$, $L = 1.0\text{mH}$, $I_{AS} = 30.0\text{A}$, $V_{GS} = 10\text{V}$.
3. $T < 10\text{sec}$.

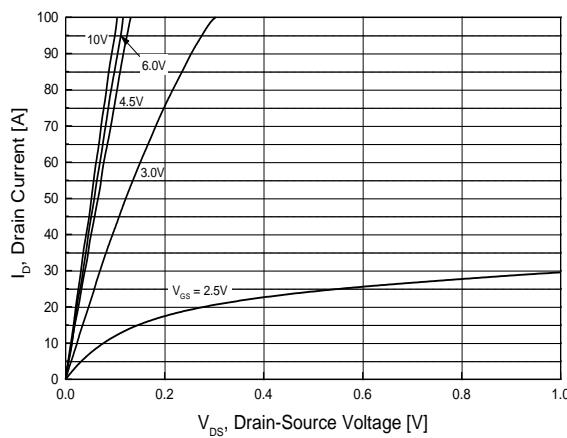


Fig.1 On-Region Characteristics

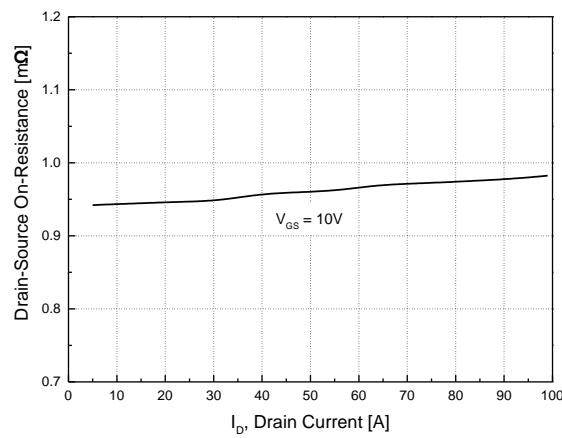


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

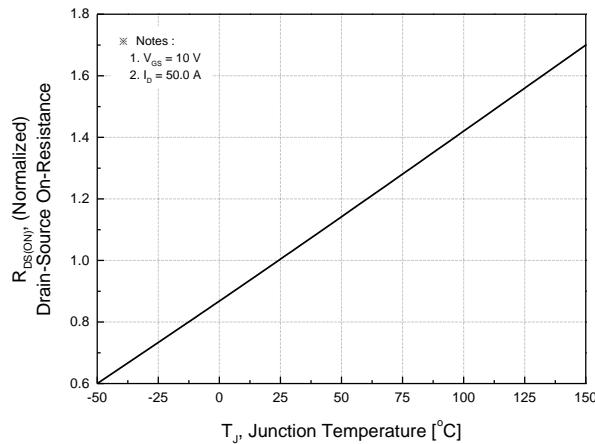


Fig.3 On-Resistance Variation with Temperature

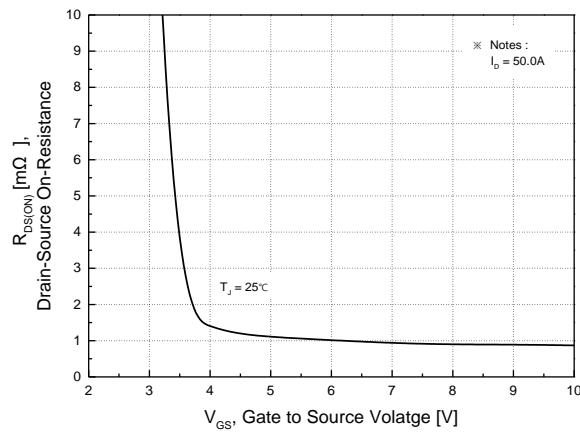


Fig.4 On-Resistance Variation with Gate to Source Voltage

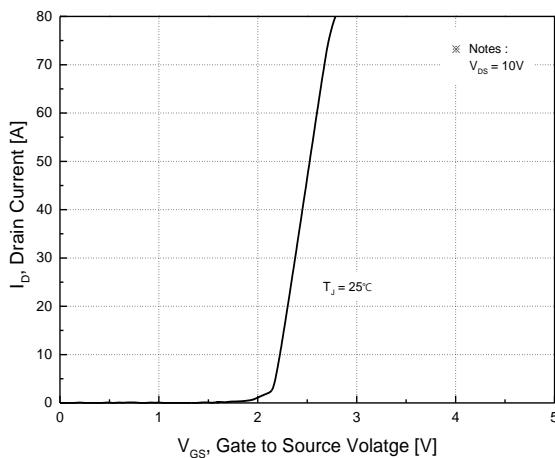


Fig.5 Transfer Characteristics

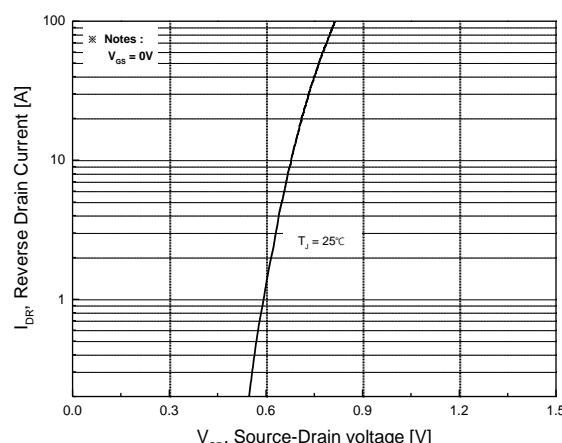
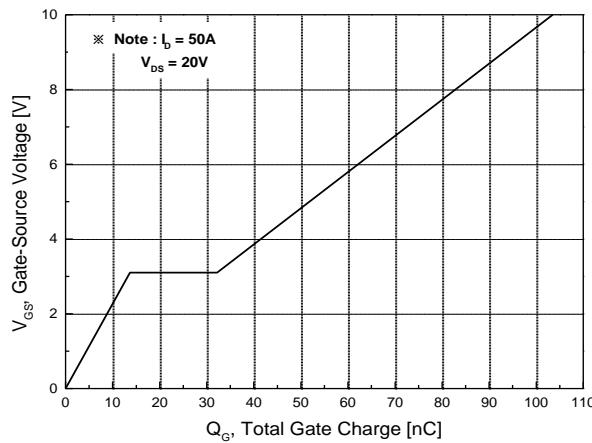
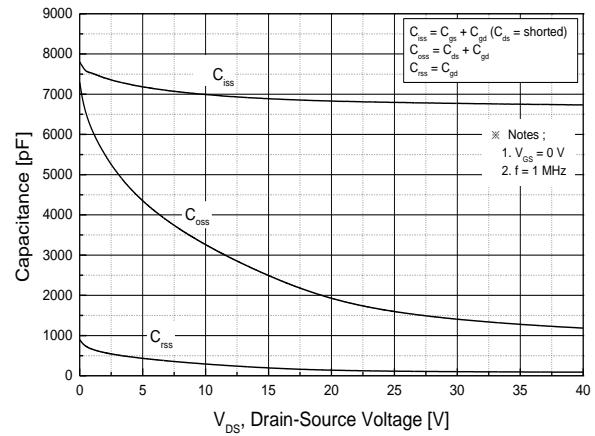
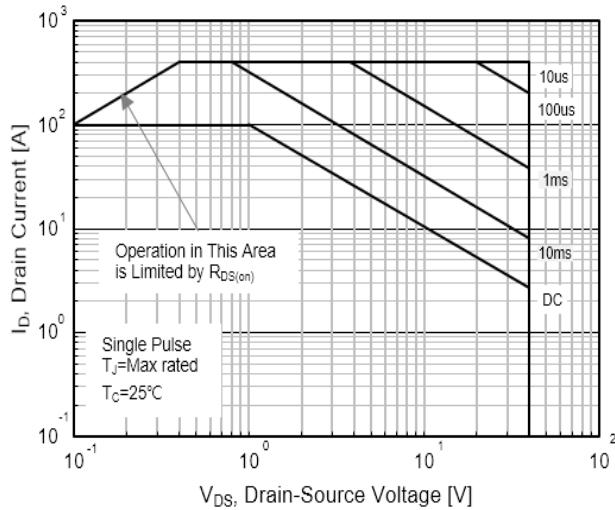
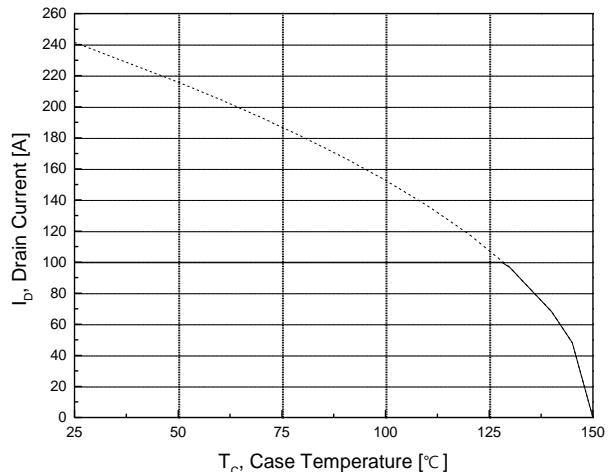
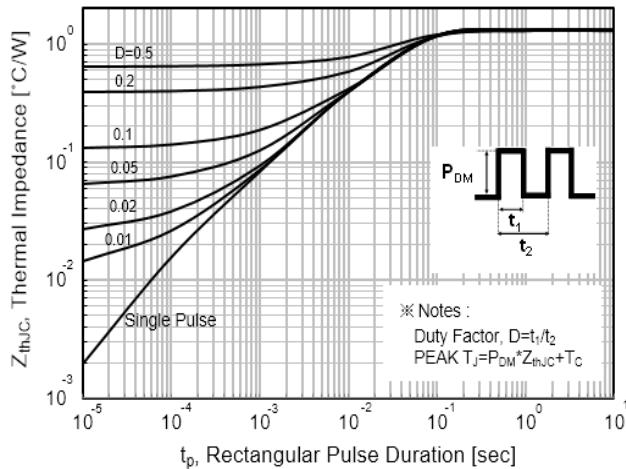


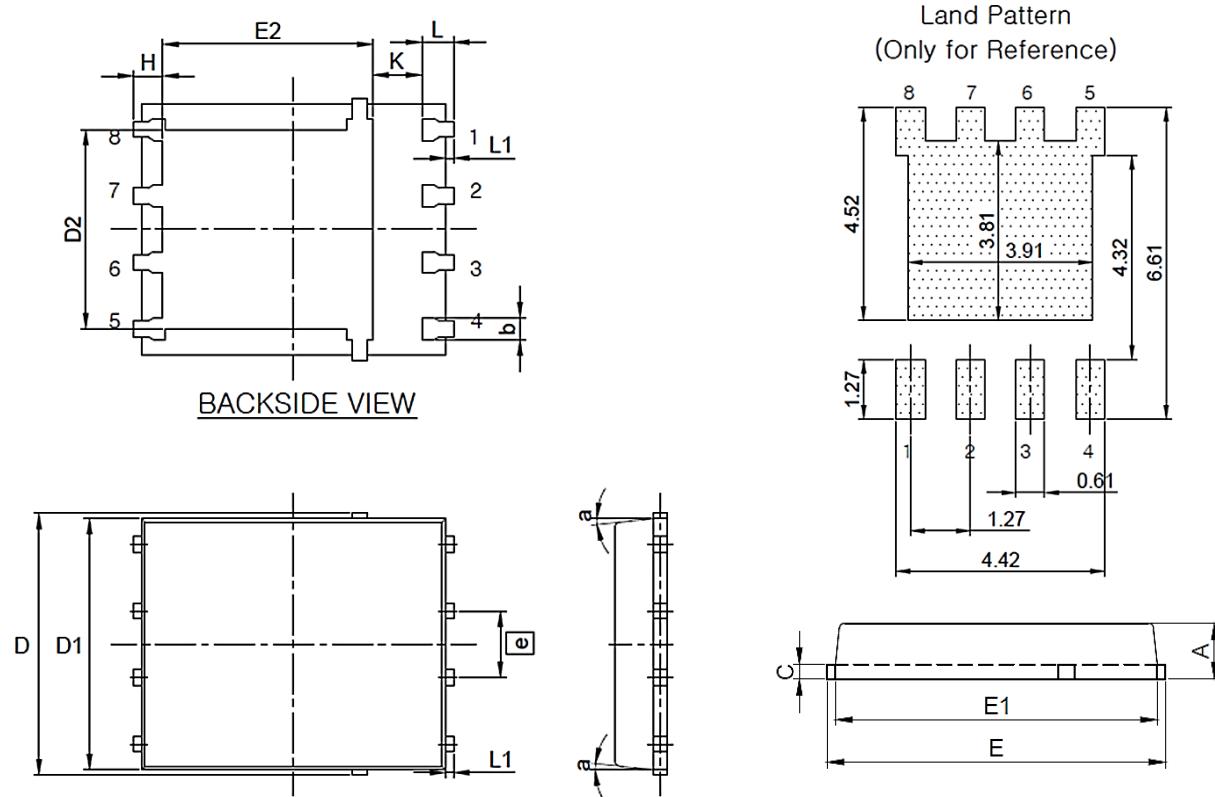
Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature


Fig.7 Gate Charge Characteristics

Fig.8 Capacitance Characteristics

Fig.9 Maximum Safe Operating Area

Fig.10 Maximum Drain Current vs. Case Temperature

Fig.11 Transient Thermal Response Curve

Package Dimension

PDFN56 (5x6mm)

Dimensions are in millimeters, unless otherwise specified



Symbol	Dimension (mm)		
	Min	Nom	Max
A	0.90	—	1.10
b	0.33	—	0.51
C	0.20	—	0.34
D	4.50	—	5.30
D1	4.50	—	5.10
D2	—	—	4.22
E	5.90	—	6.30
E1	5.50	—	6.10
E2	—	—	4.30
e	1.27 BSC		
H	0.41	—	0.71
K	0.20	—	—
L	0.51	—	0.71
L1	0.06	—	0.20
a	0°	—	12°

Note : Package body size, length and width do not include mold flash, protrusions and gate burrs.

DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

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