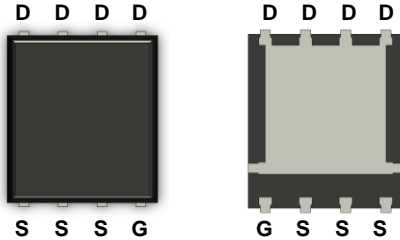


### General Description

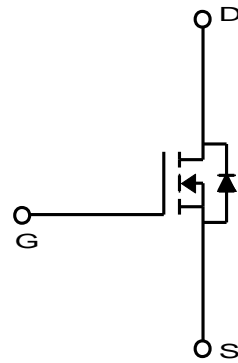
The MDU06N031 uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDU06N031 is suitable device for Synchronous Rectification For Server and general purpose applications.

### Features

- $V_{DS} = 60V$
- $I_D = 100A @ V_{GS} = 10V$
- $R_{DS(ON)} < 3.1m\Omega @ V_{GS} = 10V$
- 100% UIL Tested
- 100% Rg Tested



PDFN56



### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current <sup>(1)</sup>	$T_C=25^\circ C$ (Silicon Limited)	$I_D$	164.7	A
	$T_C=25^\circ C$ (Package Limited)		100.0	
	$T_C=100^\circ C$ (Silicon Limited)		104.2	
	$T_A=25^\circ C$ <sup>(3)</sup>		22.2 <sup>(3)</sup>	
Pulsed Drain Current		$I_{DM}$	400.0	
Power Dissipation	$T_C=25^\circ C$	$P_D$	138.9	W
	$T_C=100^\circ C$		55.6	
	$T_A=25^\circ C$ <sup>(3)</sup>		2.5 <sup>(3)</sup>	
Single Pulse Avalanche Energy <sup>(2)</sup>		$E_{AS}$	288	mJ
Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~150	°C

### Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	50.0	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.9	

## Ordering Information

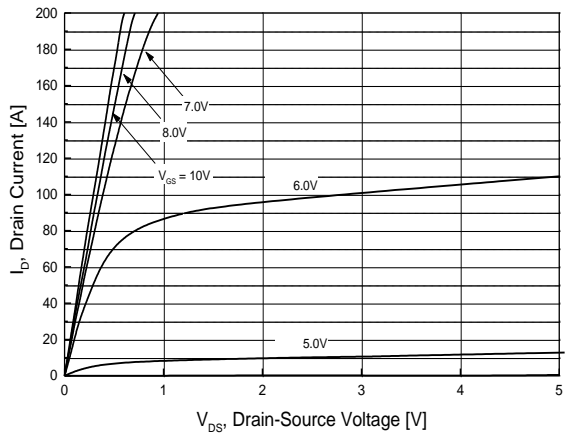
Part Number	Temp. Range	Package	Packing	RoHS Status
MDU06N031VRH	-55~150°C	PDFN56	Tape & Reel	Halogen Free

## Electrical Characteristics (T<sub>J</sub> =25°C)

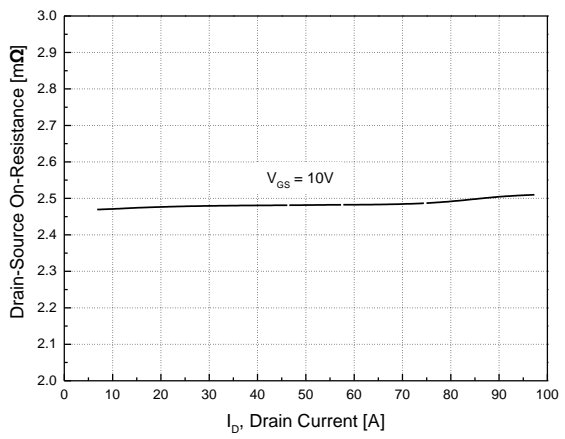
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V	-	-	1.0	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V	-	-	±0.1	
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> =50A	-	2.5	3.1	mΩ
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> =50A	-	95.0	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g(10.0V)</sub>	V <sub>DS</sub> = 30.0V, I <sub>D</sub> = 50.0A, V <sub>GS</sub> = 10V	-	69.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	22.1	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	16.8	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 30.0V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	4,819.5	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	76.0	-	
Output Capacitance	C <sub>oss</sub>		-	998.5	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 30.0V, I <sub>D</sub> = 50A, R <sub>G</sub> = 3.0Ω	-	33.1	-	ns
Rise Time	t <sub>r</sub>		-	17.3	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	74.8	-	
Fall Time	t <sub>f</sub>		-	35.0	-	
Gate Resistance	R <sub>g</sub>	f=1 MHz	-	1.5	-	Ω
<b>Drain-Source Body Diode Characteristics</b>						
Source-Drain Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 50A, V <sub>GS</sub> = 0V	-	0.80	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =50A, dI/dt = 100A/μs	-	62.5	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	99.7	-	nC

Note :

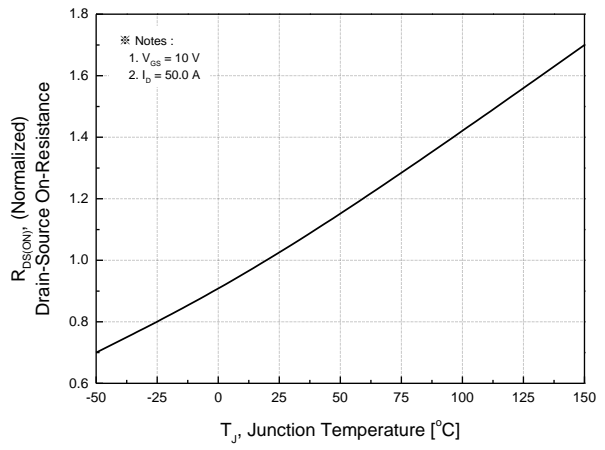
- Surface mounted FR-4 board by JEDEC (jesd51-7). Continuous current at T<sub>C</sub>=25°C is silicon limited
- E<sub>AS</sub> is tested at starting T<sub>J</sub> = 25°C, L = 1.0mH, I<sub>AS</sub> = 24.0A, V<sub>GS</sub> = 10V.
- T < 10sec.



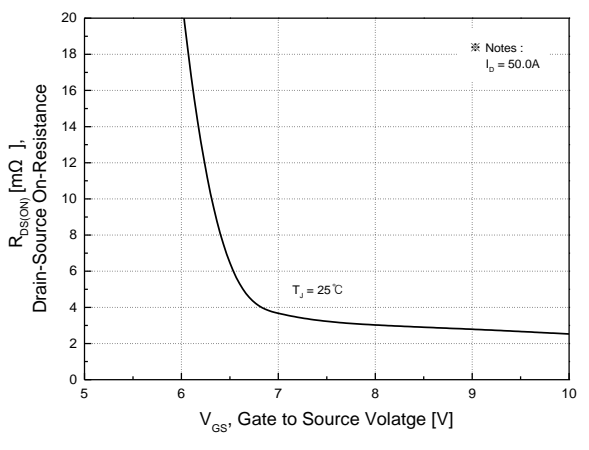
**Fig.1 On-Region Characteristics**



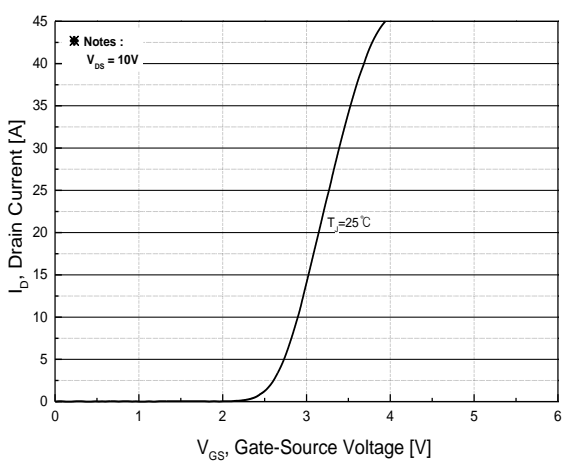
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



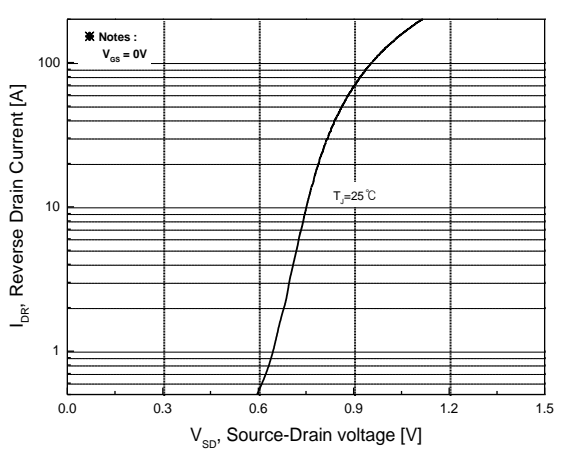
**Fig.3 On-Resistance Variation with Temperature**



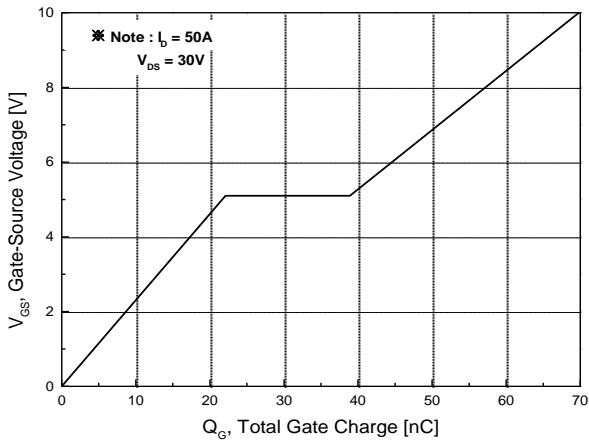
**Fig.4 On-Resistance Variation with Gate to Source Voltage**



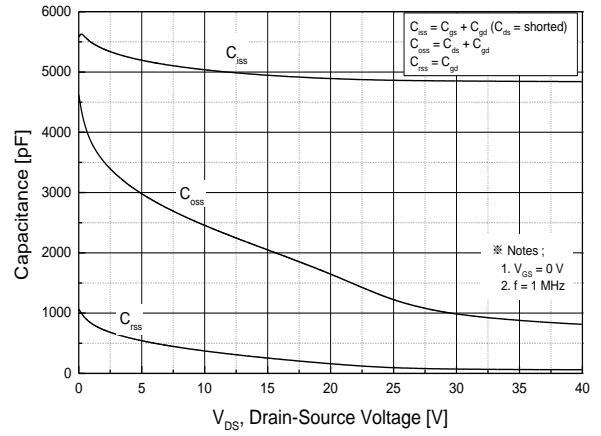
**Fig.5 Transfer Characteristics**



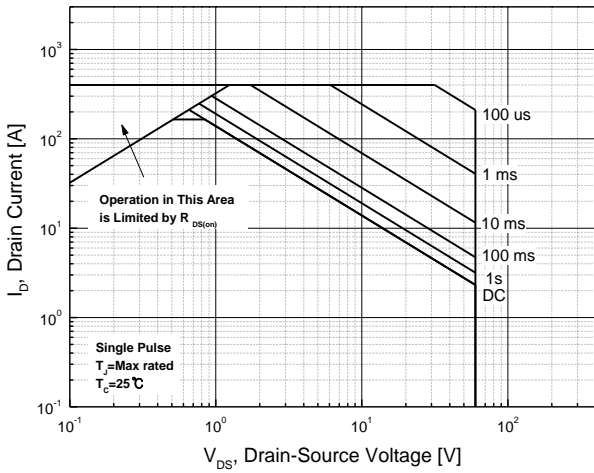
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



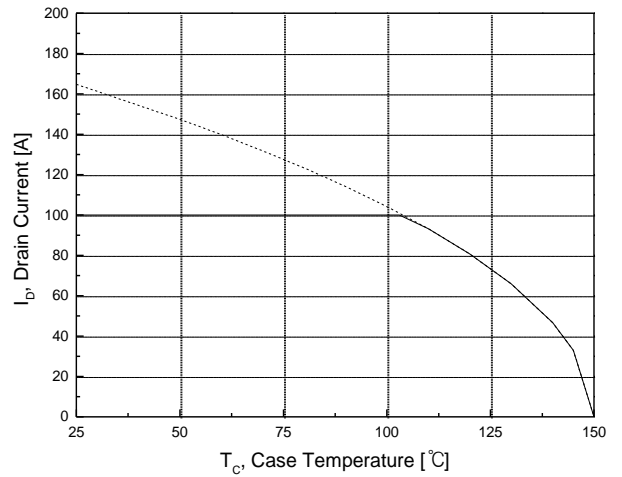
**Fig.7 Gate Charge Characteristics**



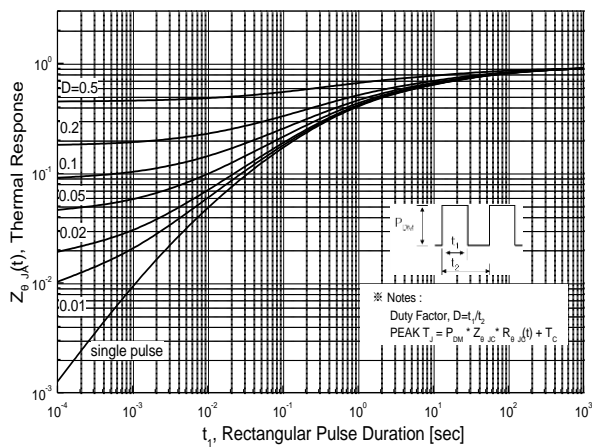
**Fig.8 Capacitance Characteristics**



**Fig.9 Maximum Safe Operating Area**



**Fig.10 Maximum Drain Current vs. Case Temperature**

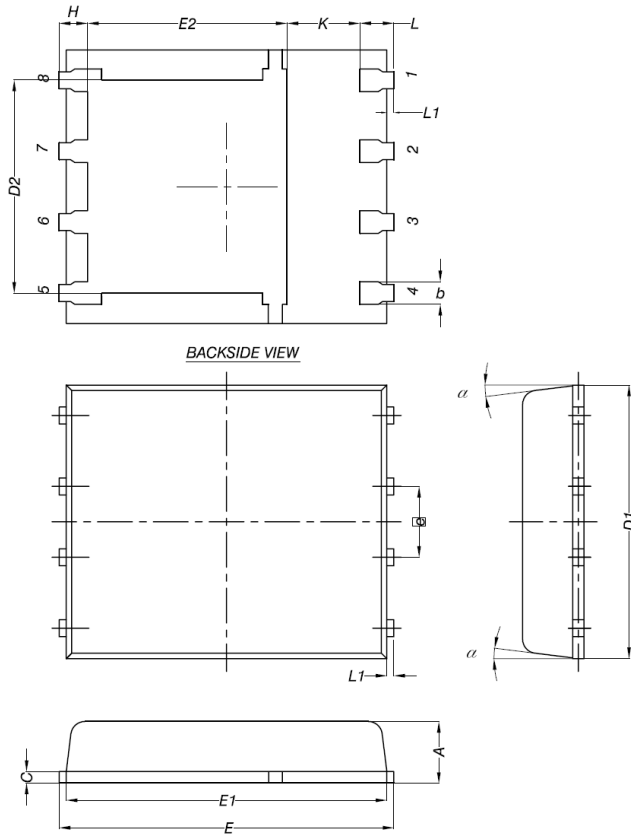


**Fig.11 Transient Thermal Response Curve**

## Package Dimension

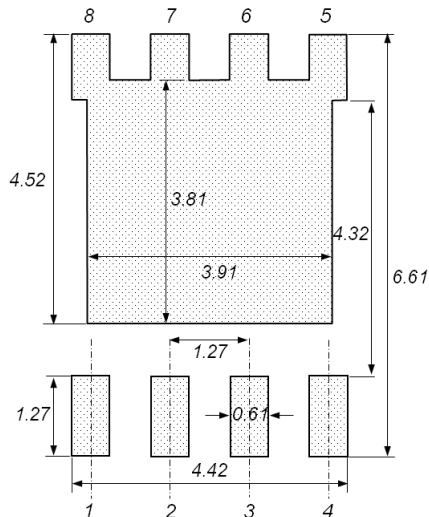
### PDFN56 (5x6mm)

Dimensions are in millimeters, unless otherwise specified



Dimension	MILLIMETERS	
	Min	Max
A	0.90	1.10
b	0.33	0.51
C	0.20	0.34
D1	4.50	5.10
D2	-	4.22
E	5.90	6.30
E1	5.50	6.10
E2	-	4.30
e	1.27BSC	
H	0.41	0.71
K	0.20	-
L	0.51	0.71
$\alpha$	0°	12°

### Land Pattern



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