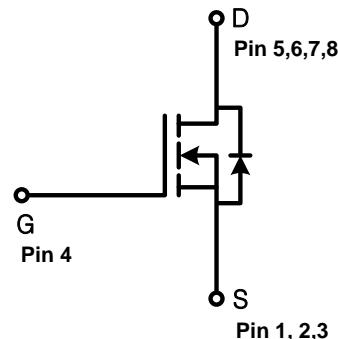
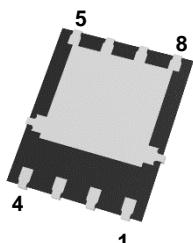


General Description

The MDU10N070RH uses advanced Magnachip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDU10N070RH is suitable device for Synchronous Rectification for Server and general purpose applications.

Features

- $V_{DS} = 100V$
- $I_D = 53 A @ V_{GS} = 10V$
- Very low on-resistance $R_{DS(ON)}$
 $< 7.4 \text{ m}\Omega @ V_{GS} = 10V$
 $< 9.1 \text{ m}\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% R_g Tested



Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	$T_c=25^\circ\text{C}$ (Silicon Limited)	I_D	85	A
	$T_c=25^\circ\text{C}$ (Package Limited)		53	
	$T_c=100^\circ\text{C}$ (Silicon Limited)		54	
	$T_A=25^\circ\text{C}$		19	
Pulsed Drain Current ⁽²⁾		I_{DM}	212	
Power Dissipation	$T_c=25^\circ\text{C}$	P_D	119	W
	$T_c=100^\circ\text{C}$		47	
	$T_A=25^\circ\text{C}$ ($T \leq 10\text{s}$)		5.5	
Single Pulse Avalanche Energy ⁽³⁾		E_{AS}	84	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ($T \leq 10\text{s}$) ⁽¹⁾		$R_{\theta JA}$	22.7	°C/W
Thermal Resistance, Junction-to-Ambient (Steady State) ⁽¹⁾		$R_{\theta JA}$	50.0	
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	1.05	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDU10N070RH	-55~150°C	PDFN56	Tape & Reel	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	100	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.2	-	2.2	
Drain Cut-Off Current	I_{DSS}	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$	-	-	1.0	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 100	nA
Drain-Source ON Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$	-	5.9	7.4	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 15\text{A}$	-	7.0	9.1	
Forward Transconductance	g_f	$V_{DS} = 10\text{V}, I_D = 20\text{A}$	-	92	-	S
Dynamic Characteristics						
Total Gate Charge	$Q_{g(10\text{V})}$	$V_{DD} = 50\text{V}, I_D = 20\text{A}, V_{GS} = 10\text{V}$	-	63	-	nC
Gate-Source Charge	Q_{gs}		-	10	-	
Gate-Drain Charge	Q_{gd}		-	10	-	
Input Capacitance	C_{iss}	$V_{DS} = 40\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	-	4140	-	pF
Reverse Transfer Capacitance	C_{rss}		-	31	-	
Output Capacitance	C_{oss}		-	586	-	
Turn-On Delay Time	$t_{d(on)}$		-	13.5	-	ns
Rise Time	t_r	$V_{GS} = 10\text{V}, V_{DD} = 50\text{V}, I_D = 20\text{A}, R_G = 3\Omega$	-	12	-	
Turn-Off Delay Time	$t_{d(off)}$		-	61	-	
Fall Time	t_f		-	8.8	-	
Gate Resistance	R_g	$f=1.0 \text{ MHz}$	-	3.0	-	Ω
Drain-Source Body Diode Characteristics						
Diode continuous forward current	I_S	$T_C=25^\circ\text{C}$ (Package Limited)	-	-	53	A
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 20\text{A}, V_{GS} = 0\text{V}$	-	0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	70	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	183	-	nC

Note :

- The R_{thja} was measured with the device mounted on $74.2 * 74.2 \text{ mm}^2$ Copper buried FR4 board. The heat sink paddle size for the drain connection of device is $4.5 * 6.0 \text{ mm}^2$.
- Pulse width limited by T_{jmax}
- E_{AS} is tested at starting $T_J = 25^\circ\text{C}$, $L = 1.0\text{mH}$, $I_{AS} = 13\text{A}$, $V_{DD} = 50\text{V}$, $V_{GS} = 10\text{V}$

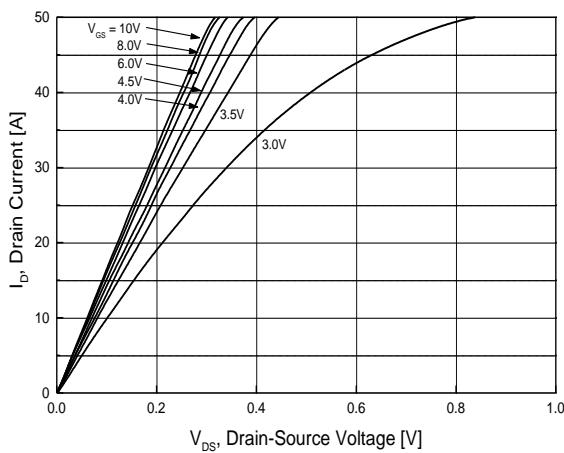


Fig.1 On-Region Characteristics

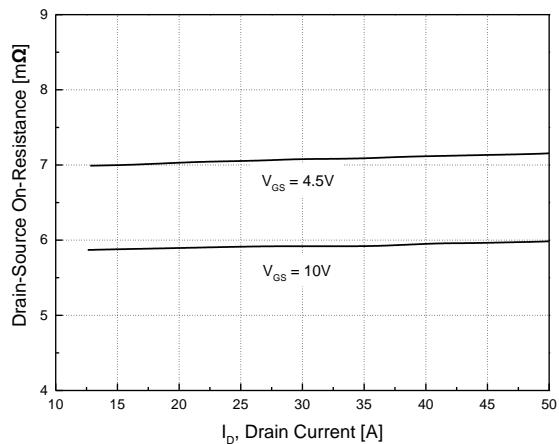


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

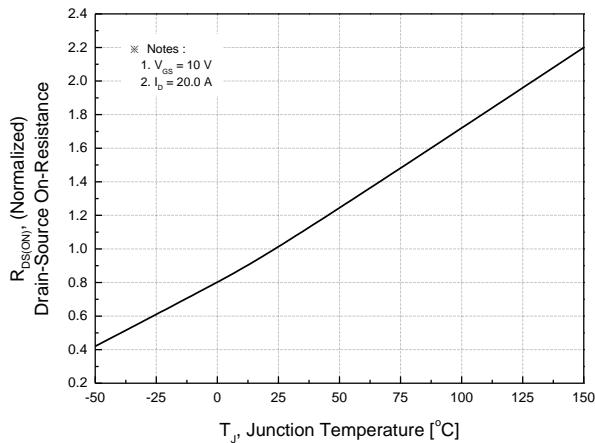


Fig.3 On-Resistance Variation with Temperature

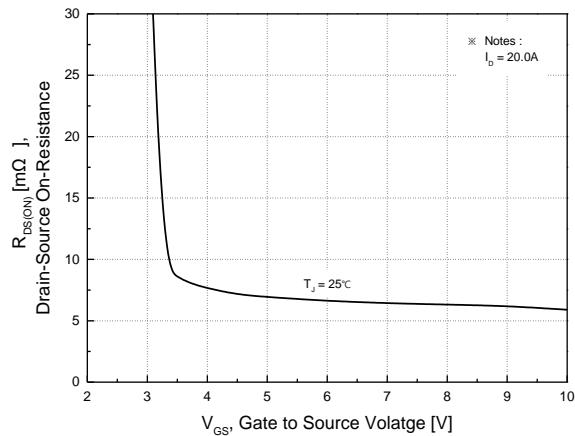


Fig.4 On-Resistance Variation with Gate to Source Voltage

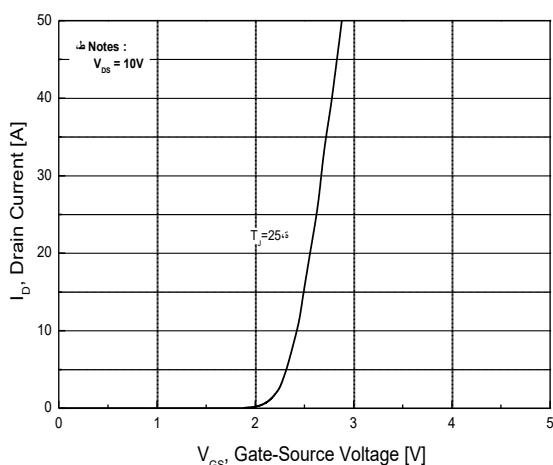


Fig.5 Transfer Characteristics

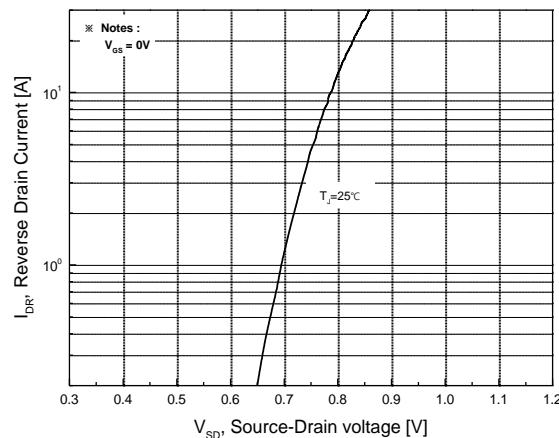


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

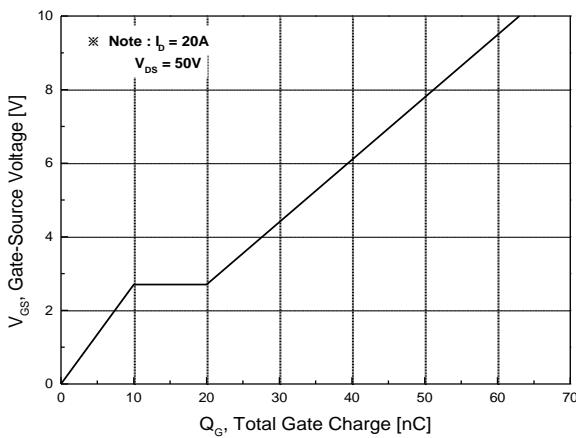


Fig.7 Gate Charge Characteristics

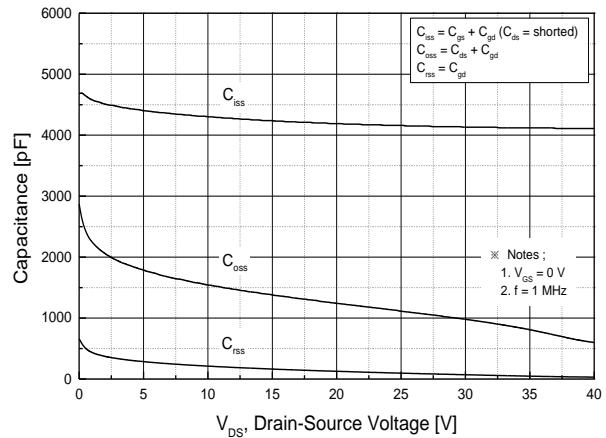


Fig.8 Capacitance Characteristics

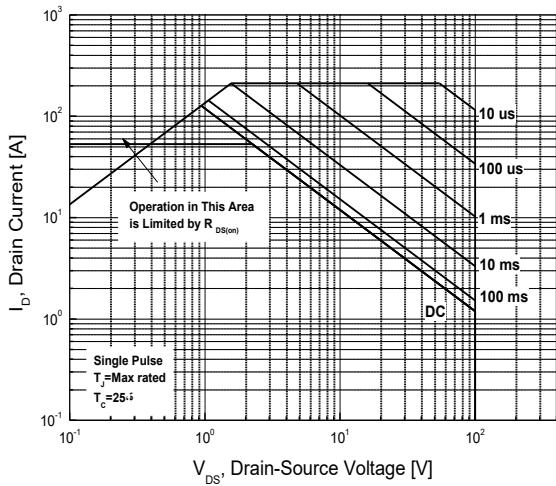


Fig.9 Maximum Safe Operating Area

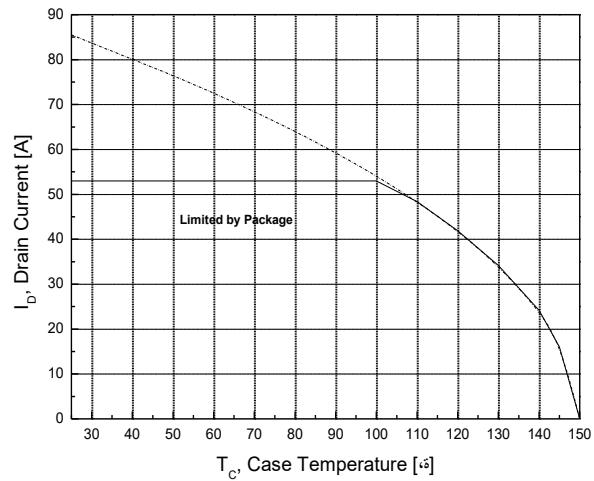


Fig.10 Maximum Drain Current vs. Case Temperature

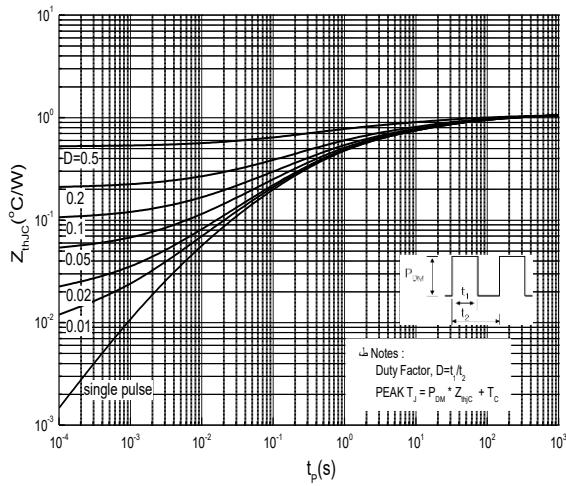
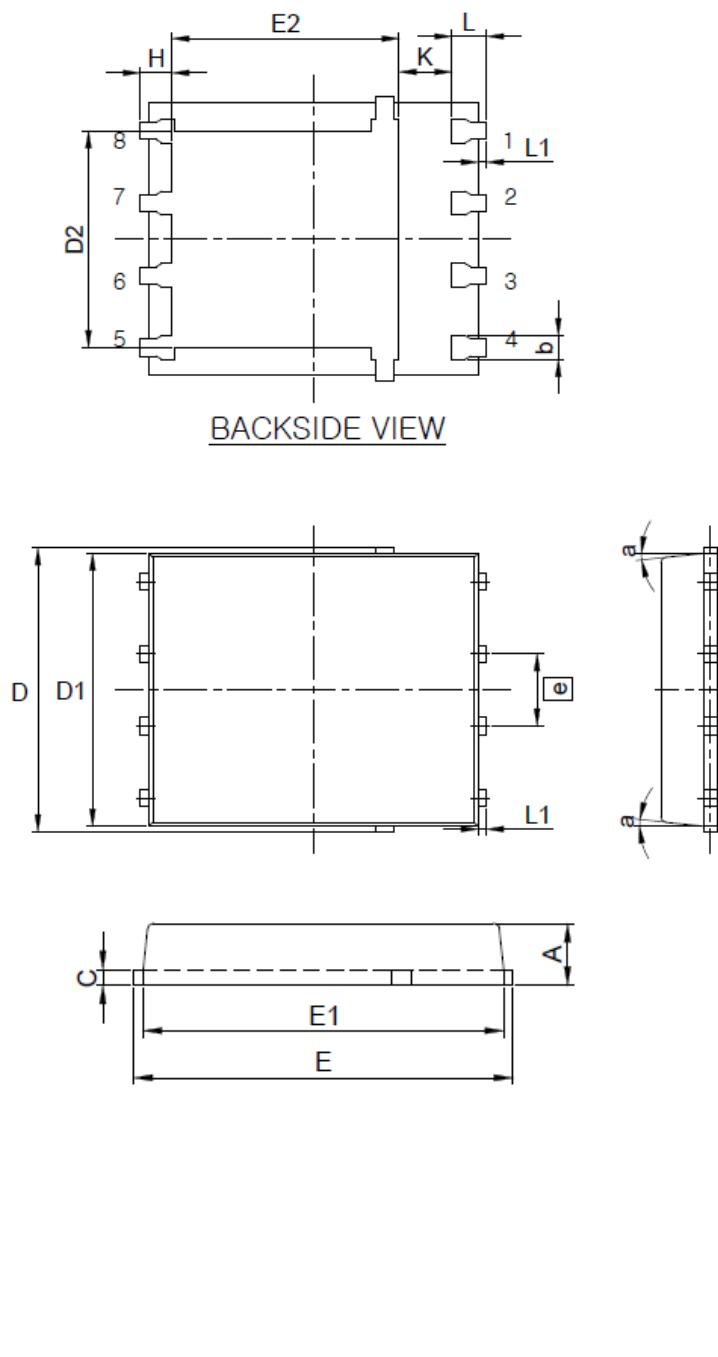


Fig.11 Transient Thermal Response Curve

Package Dimension

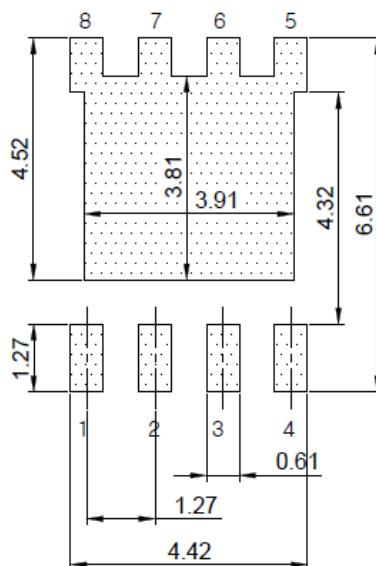
PDFN56 (5x6mm²)

Dimensions are in millimeters, unless otherwise specified



Dimension	MILLIMETERS	
	Min	Max
A	0.90	1.10
b	0.33	0.51
C	0.20	0.34
D	4.50	5.30
D1	4.50	5.10
D2	-	4.22
E	5.90	6.30
E1	5.50	6.10
E2	-	4.30
e	1.27BSC	
H	0.41	0.71
K	0.20	-
L	0.51	0.71
α	0°	12°

Land Pattern
(Only for Reference)



Note : Package body size, length and width do not include mold flash, protrusions and gate burrs.

DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

Magnachip reserves the right to change the specifications and circuitry without notice at any time. Magnachip does not consider responsibility for use of any circuitry other than circuitry entirely included in a Magnachip product.  Magnachip is a registered trademark of Magnachip Semiconductor Ltd.