



RoHS Compliant

MDV1529E

Single N-channel Trench MOSFET 30V, 28A, 4.5mΩ

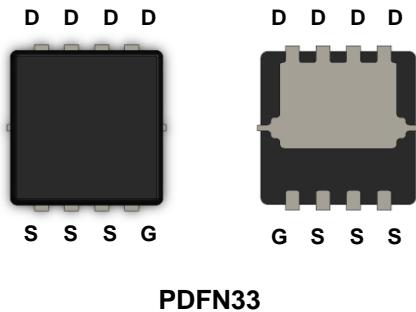
MDV1529E – Single N-Channel Trench MOSFET 30V

General Description

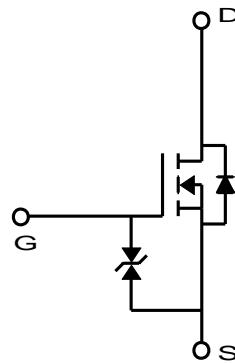
The MDV1529E uses advanced MagnaChip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDV1529E is suitable for DC/DC converter and general purpose applications.

Features

- $V_{DS} = 32V$
- $I_D = 28A @ V_{GS} = 10V$
- $R_{DS(ON)}$
 $< 4.5m\Omega @ V_{GS} = 10V$
 $< 6.5m\Omega @ V_{GS} = 4.5V$



PDFN33



Absolute Maximum Ratings ($T_a = 25^\circ C$ unless otherwise specified)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	30	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	I_D	$T_c=25^\circ C$ (Silicon limited)	A
		$T_c=25^\circ C$ (Package limited)	
		$T_c=70^\circ C$	
Pulsed Drain Current	I_{DM}	84	A
Power Dissipation	P_D	$T_c=25^\circ C$	W
		$T_c=70^\circ C$	
Single Pulse Avalanche Energy ⁽²⁾	E_{AS}	136	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	$R_{\theta JA}$	36	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.5	

Ordering Information

Part Number	Temp. Range	Package	Packing	Quantity	RoHS Status
MDV1529EURH	-55~150°C	PDFN33	Tape & Reel	5000 units	Halogen Free

Electrical Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	32	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.3	1.9	2.7	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 32V, V _{GS} = 0V	-	-	10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±16V, V _{DS} = 0V	-	-	±10	
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 16A	-	3.7	4.5	mΩ
		V _{GS} = 4.5V, I _D = 13A	-	5.2	6.5	
Dynamic Characteristics						
Total Gate Charge	Q _{g(10V)}	V _{DS} = 15.0V, I _D = 16A, V _{GS} = 10V	-	44.0	-	nC
Total Gate Charge	Q _{g(4.5V)}		-	22.5	-	
Gate-Source Charge	Q _{gs}		-	7.0	-	
Gate-Drain Charge	Q _{gd}		-	11.0	-	
Input Capacitance	C _{iss}	V _{DS} = 15.0V, V _{GS} = 0V, f = 1.0MHz	-	2050	-	pF
Reverse Transfer Capacitance	C _{rss}		-	290	-	
Output Capacitance	C _{oss}		-	410	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DS} = 15.0V, I _D = 16A, R _G = 3.0Ω	-	9.0	-	ns
Rise Time	t _r		-	5.5	-	
Turn-Off Delay Time	t _{d(off)}		-	250	-	
Fall Time	t _f		-	215	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = 16A, V _{GS} = 0V	-	0.7	1.2	V

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. E_{AS} is tested at starting T_j = 25°C, L = 0.1mH, I_{AS} = 30.4A, V_{DD} = 27V, V_{GS} = 10V.

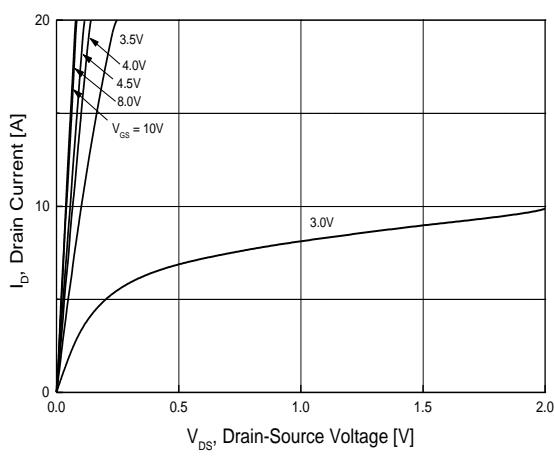


Fig.1 On-Region Characteristics

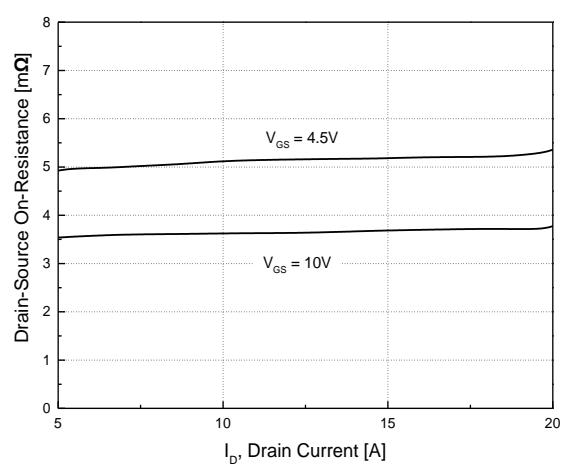


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

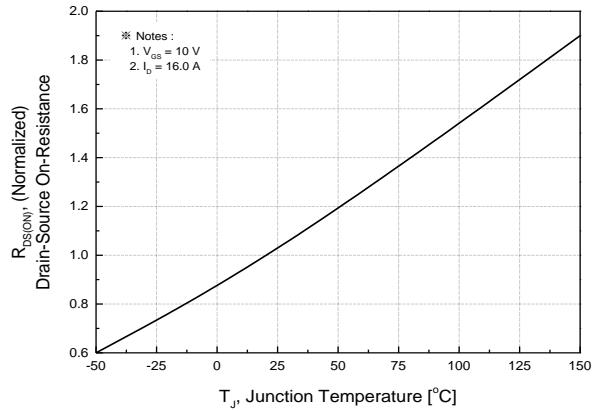


Fig.3 On-Resistance Variation with Temperature

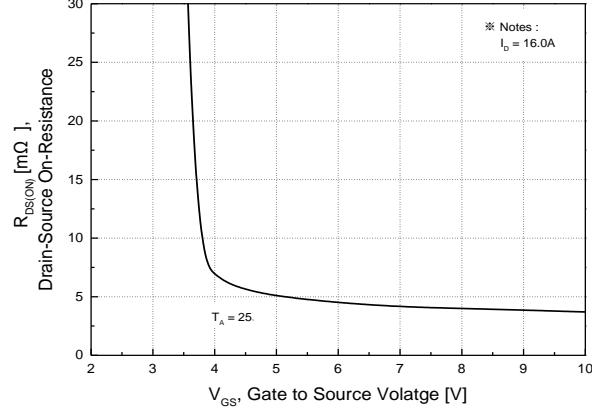


Fig.4 On-Resistance Variation with Gate to Source Voltage

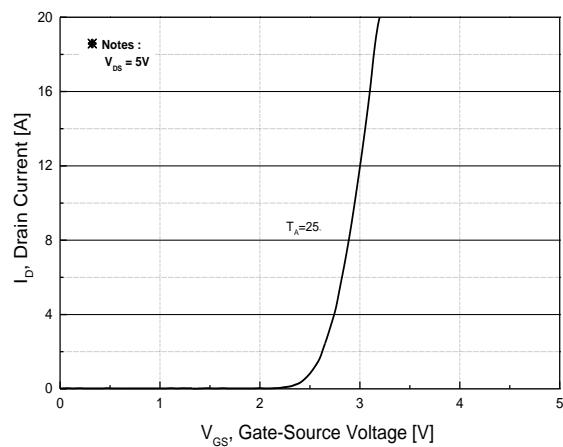


Fig.5 Transfer Characteristics

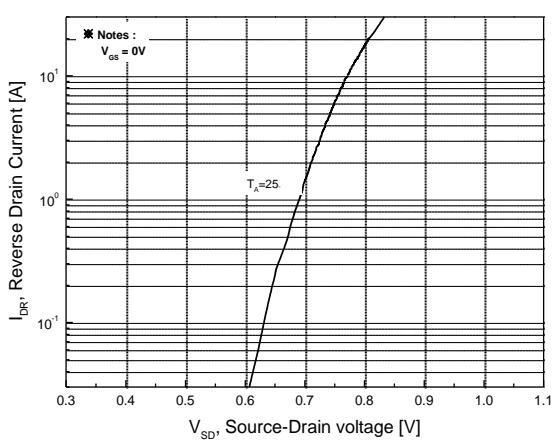


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

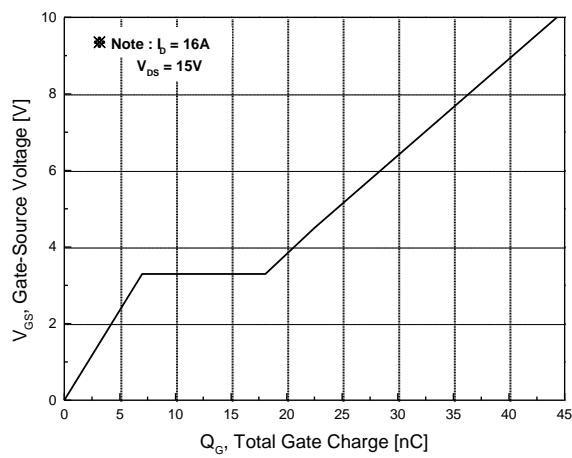


Fig.7 Gate Charge Characteristics

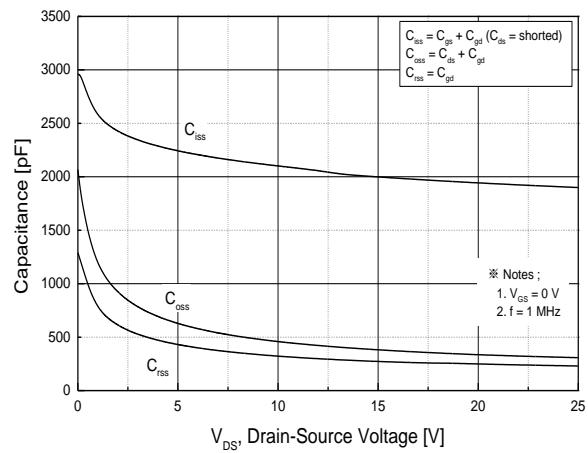


Fig.8 Capacitance Characteristics

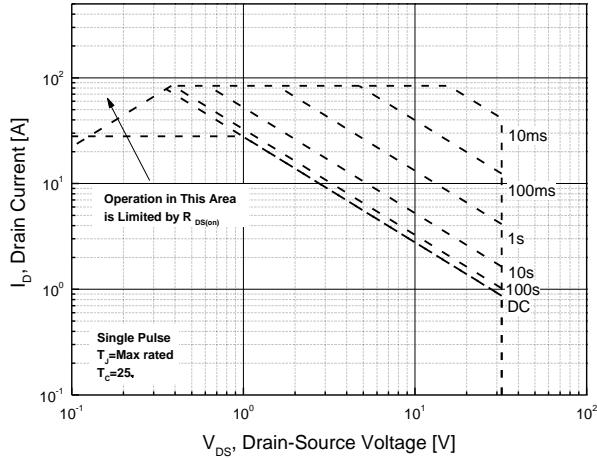


Fig.9 Maximum Safe Operating Area

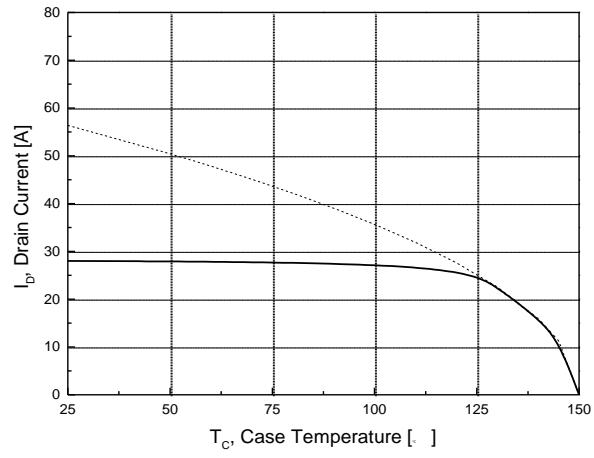


Fig.10 Maximum Drain Current Vs. Case Temperature

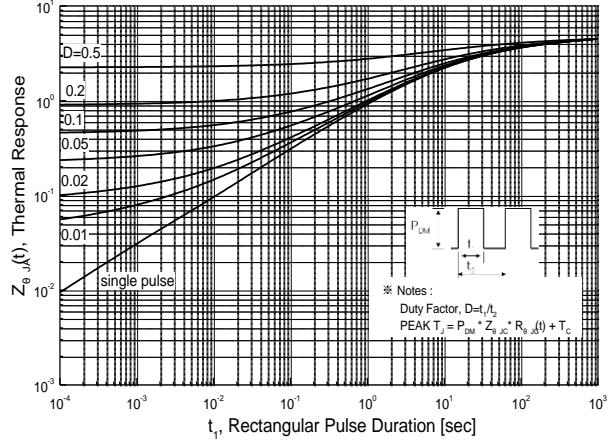
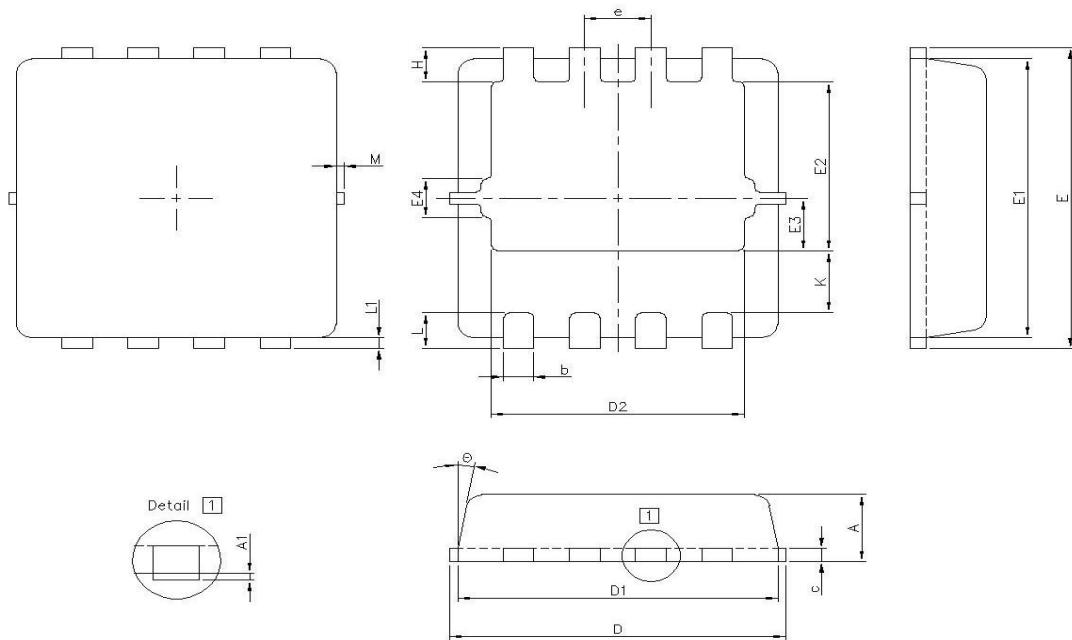


Fig.11 Transient Thermal Response Curve

Package Dimension

PowerDFN33 (3.3x3.3mm)

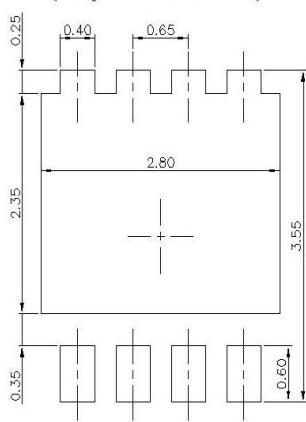
Dimensions are in millimeters, unless otherwise specified



(Unit: mm)

DIM	Min	Max	DIM	Min	Max
A	0.70	0.80	E2	1.78	1.98
A1	0.00	0.05	E3	0.49	0.69
b	0.25	0.35	E4	0.35	TYP.
c	0.10	0.25	e	0.65	BSC
D	3.20	3.40	K	0.70	TYP.
D1	3.00	3.20	L	0.30	0.50
D2	2.39	2.59	L1	0.13	TYP.
E	3.25	3.45	H	0.27	0.47
E1	3.00	3.20	Θ	0	12

Land Pattern
(Only for Reference)



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